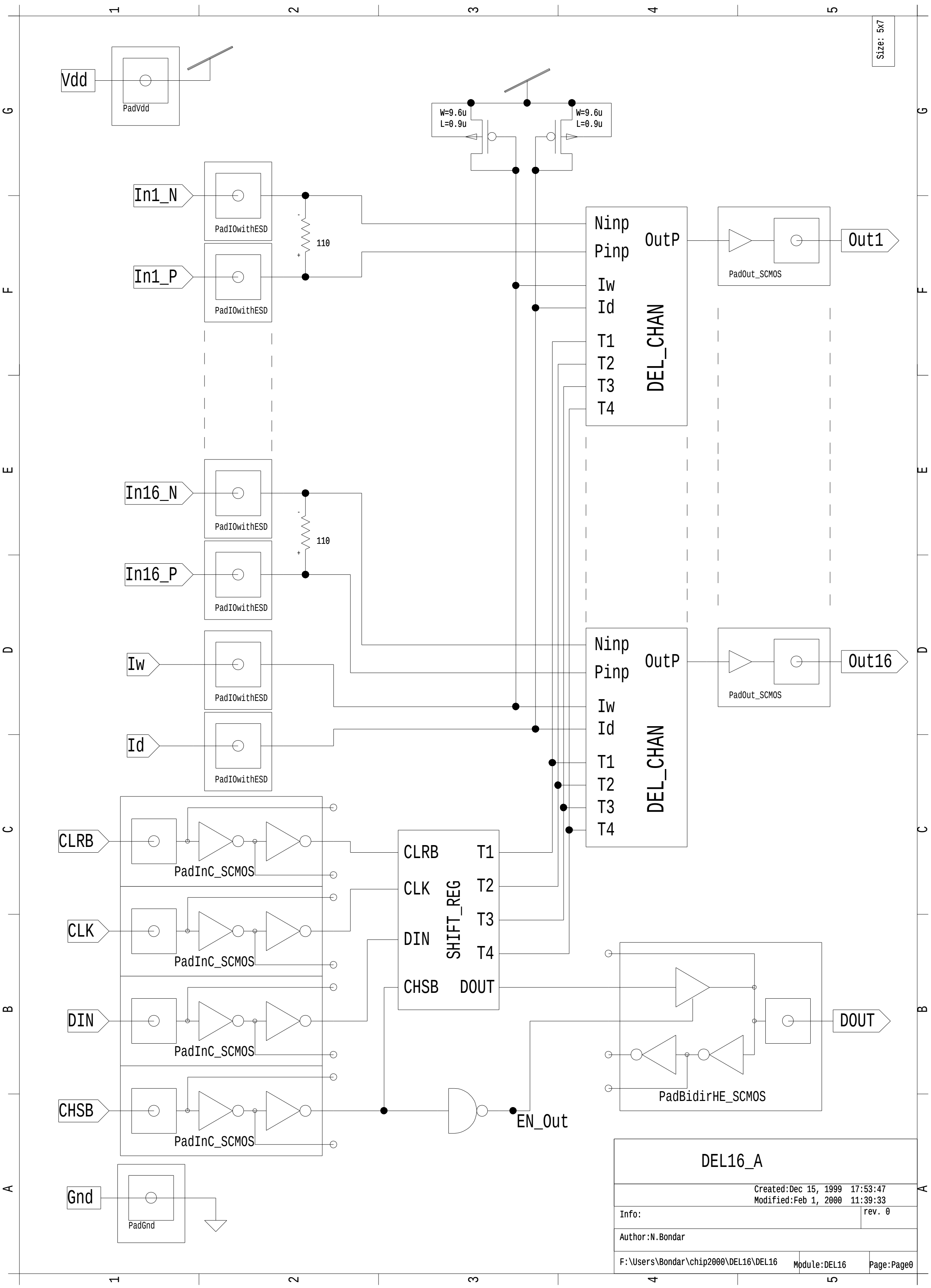

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Vdd
PadVdd
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L=0.9u
W=9.6u
L=0.9u
In1_N
PadIOwithESD
110
In1_P
PadIOwithESD
Ninp
Pinp
OutP
Iw
Id
T1
T2
T3
T4
DEL_CHAN
PadOut_SCMOS
Out1
In16_N
PadIOwithESD
110
In16_P
PadIOwithESD
Iw
PadIOwithESD
Id
PadIOwithESD
Ninp
Pinp
OutP
Iw
Id
T1
T2
T3
T4
DEL_CHAN
PadOut_SCMOS
Out16
CLRB
PadInC_SCMOS
CLK
PadInC_SCMOS
DIN
PadInC_SCMOS
CHSB
PadInC_SCMOS
CLRB
CLK
DIN
CHSB
SHIFT_REG
T1
T2
T3
T4
DOUT
PadBidirHE_SCMOS
DOUT
EN_Out
Gnd
PadGnd
DEL16_A
Created:Dec 15, 1999 17:53:47
Modified:Feb 1, 2000 11:39:33
Info:
rev. 0
Author:N.Bondar
F:\Users\Bondar\chip2000\DEL16\DEL16
Module:DEL16
Page:Page0