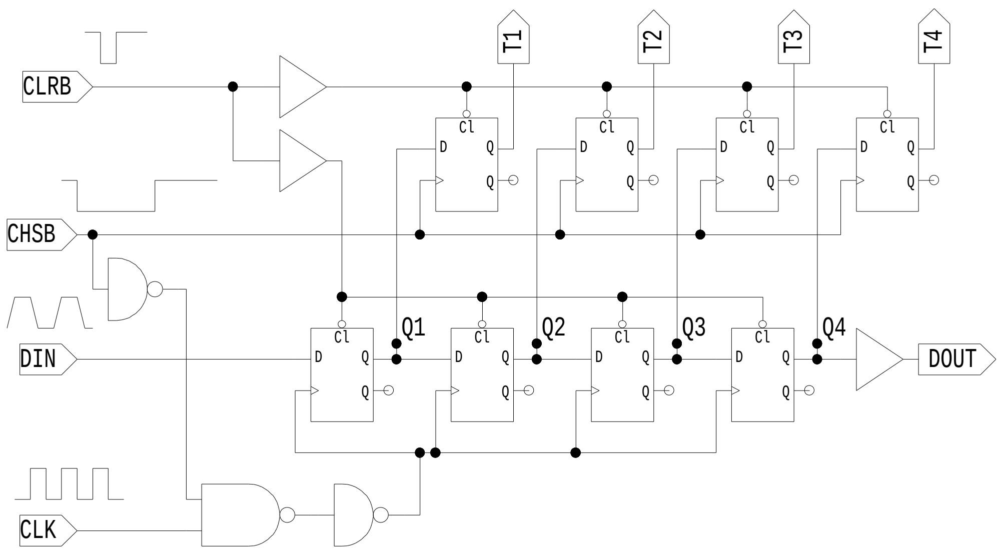
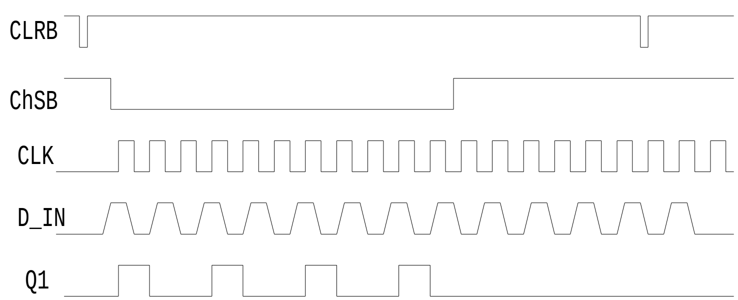
Delay chip. Control network






```
<< Devices and Process Parameters >>

.param l=0.3u

l: the layout unit / scale factor (um).

P_4 / N_4: P / N-channel MOSFET
   W: Gate Width (l).
   L: Gate Length (l).
   AS / AD: Source/Drain Area A=W*L*l*l (l)
      A: Area (squm).
   PS /PD: Source/Drain parameter P=(W+L)*2*l (l)
      P: parameter (um).

For SPICE Export, this .param statement must
be placed on top level of the design hierarchy.
```

SPICE OUTPUT=.include F:userbondarcmp16D_99cmp16_fsh_r.cmd

SPICE OUTPUT=.include F:userbondarcmp16D_99cmp16_fami05.mod

Substrate Power Connection

Shift register

Created:Dec 15, 1999 08:14:30 Modified:Feb 14, 2000 10:10:07	
Info:	rev. 0
Author:N.Bondar	Size: 5x7
F:\Users\Bondar\chip2000\DEL16\DEL16 Module:shift_register	Page:Page0