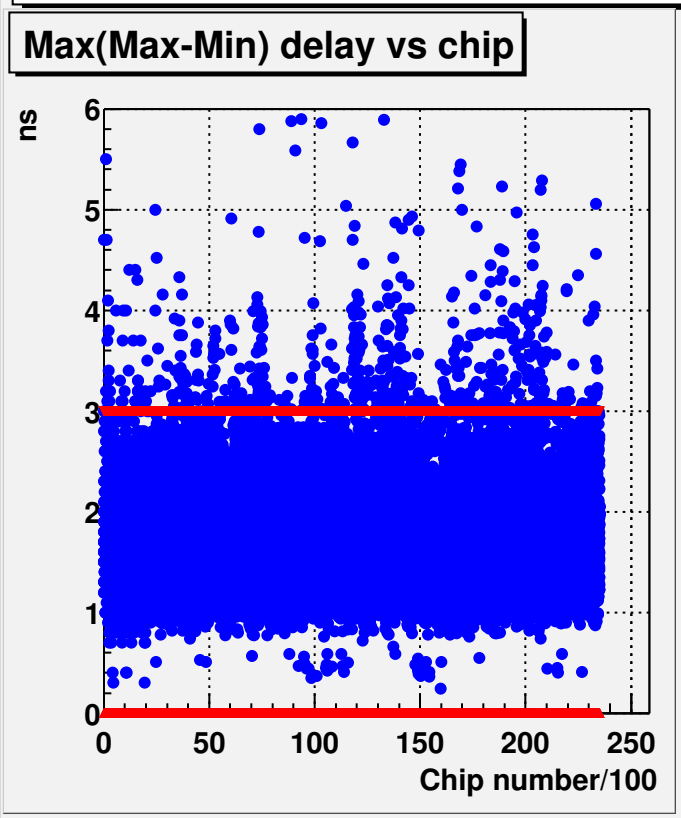
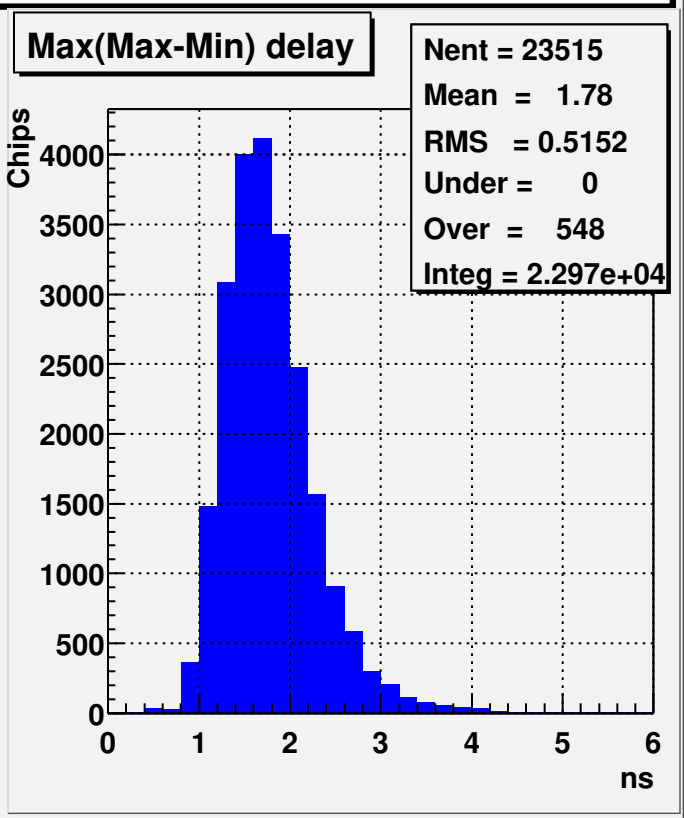
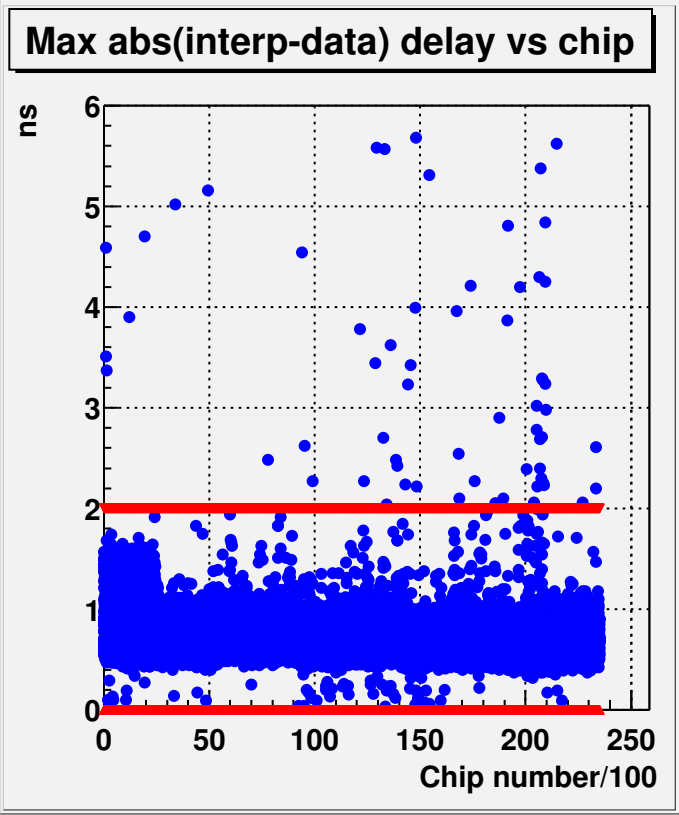
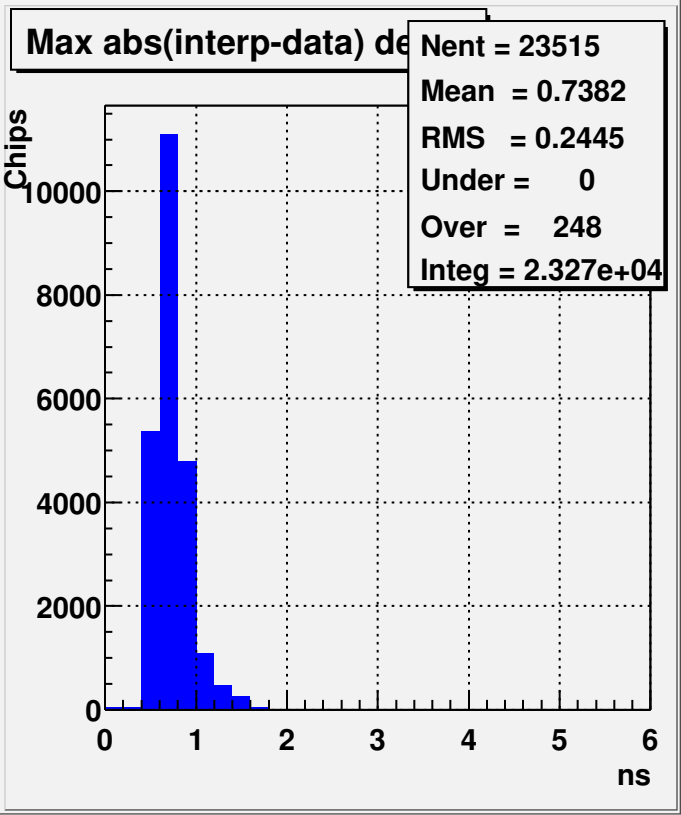
CMS EMU CSC D16G Prod. Test. Trays 1-149. All chips. CMU-FNAL

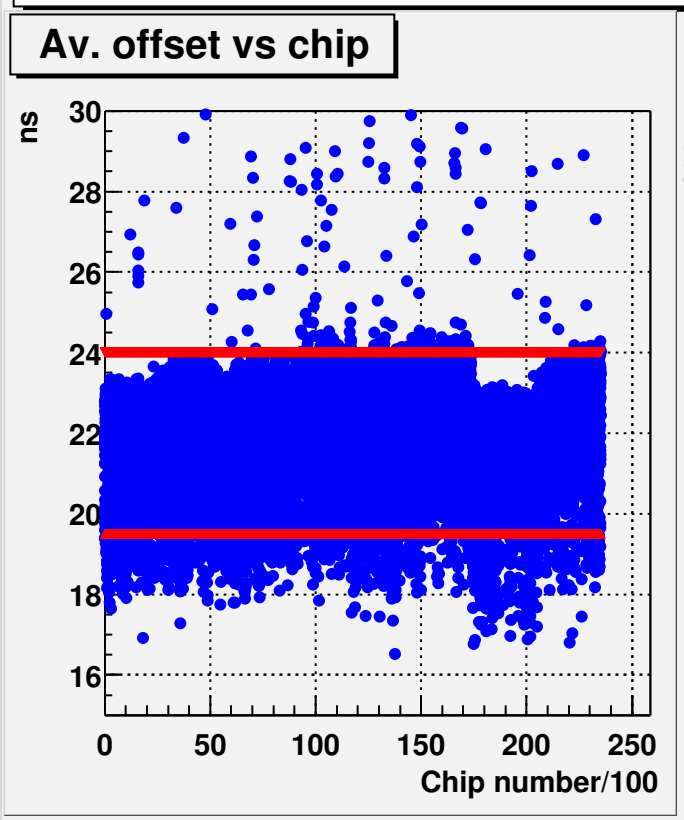


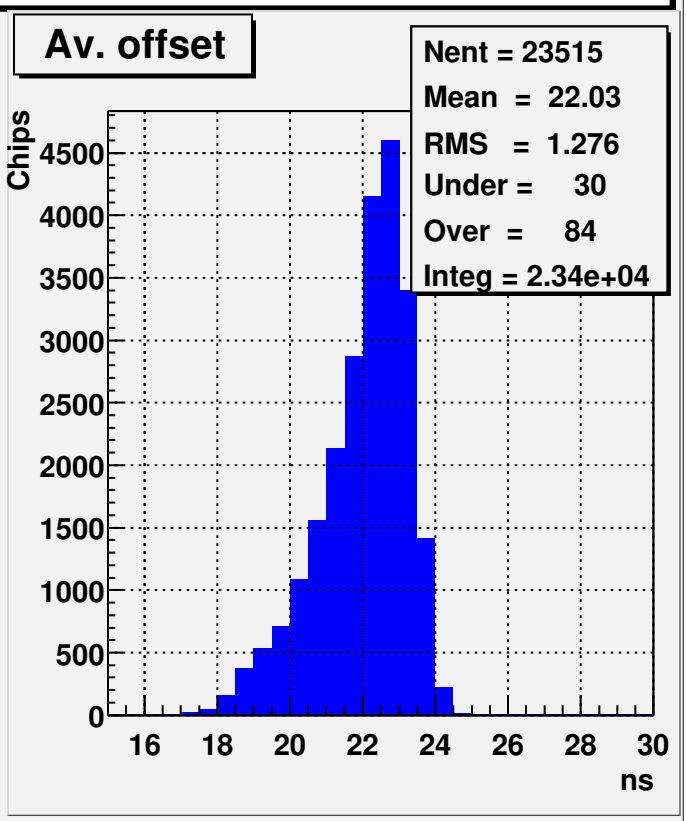


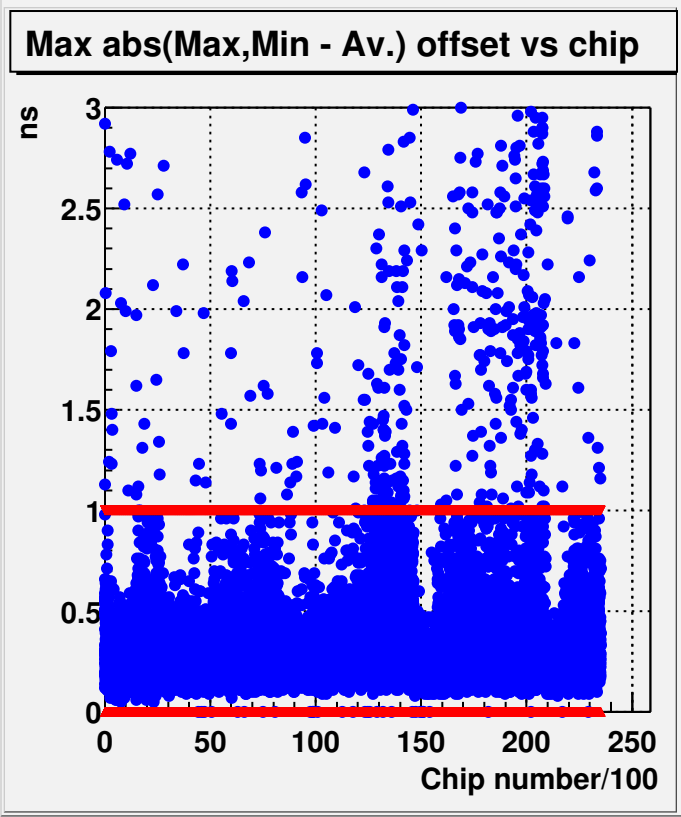


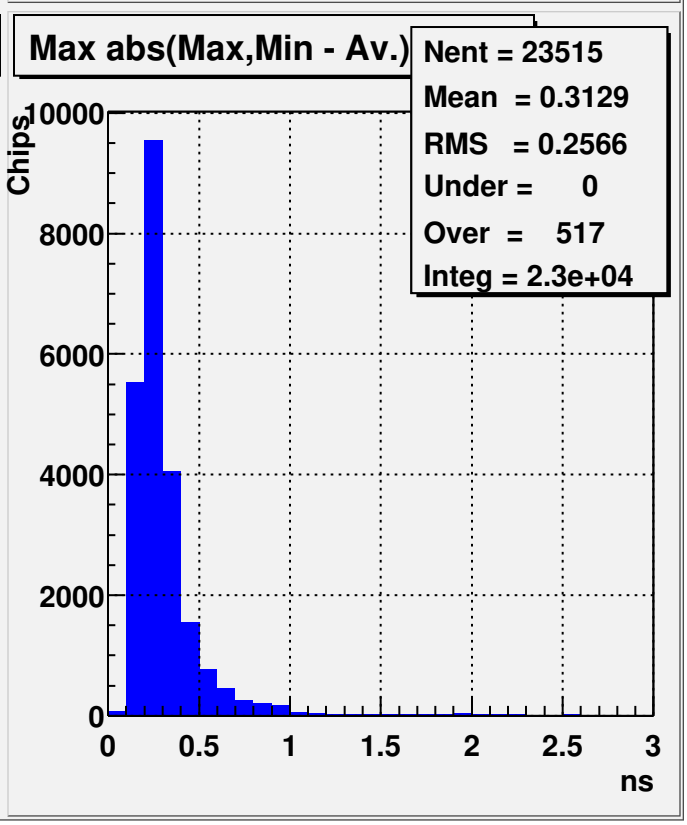


CMS EMU CSC D16G Prod. Test. Trays 1-149. All chips. CMU-FNAL

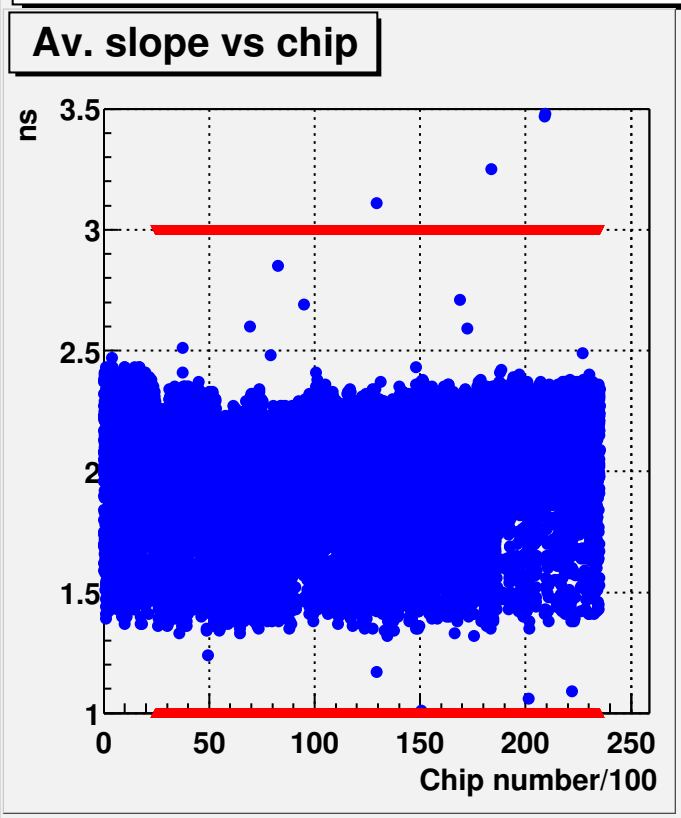


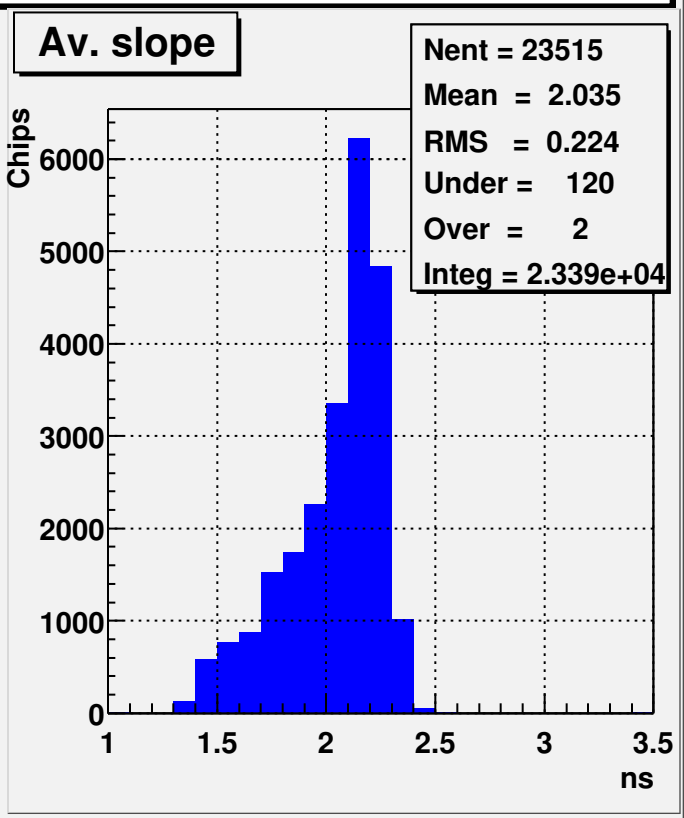


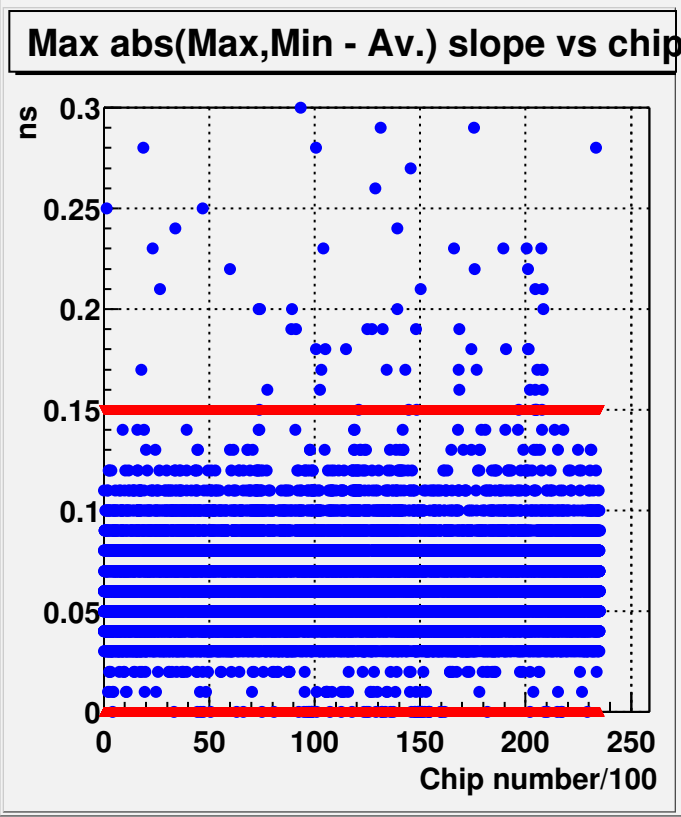


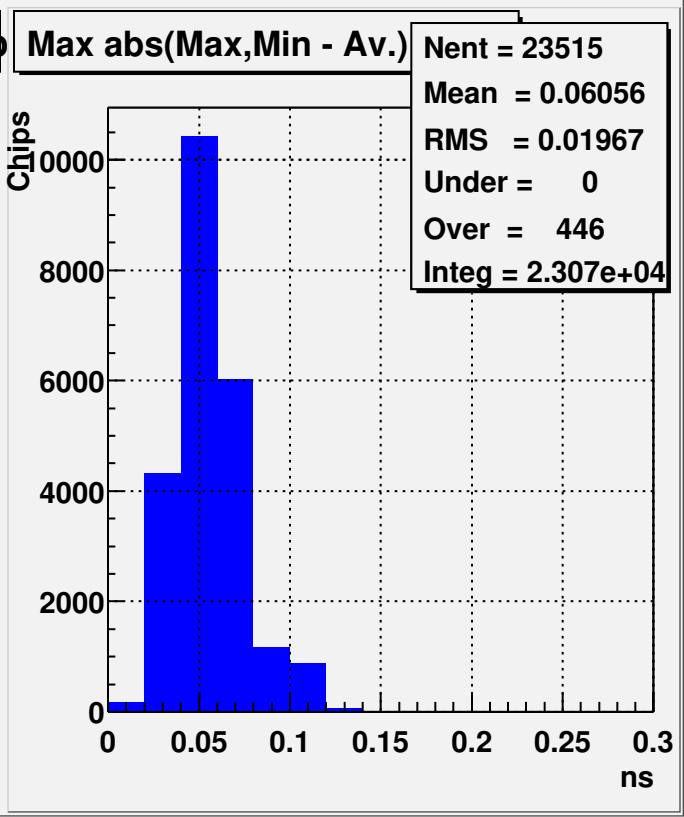


CMS EMU CSC D16G Prod. Test. Trays 1-149. All chips. CMU-FNAL

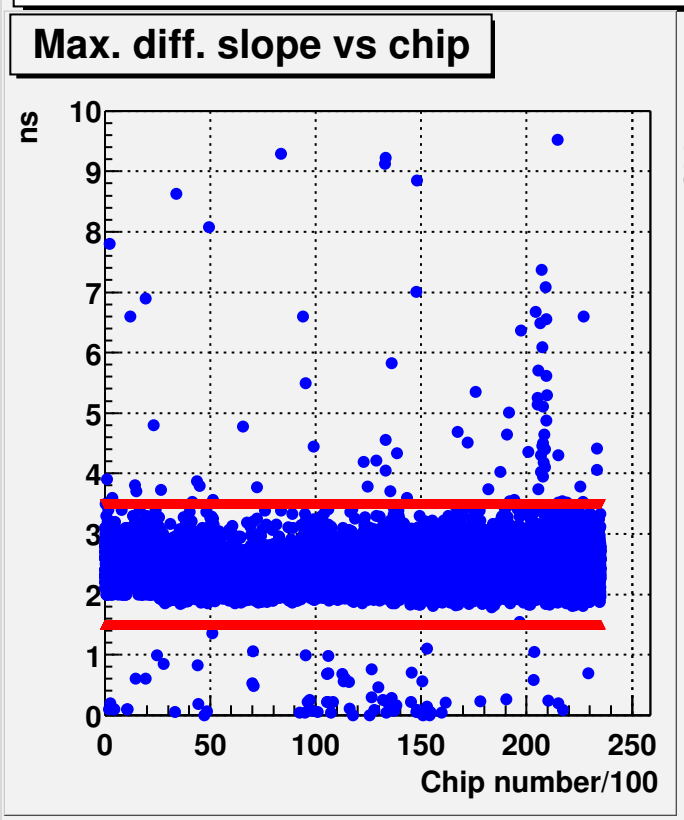


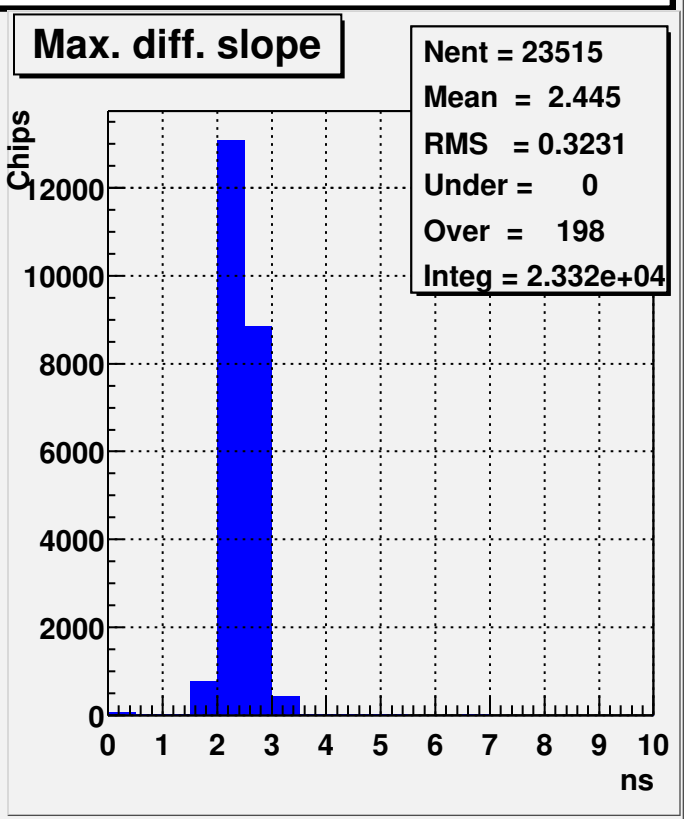


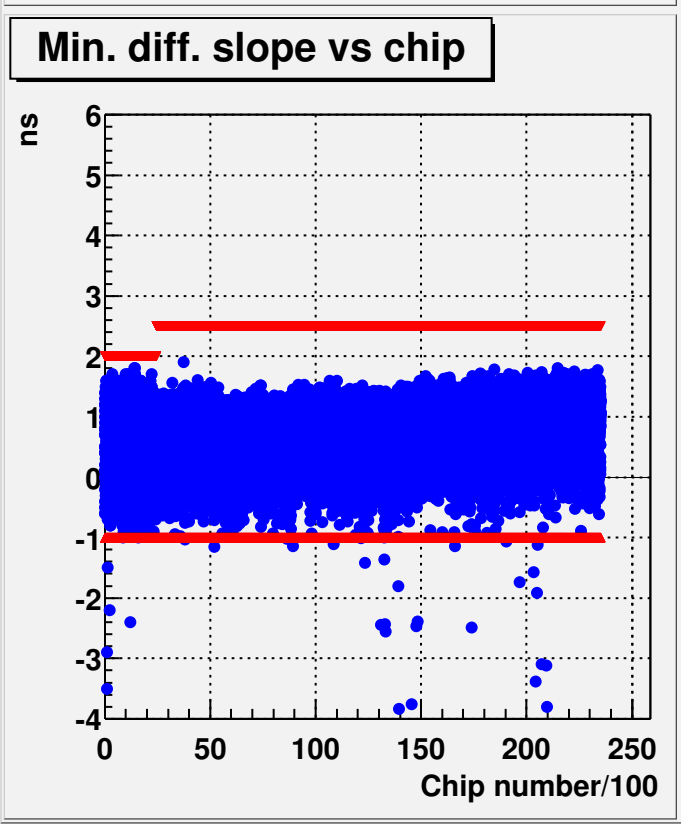


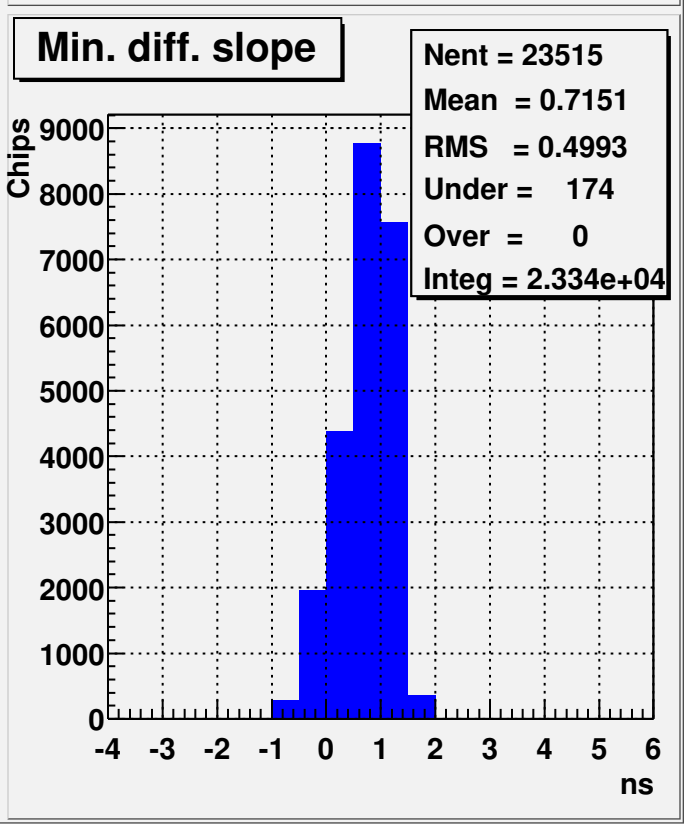


CMS EMU CSC D16G Prod. Test. Trays 1-149. All chips. CMU-FNAL

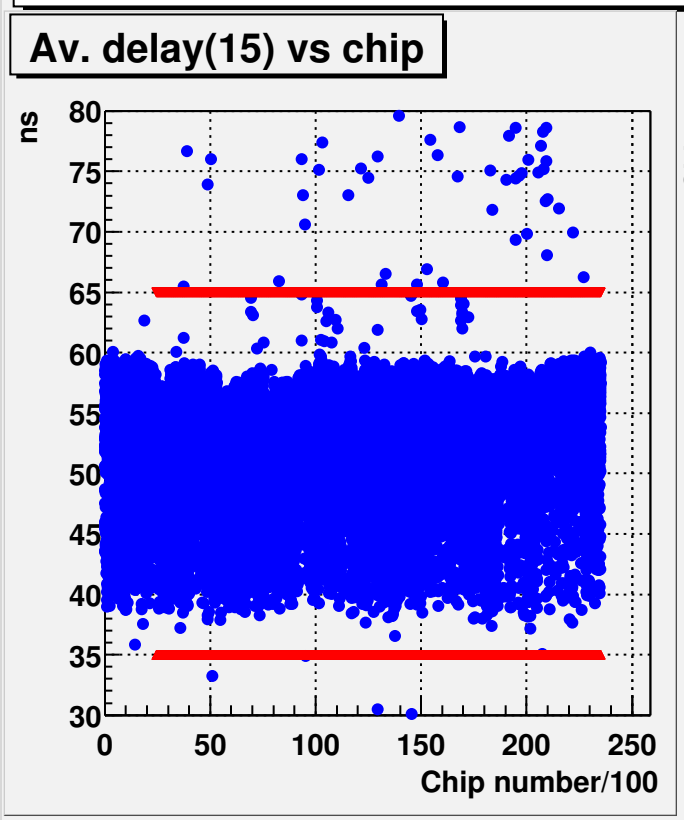


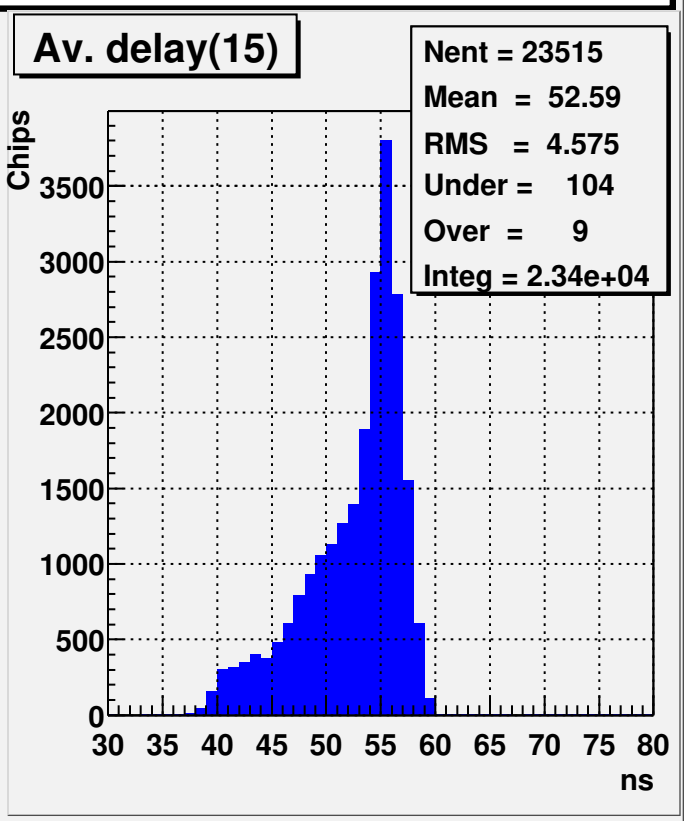


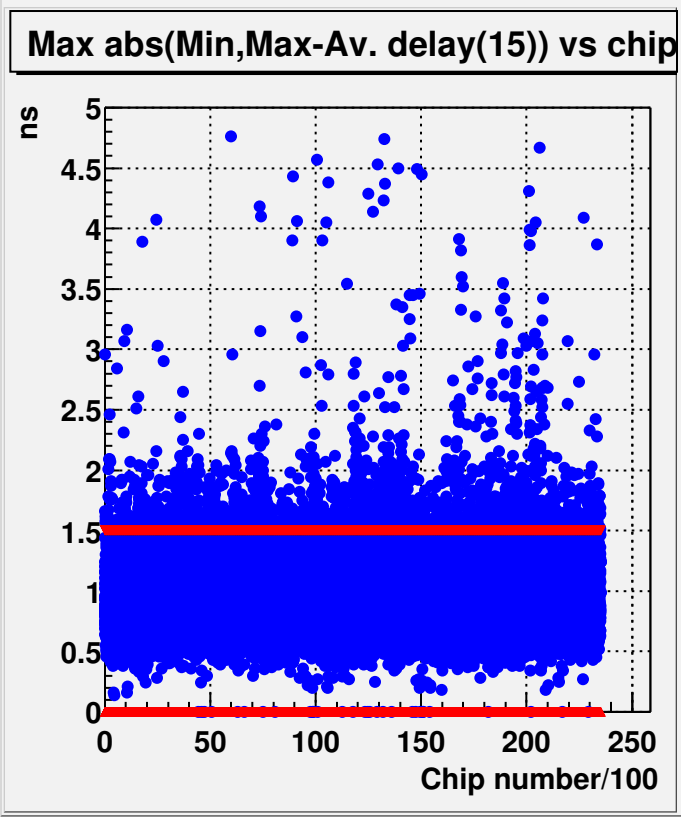


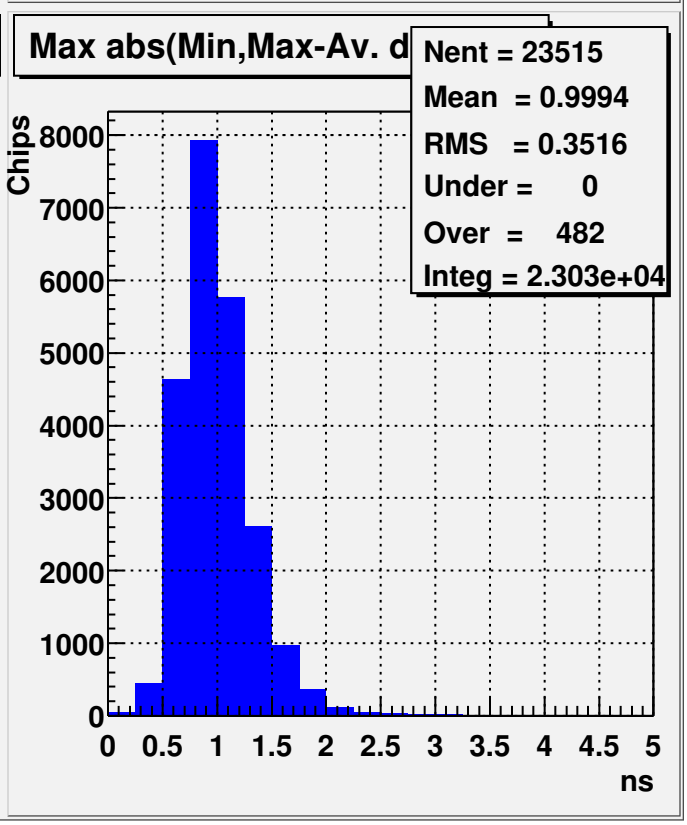


CMS EMU CSC D16G Prod. Test. Trays 1-149. All chips. CMU-FNAL



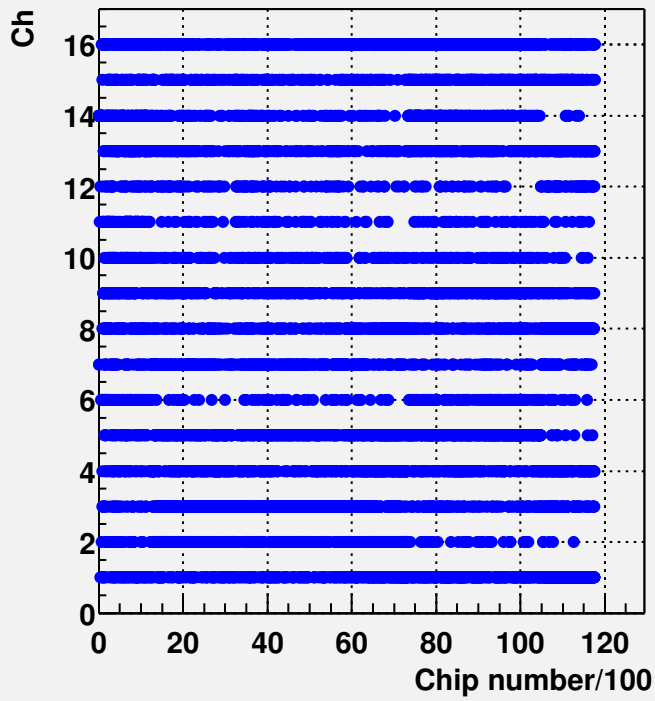




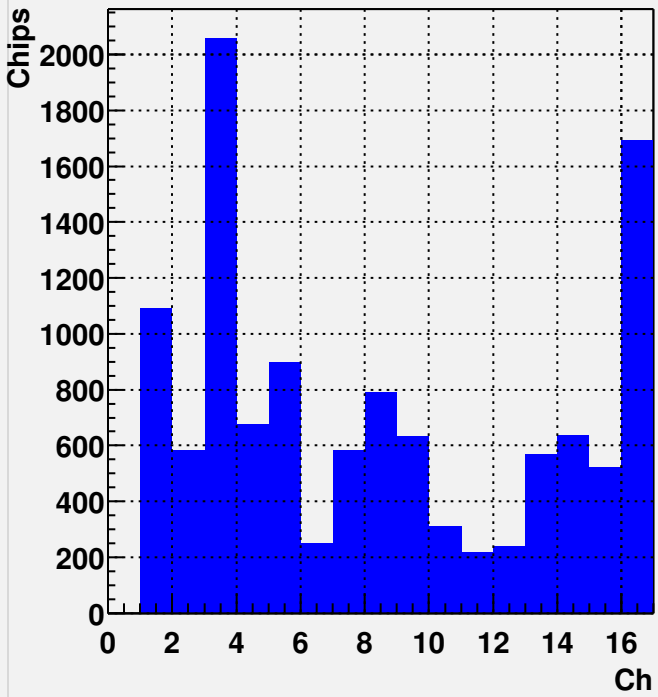


CMS EMU CSC D16G Prod. Test. Trays 1-149. All chips. CMU-FNAL

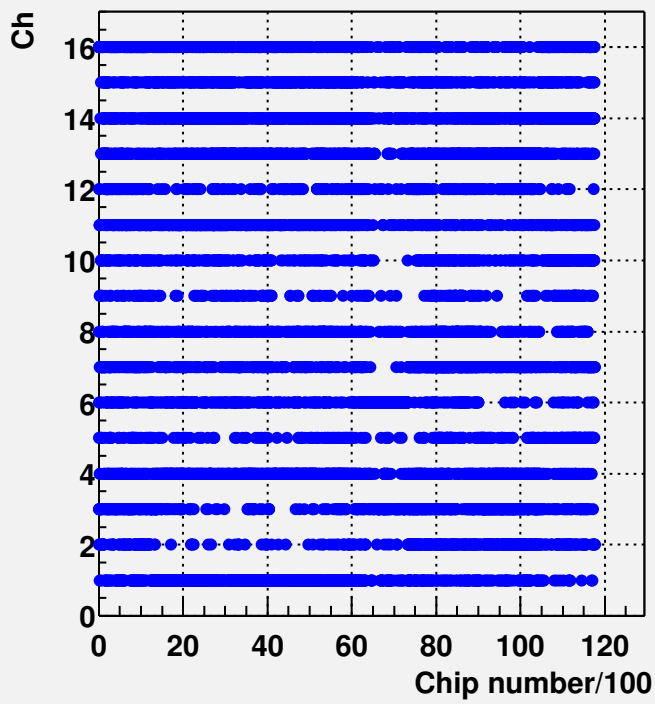
Slot 1 Chan. with min. offset vs chip



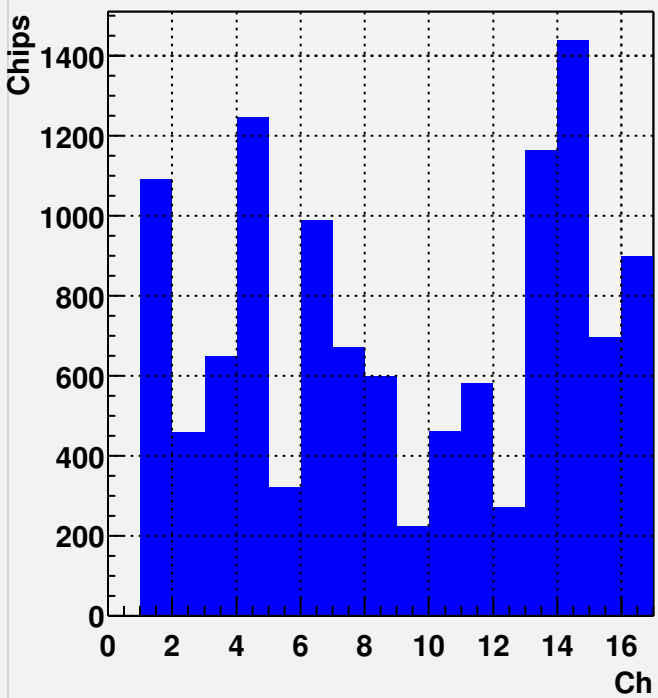
Slot 1 Chan. with min. offset



Slot 1 Chan. with max. offset vs chip

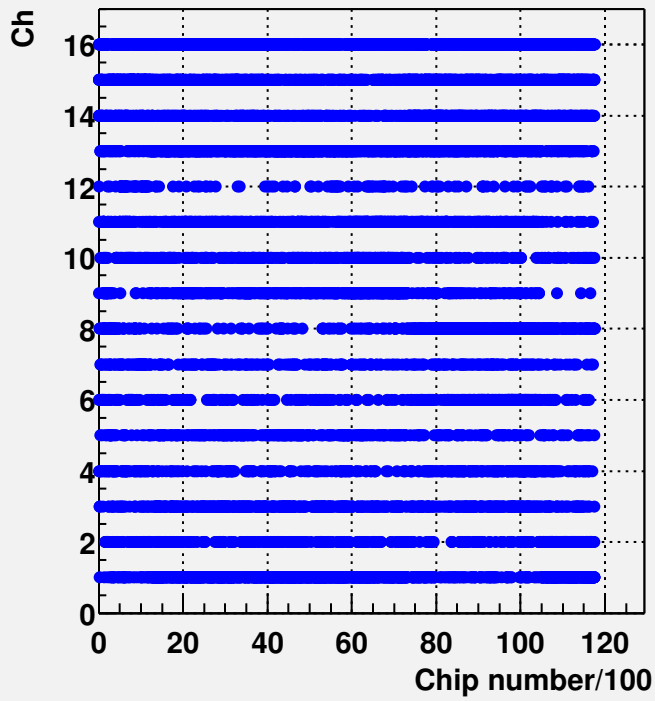


Slot 1 Chan. with max. offset

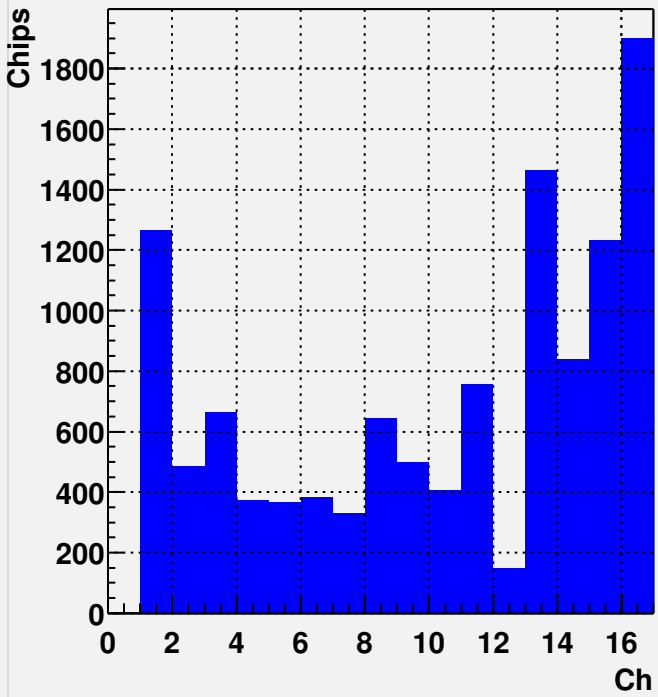


CMS EMU CSC D16G Prod. Test. Trays 1-149. All chips. CMU-FNAL

Slot 2 Chan. with min. offset vs chip



Slot 2 Chan. with min. offset



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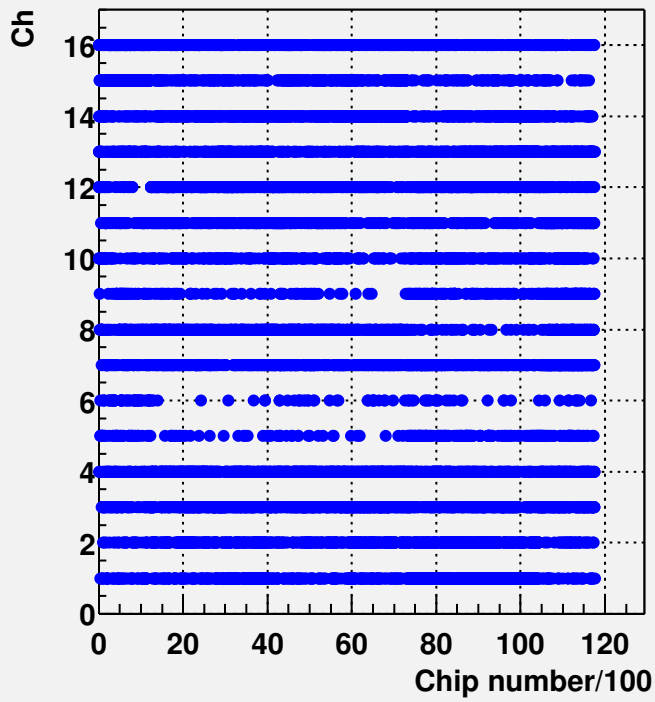
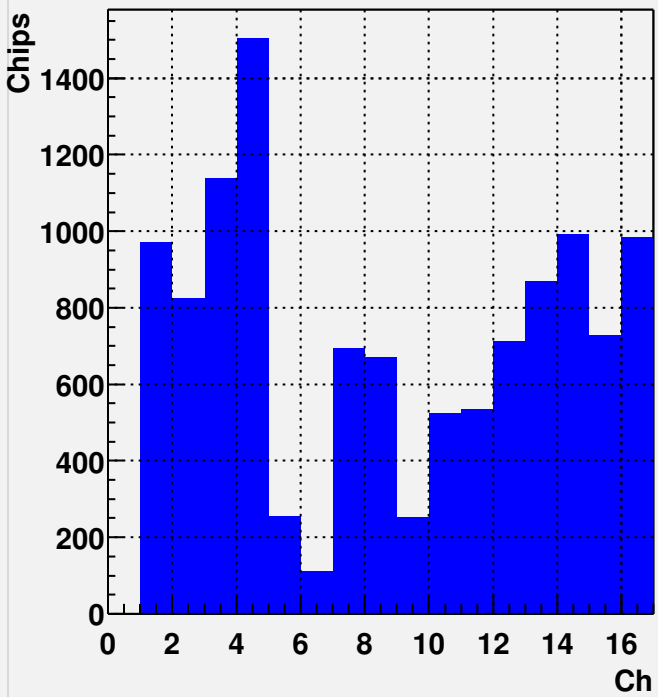
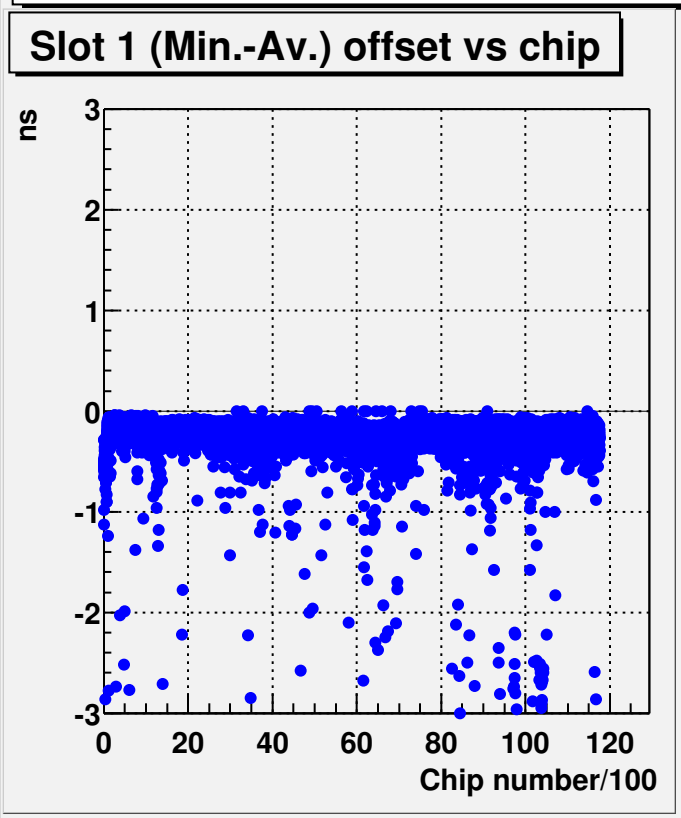
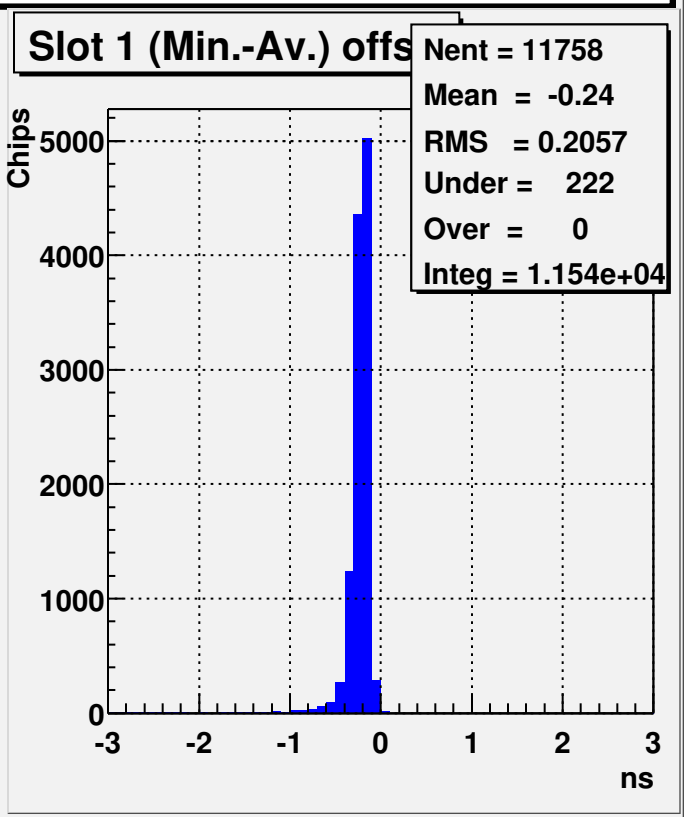


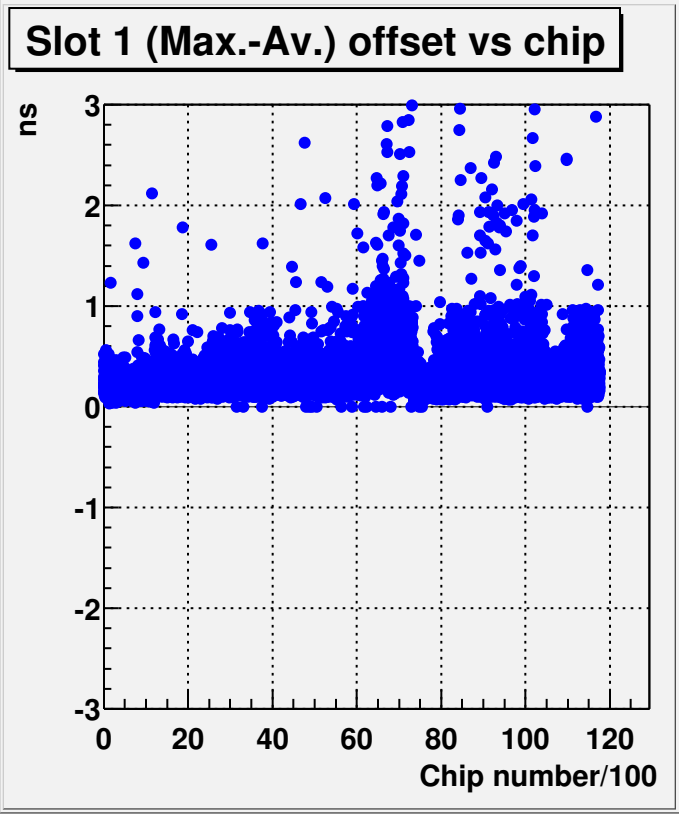
Slot 2 Chan. with max. offset

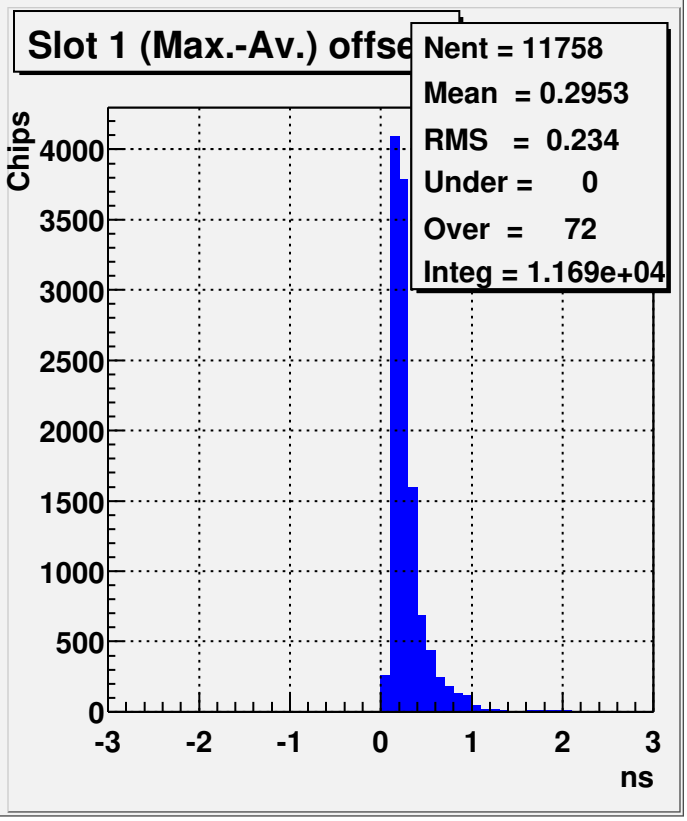


CMS EMU CSC D16G Prod. Test. Trays 1-149. All chips. CMU-FNAL

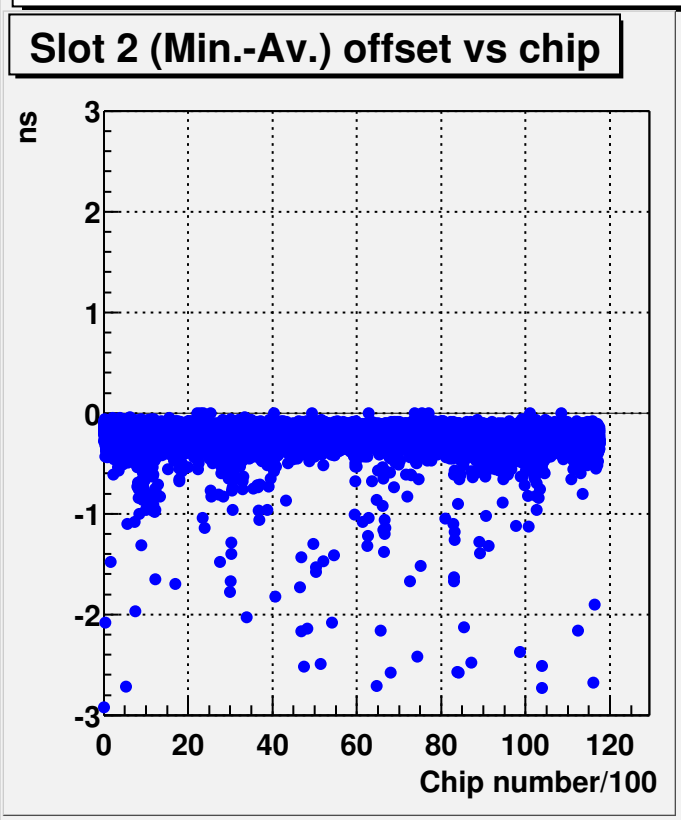


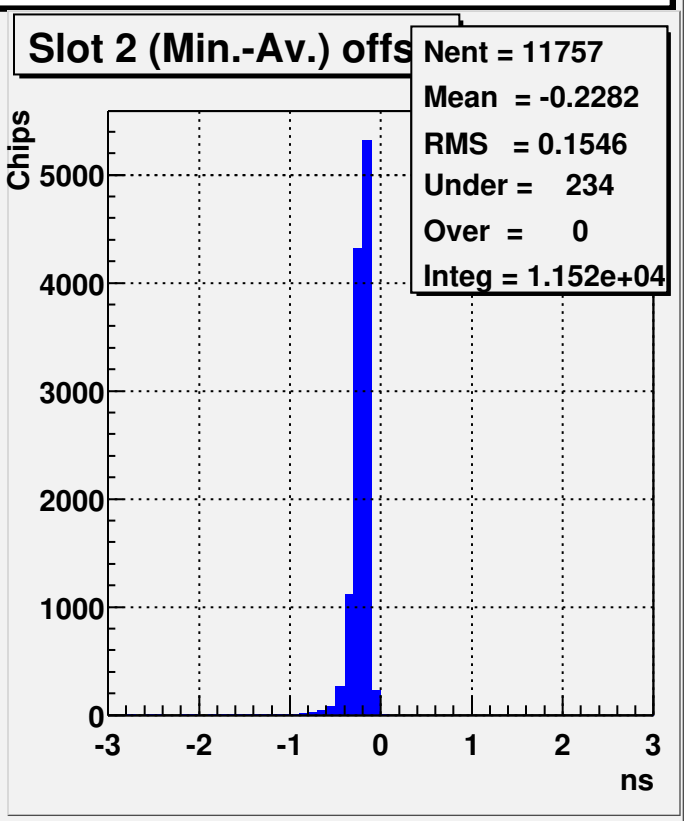


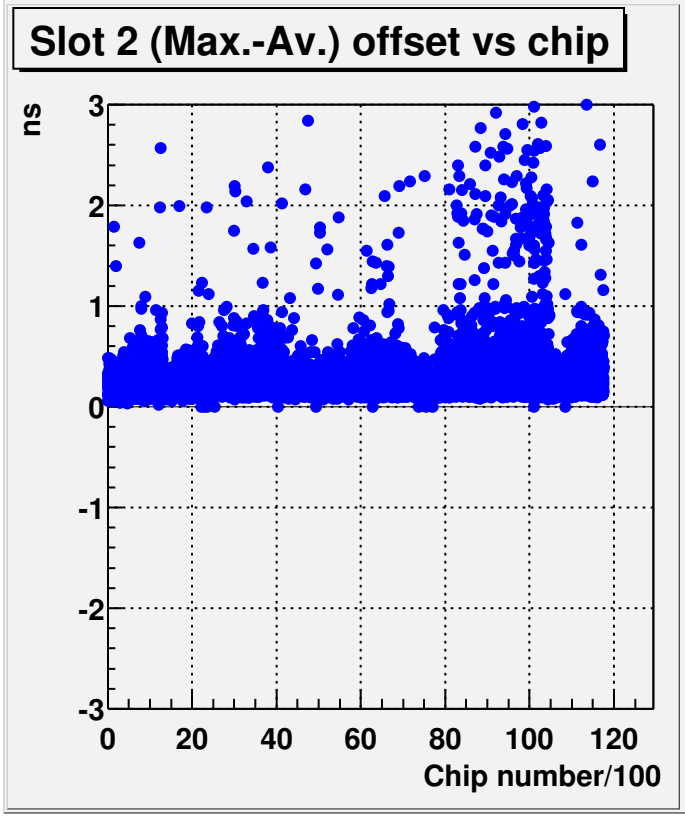


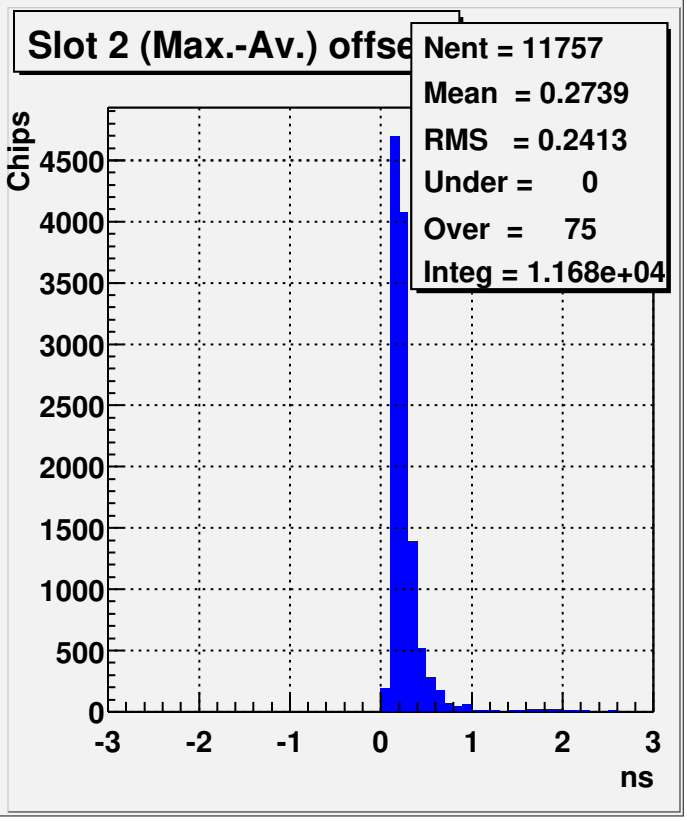


CMS EMU CSC D16G Prod. Test. Trays 1-149. All chips. CMU-FNAL



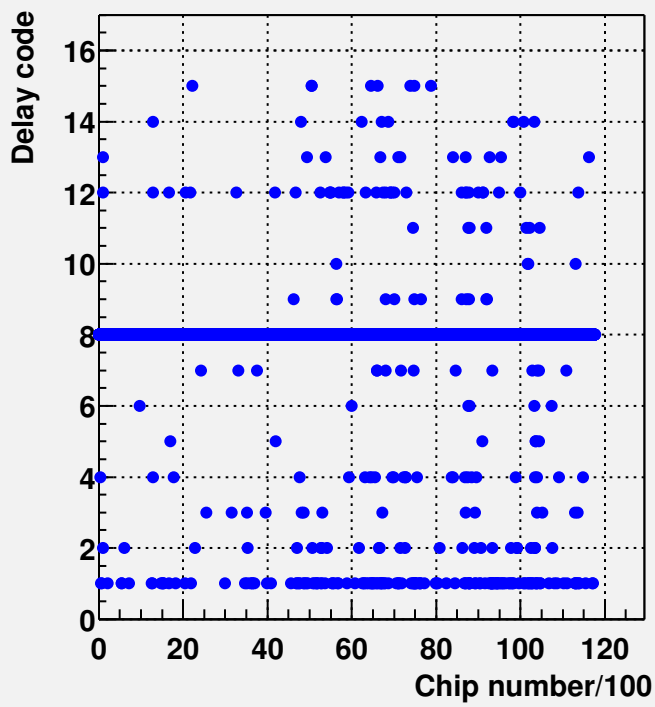




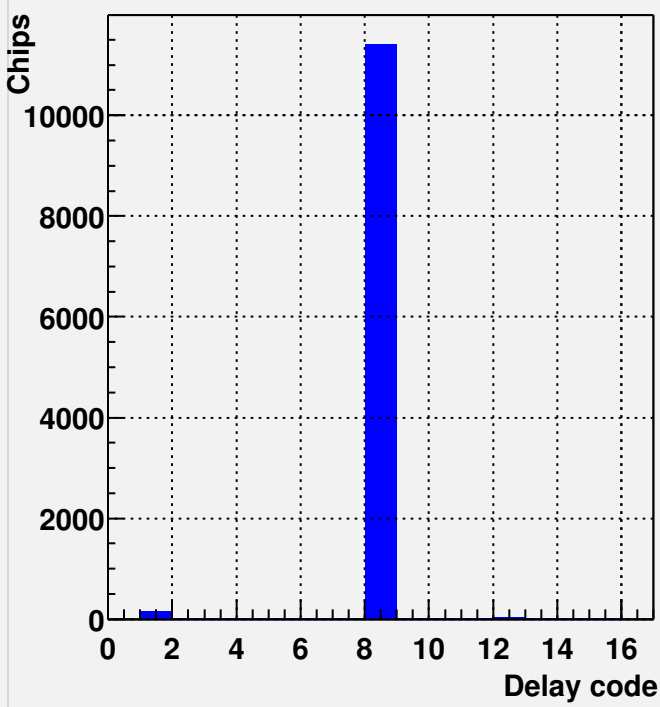


CMS EMU CSC D16G Prod. Test. Trays 1-149. All chips. CMU-FNAL

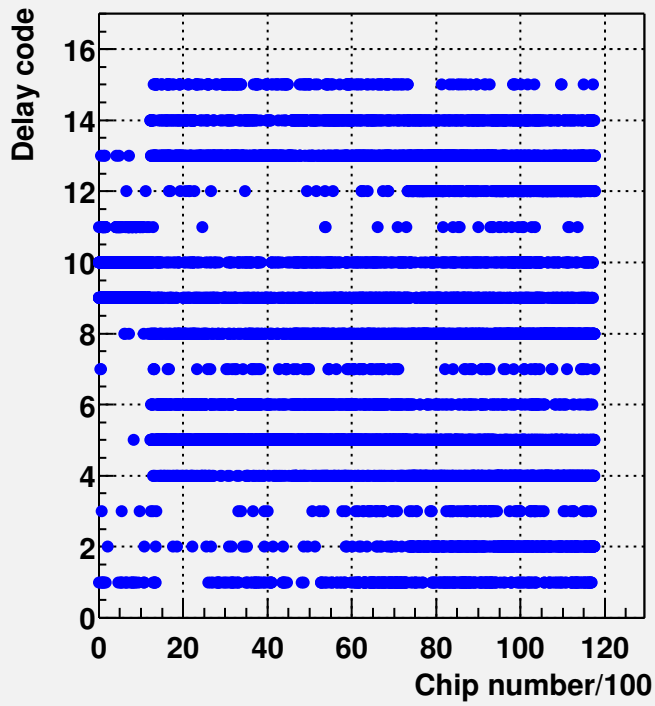
Slot 1. Delay code for min. dif. slope vs chip



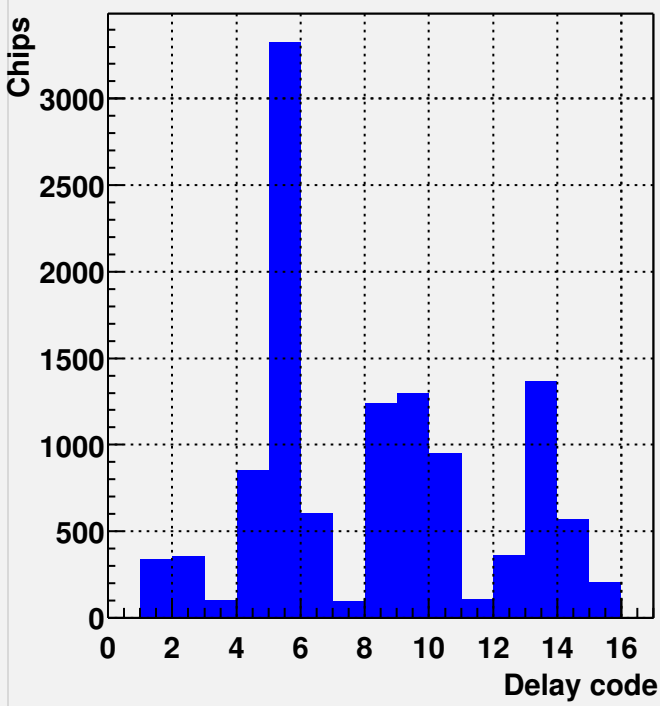
Slot 1. Delay code for min. dif. slope



Slot 1. Delay code for max. dif. slope vs chip

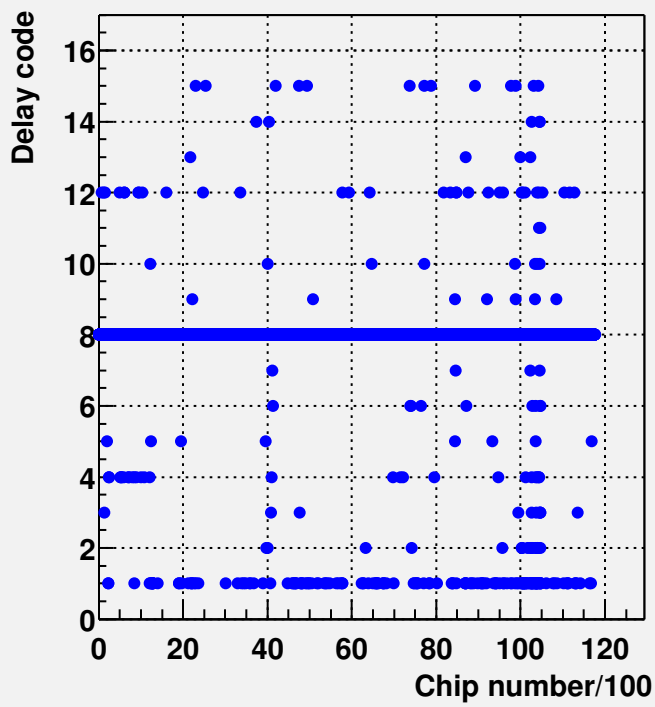


Slot 1. Delay code for max. dif. slope

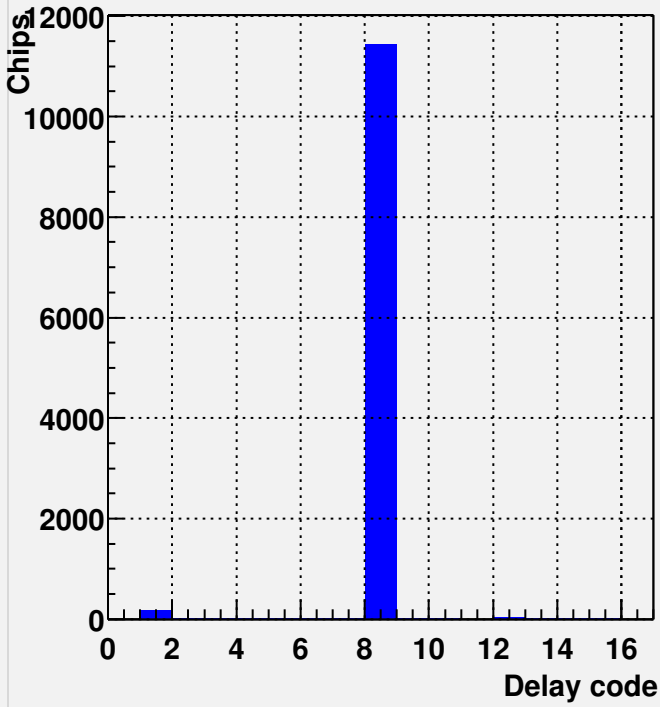


CMS EMU CSC D16G Prod. Test. Trays 1-149. All chips. CMU-FNAL

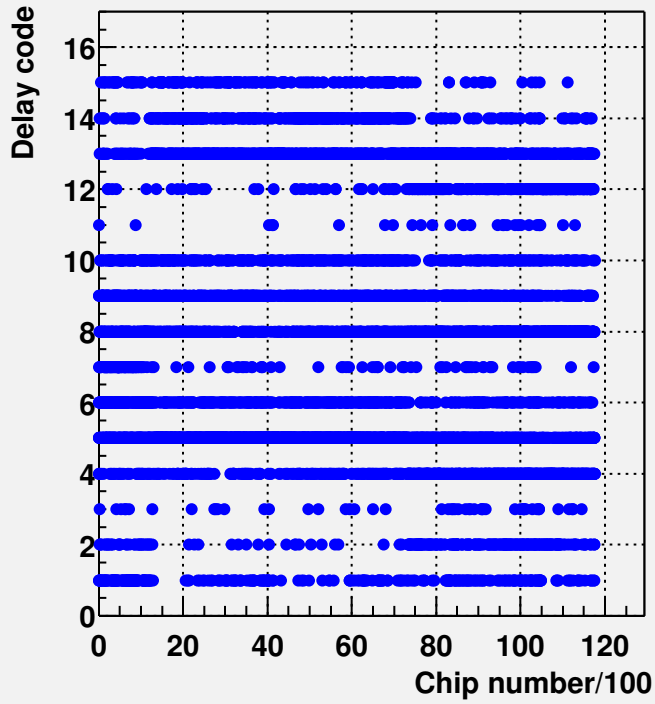
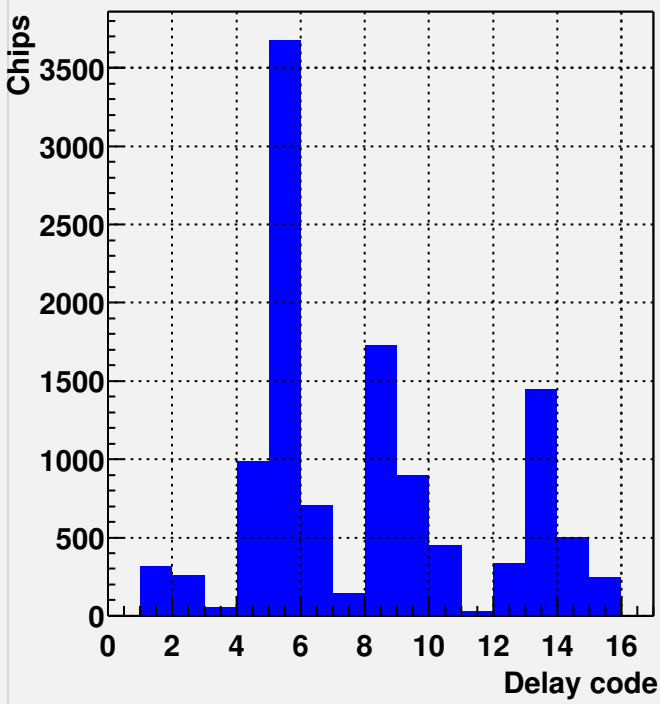
Slot 2. Delay code for min. dif. slope vs chip



Slot 2. Delay code for min. dif. slope



Slot 2. Delay code for max. dif. slope vs chip



Slot 2. Delay code for max. dif. slope