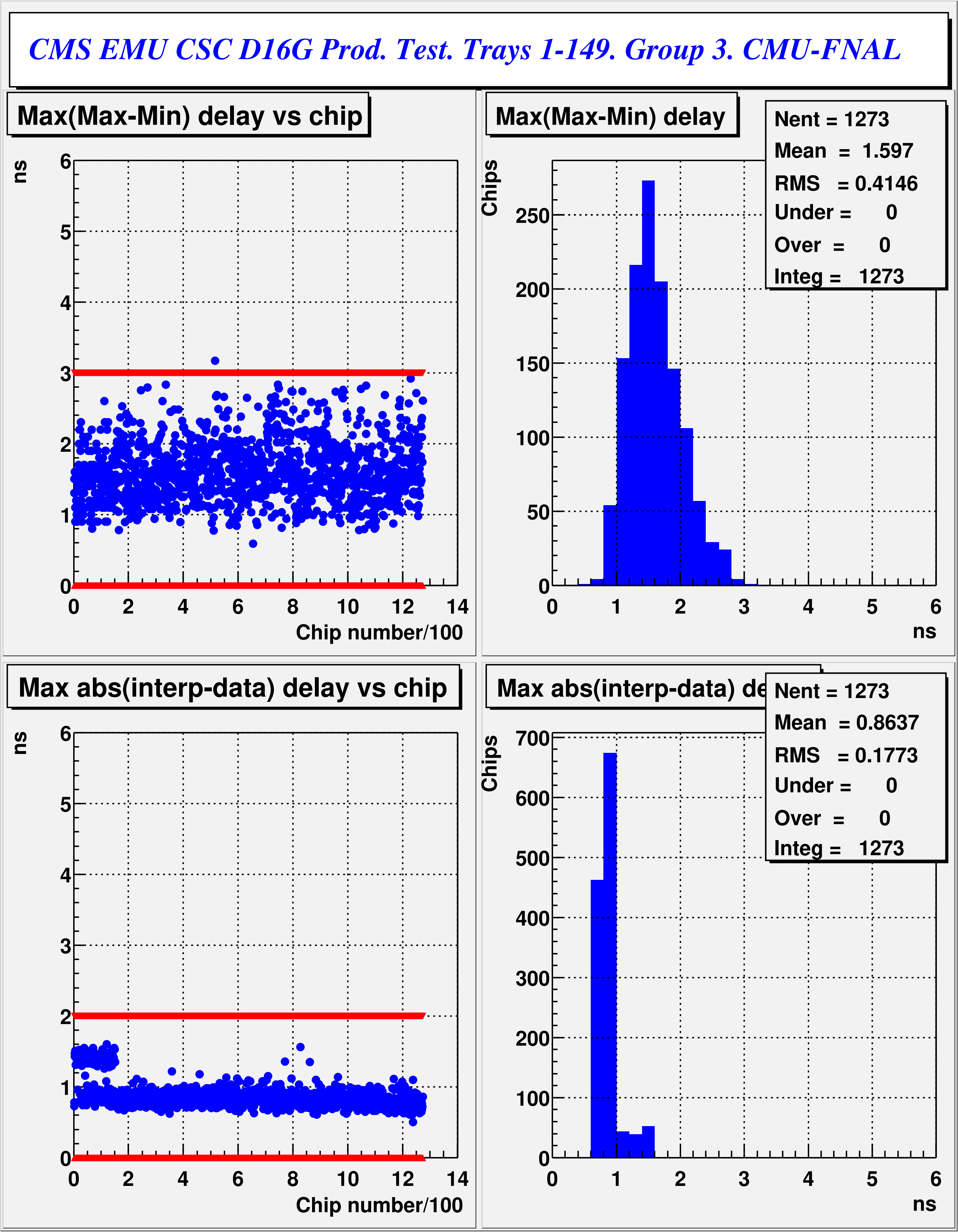
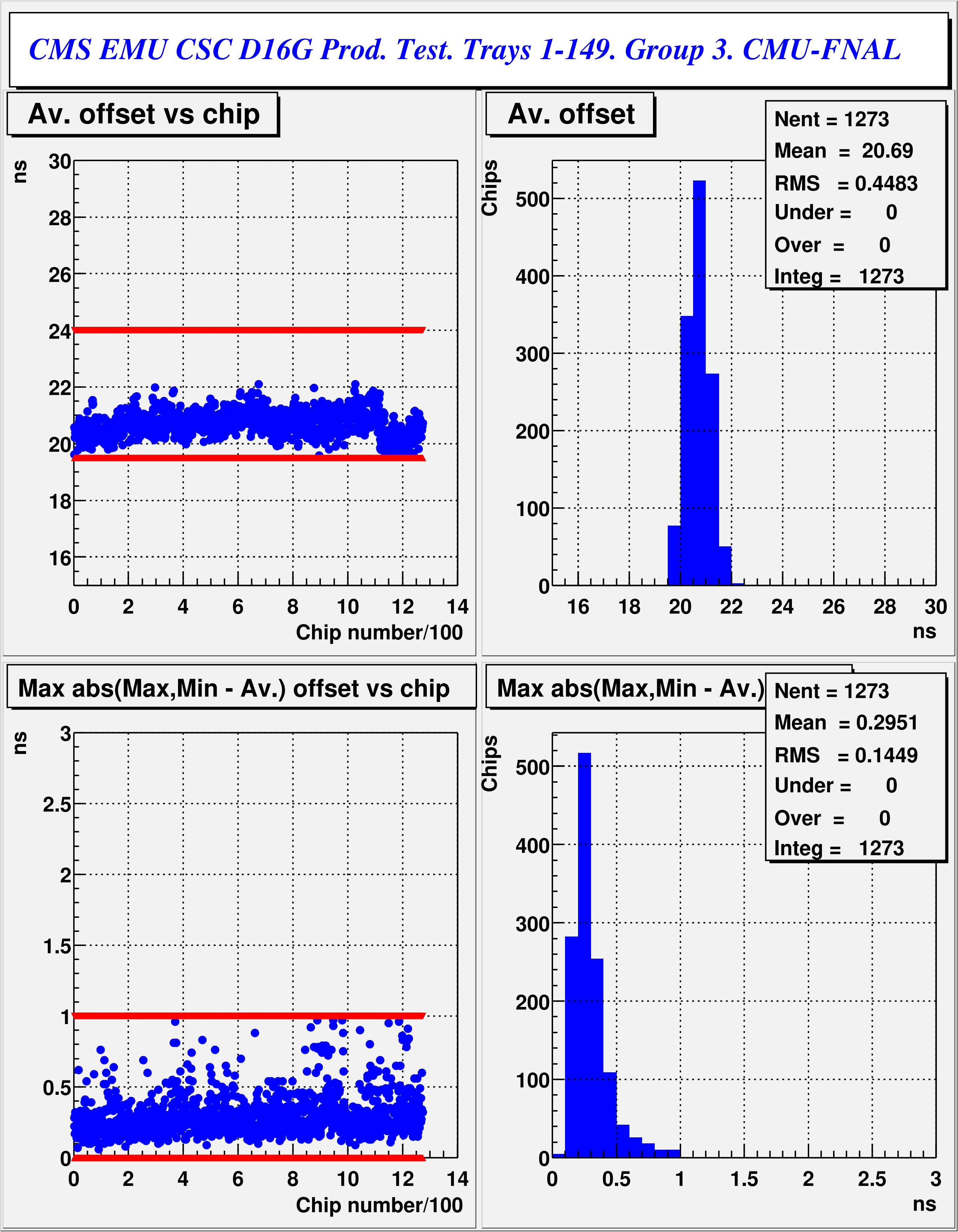
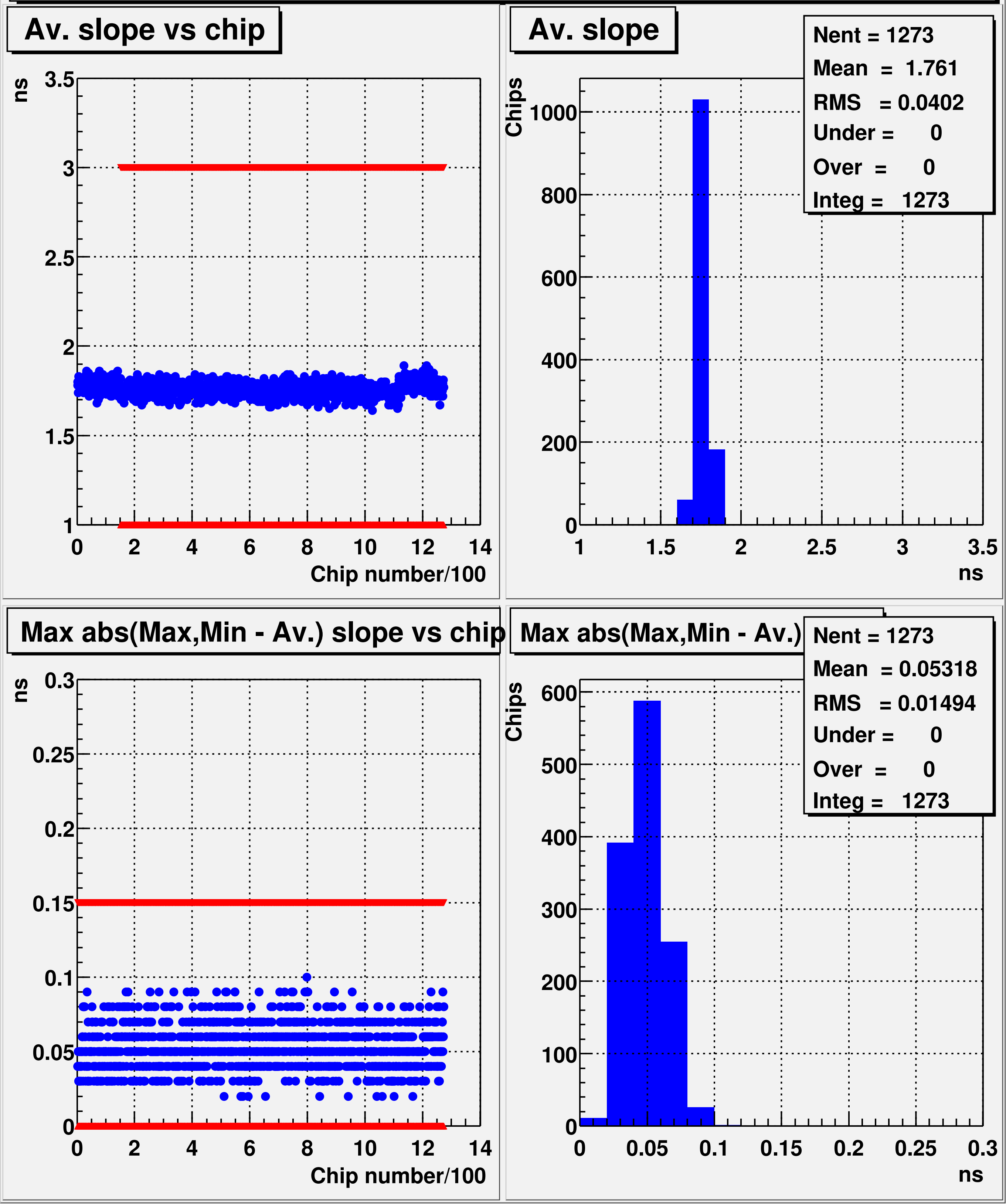
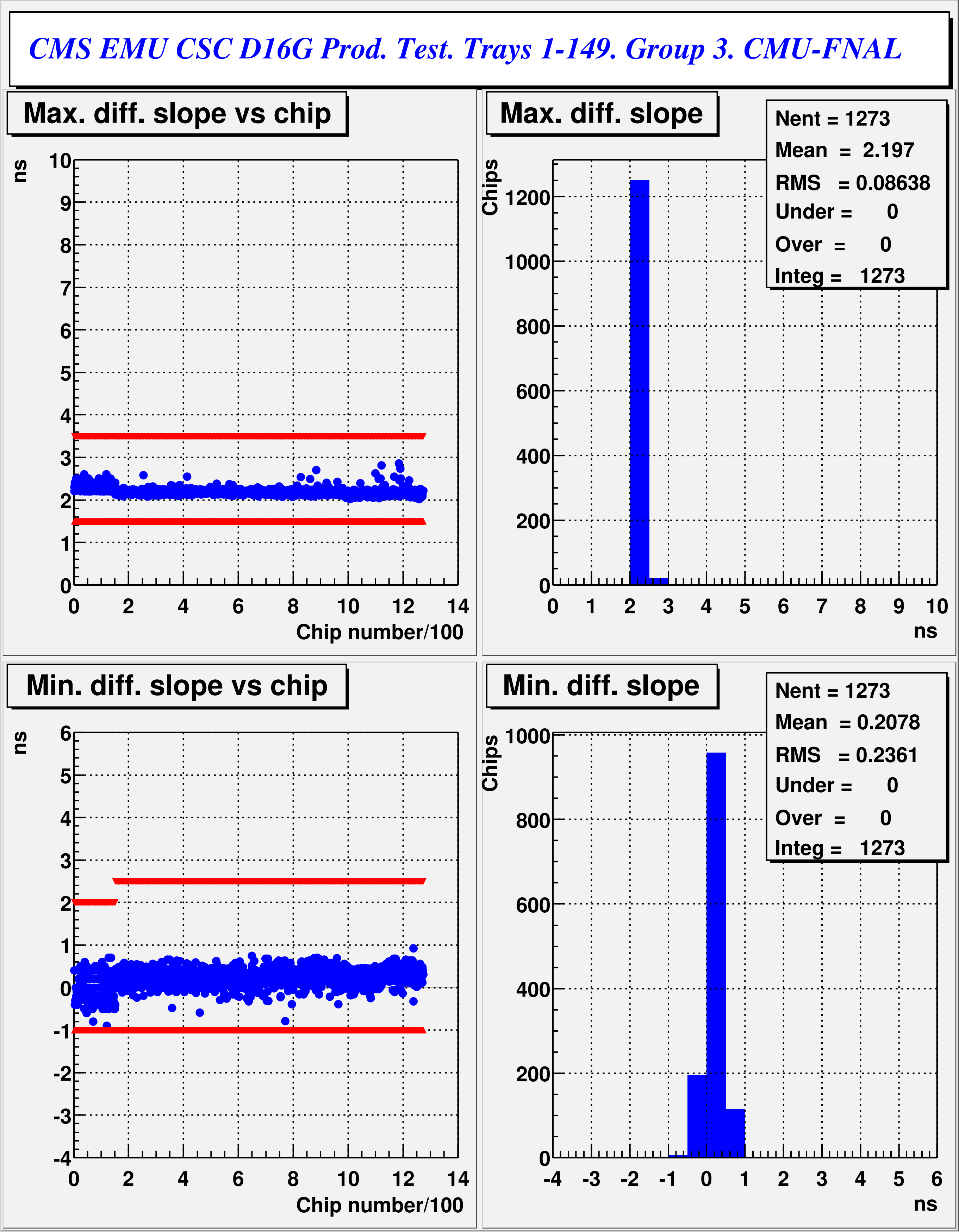

CMS EMU CSC D16G Prod. Test. Trays 1-149. Group 3. CMU-FNAL
Max(Max-Min) delay vs chip
ns
Chip number/100
Max(Max-Min) delay
Chips
ns
Nent = 1273
Mean = 1.597
RMS = 0.4146
Under = 0
Over = 0
Integ = 1273
Max abs(interp-data) delay vs chip
ns
Chip number/100
Max abs(interp-data) de
Chips
ns
Nent = 1273
Mean = 0.8637
RMS = 0.1773
Under = 0
Over = 0
Integ = 1273

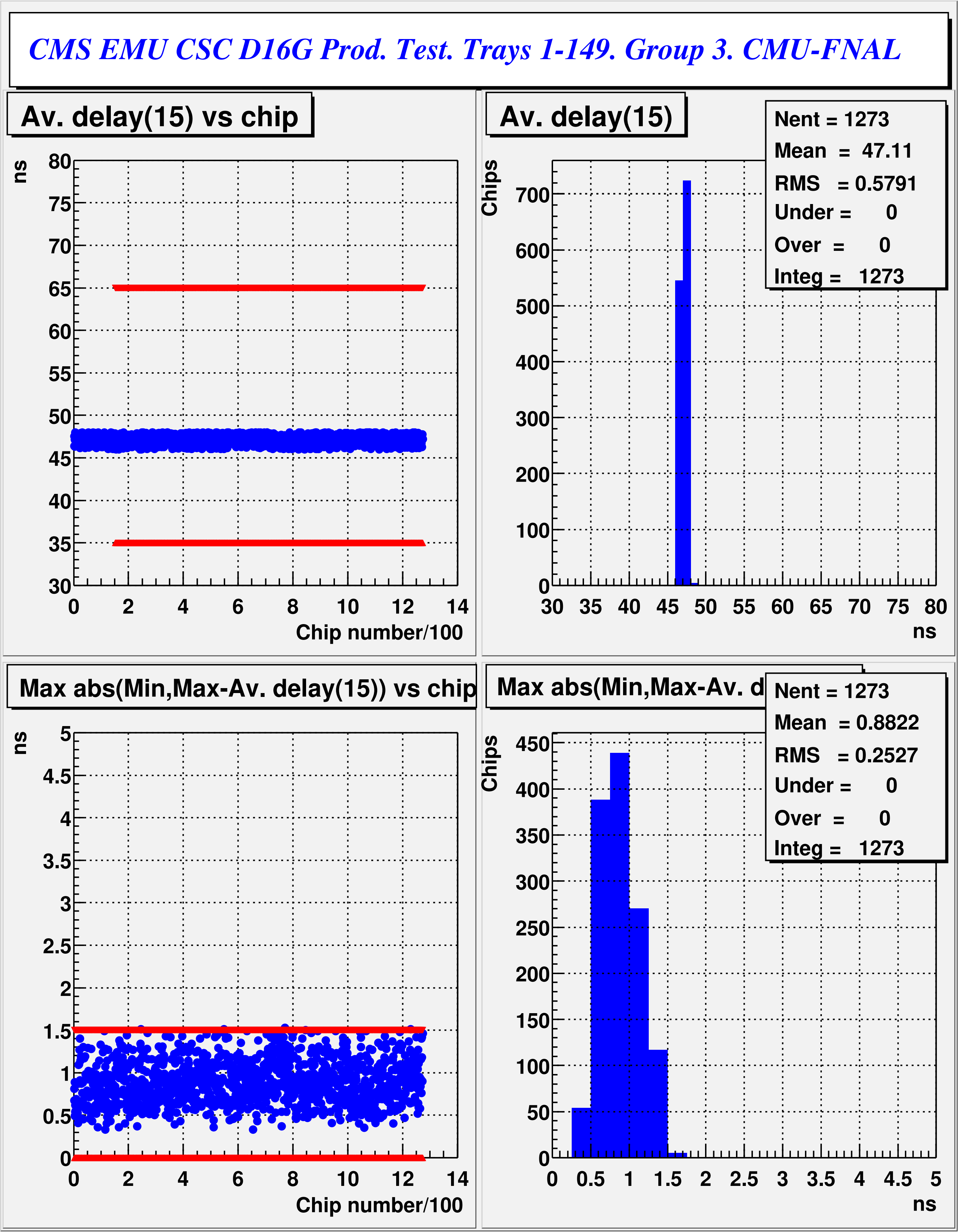


CMS EMU CSC D16G Prod. Test. Trays 1-149. Group 3. CMU-FNAL
Av. offset vs chip
ns
Chip number/100
Av. offset
Chips
ns
Nent = 1273
Mean = 20.69
RMS = 0.4483
Under = 0
Over = 0
Integ = 1273
Max abs(Max,Min - Av.) offset vs chip
ns
Chip number/100
Max abs(Max,Min - Av.)
Chips
ns
Nent = 1273
Mean = 0.2951
RMS = 0.1449
Under = 0
Over = 0
Integ = 1273

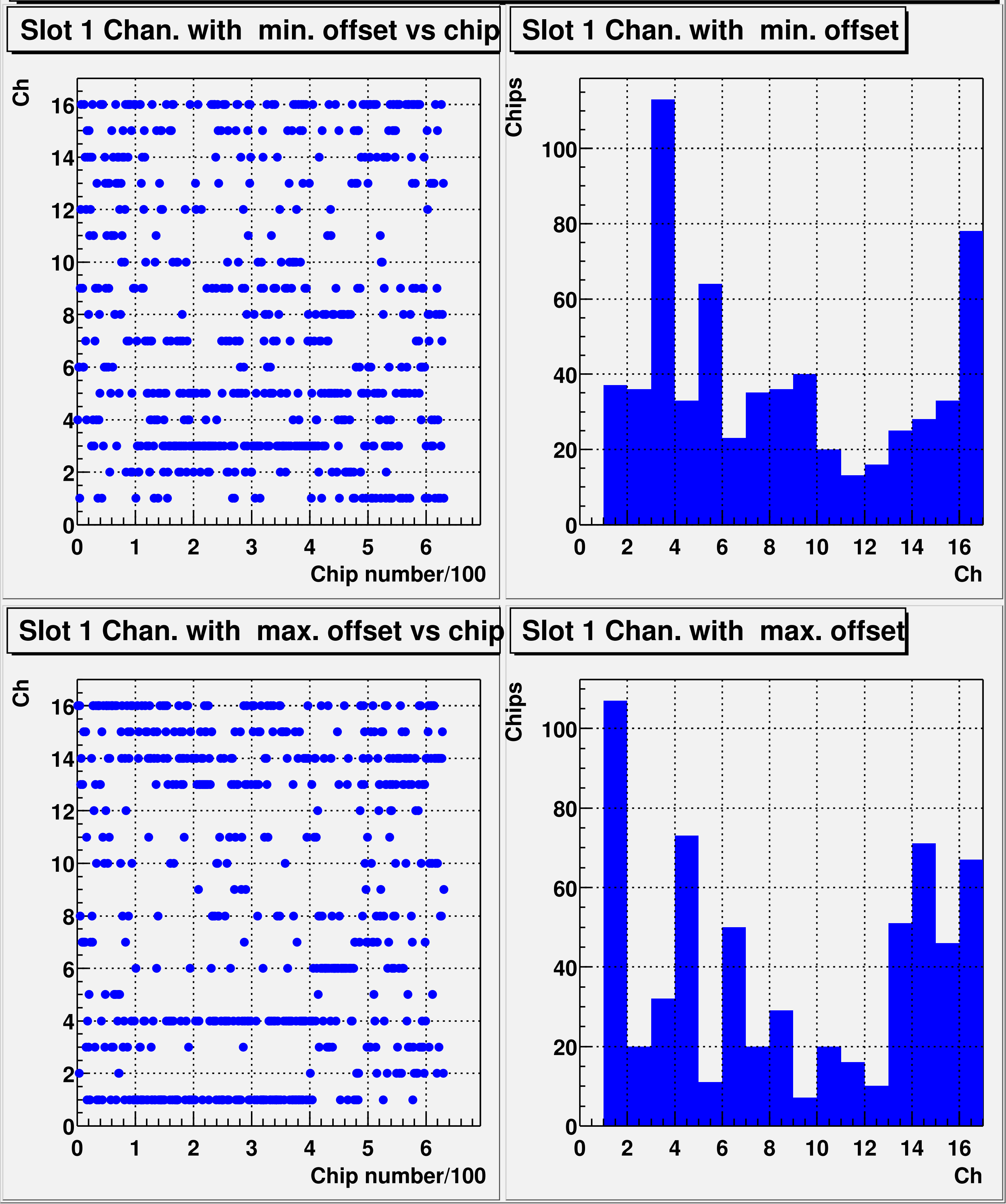
CMS EMU CSC D16G Prod. Test. Trays 1-149. Group 3. CMU-FNAL

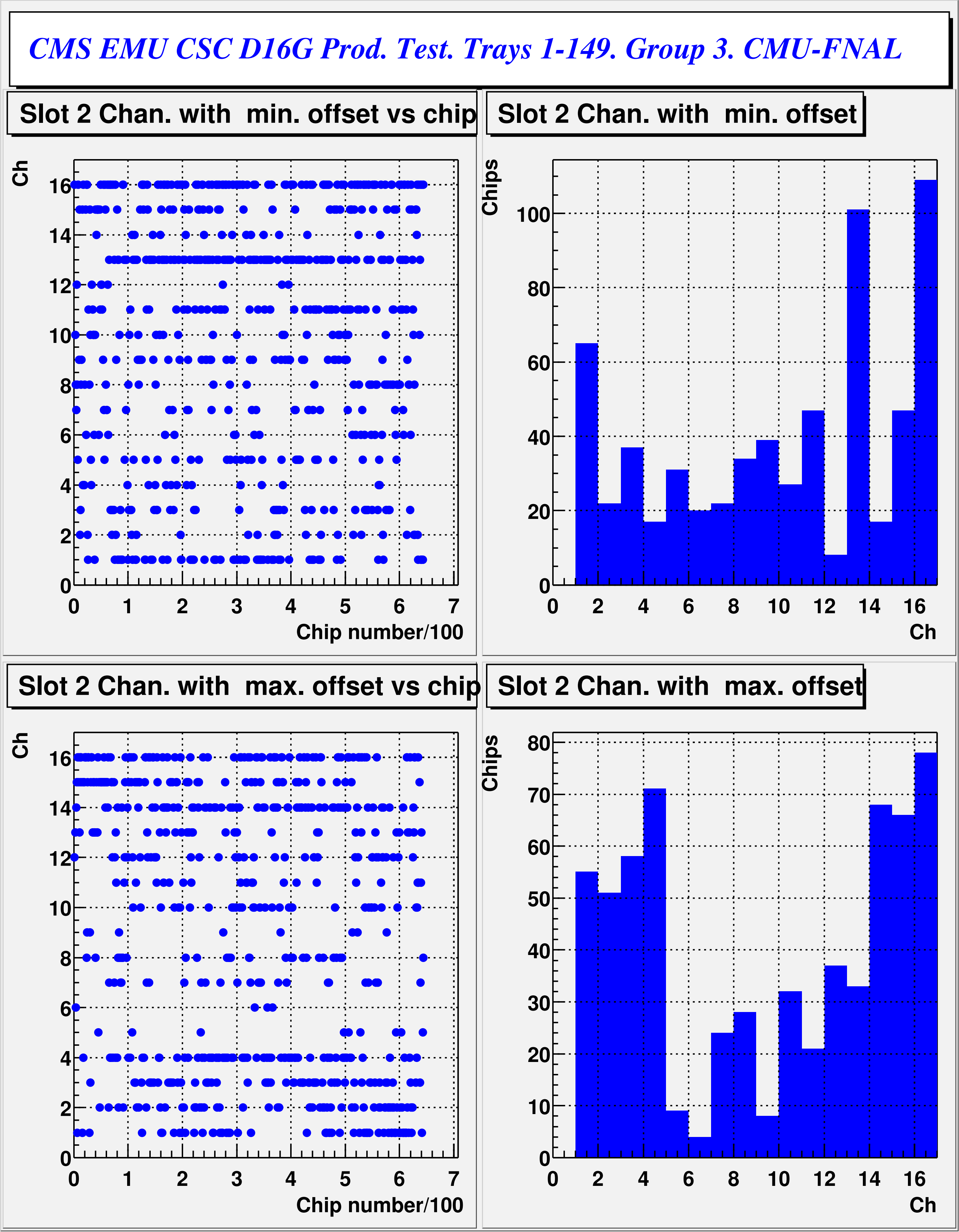


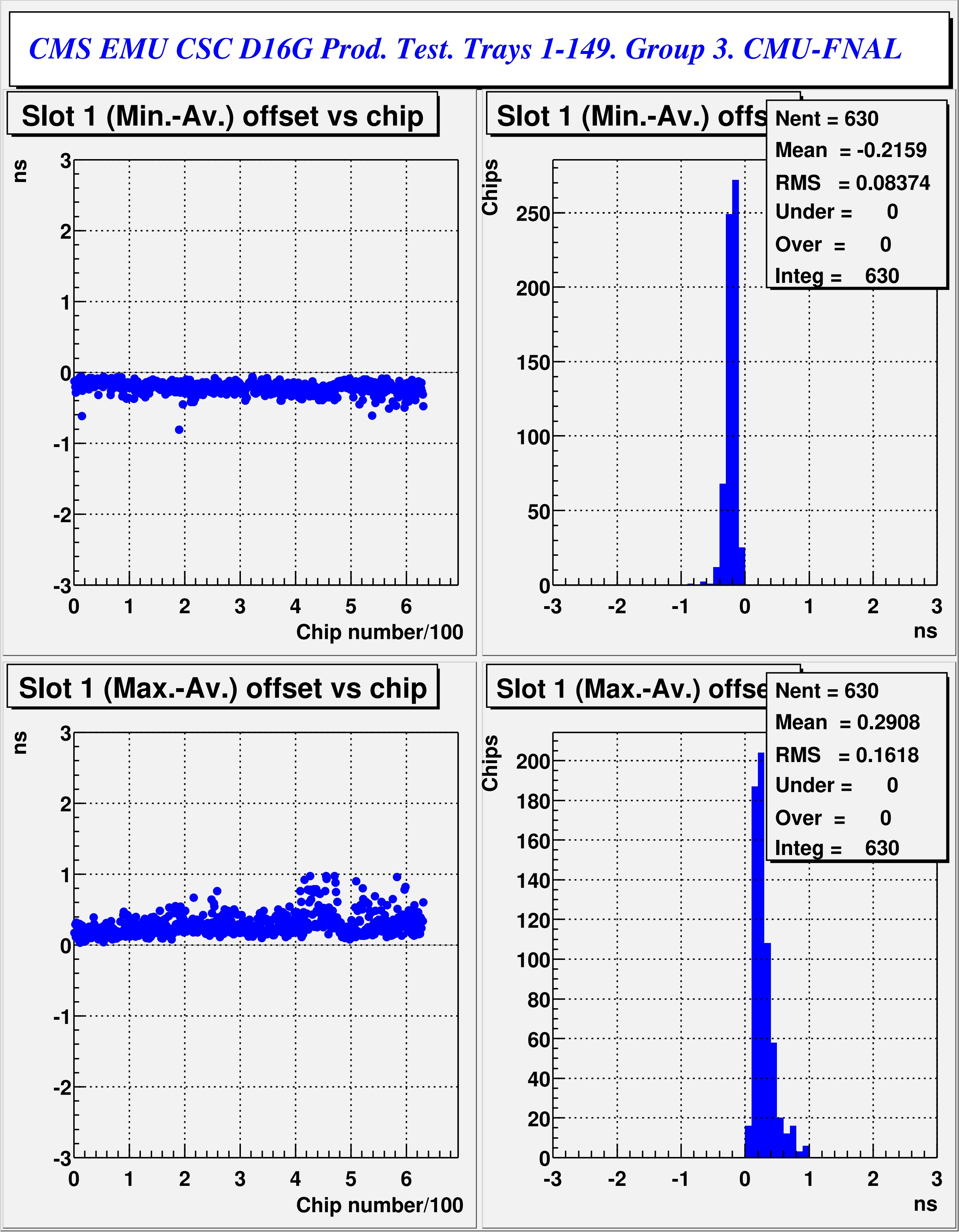

CMS EMU CSC D16G Prod. Test. Trays 1-149. Group 3. CMU-FNAL
Max. diff. slope vs chip
ns
Chip number/100
Max. diff. slope
Chips
ns
Nent = 1273
Mean = 2.197
RMS = 0.08638
Under = 0
Over = 0
Integ = 1273
Min. diff. slope vs chip
ns
Chip number/100
Min. diff. slope
Chips
ns
Nent = 1273
Mean = 0.2078
RMS = 0.2361
Under = 0
Over = 0
Integ = 1273

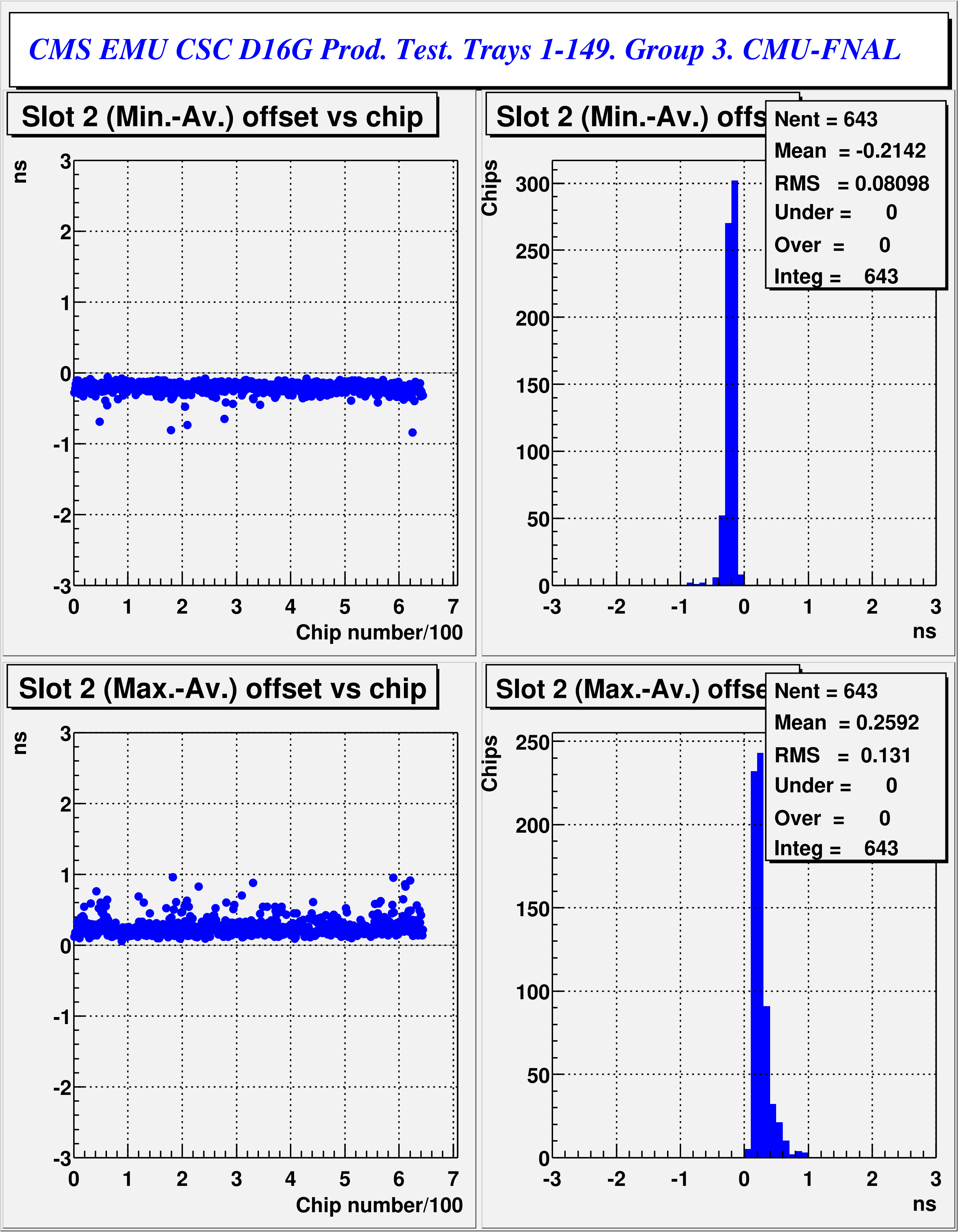

CMS EMU CSC D16G Prod. Test. Trays 1-149. Group 3. CMU-FNAL
Av. delay(15) vs chip
ns
Chip number/100
Av. delay(15)
Nent = 1273
Mean = 47.11
RMS = 0.5791
Under = 0
Over = 0
Integ = 1273
Chips
ns
Max abs(Min,Max-Av. delay(15)) vs chip
ns
Chip number/100
Max abs(Min,Max-Av. d
Nent = 1273
Mean = 0.8822
RMS = 0.2527
Under = 0
Over = 0
Integ = 1273
Chips
ns

CMS EMU CSC D16G Prod. Test. Trays 1-149. Group 3. CMU-FNAL

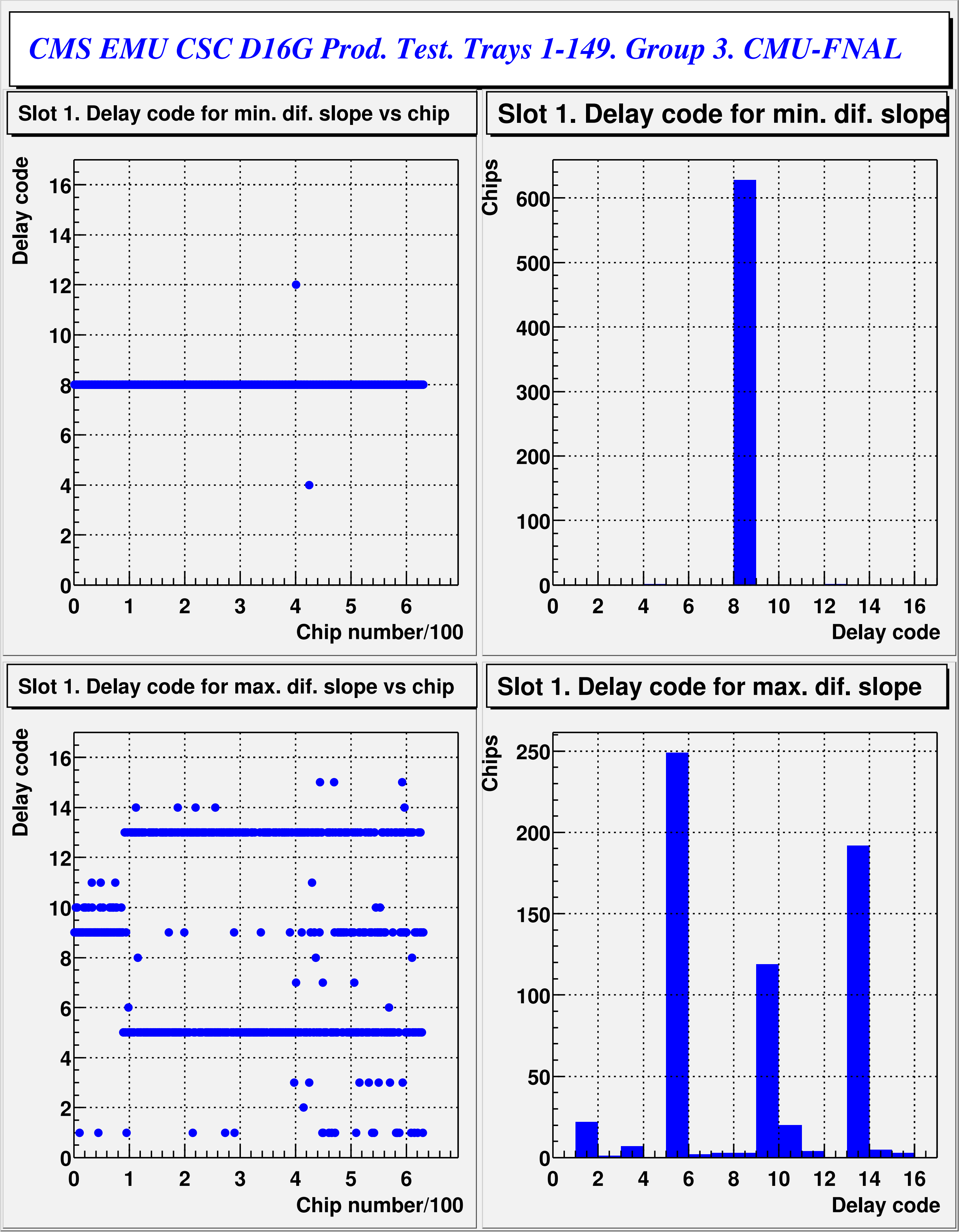



CMS EMU CSC D16G Prod. Test. Trays 1-149. Group 3. CMU-FNAL
Slot 2 Chan. with min. offset vs chip
Ch
Chip number/100
Slot 2 Chan. with min. offset
Chips
Ch
Slot 2 Chan. with max. offset vs chip
Ch
Chip number/100
Slot 2 Chan. with max. offset
Chips
Ch


CMS EMU CSC D16G Prod. Test. Trays 1-149. Group 3. CMU-FNAL
Slot 1 (Min.-Av.) offset vs chip
ns
Chip number/100
Slot 1 (Min.-Av.) offs
Nent = 630
Mean = -0.2159
RMS = 0.08374
Under = 0
Over = 0
Integ = 630
Chips
ns
Slot 1 (Max.-Av.) offset vs chip
ns
Chip number/100
Slot 1 (Max.-Av.) offse
Nent = 630
Mean = 0.2908
RMS = 0.1618
Under = 0
Over = 0
Integ = 630
Chips
ns



CMS EMU CSC D16G Prod. Test. Trays 1-149. Group 3. CMU-FNAL
Slot 2 (Min.-Av.) offset vs chip
ns
Chip number/100
Slot 2 (Min.-Av.) offs
Nent = 643
Mean = -0.2142
RMS = 0.08098
Under = 0
Over = 0
Integ = 643
Chips
ns
Slot 2 (Max.-Av.) offset vs chip
ns
Chip number/100
Slot 2 (Max.-Av.) offse
Nent = 643
Mean = 0.2592
RMS = 0.131
Under = 0
Over = 0
Integ = 643
Chips
ns


CMS EMU CSC D16G Prod. Test. Trays 1-149. Group 3. CMU-FNAL
Slot 1. Delay code for min. dif. slope vs chip
Delay code
Chip number/100
Slot 1. Delay code for min. dif. slope
Chips
Delay code
Slot 1. Delay code for max. dif. slope vs chip
Delay code
Chip number/100
Slot 1. Delay code for max. dif. slope
Chips
Delay code

CMS EMU CSC D16G Prod. Test. Trays 1-149. Group 3. CMU-FNAL