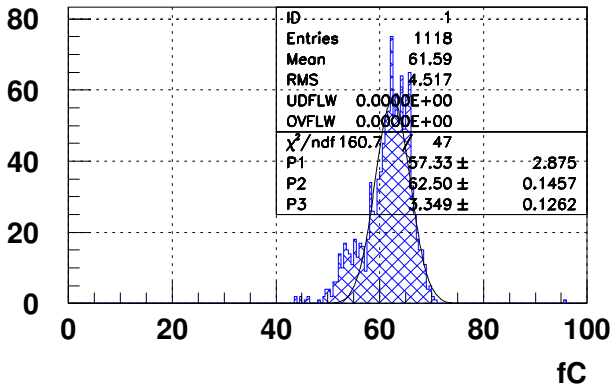
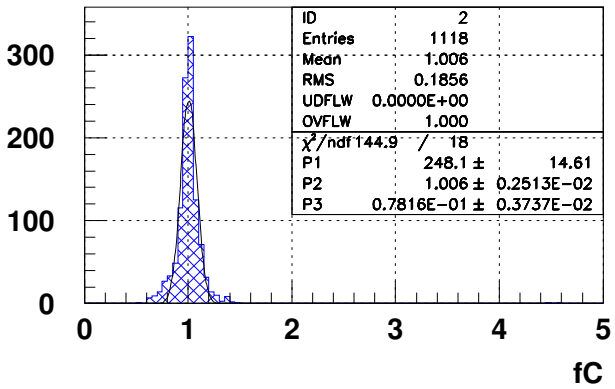
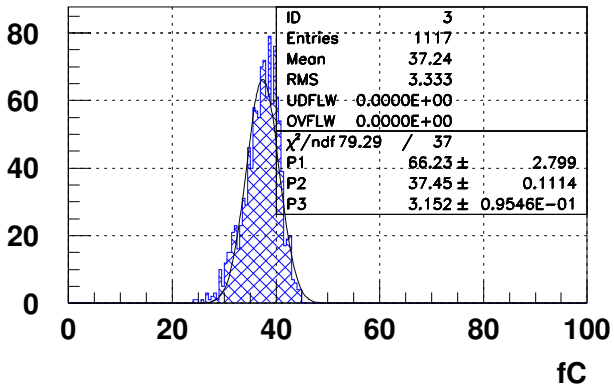
Chip CMP16F Cdet=0 pF (FNAL Stand) May-Aug 2000



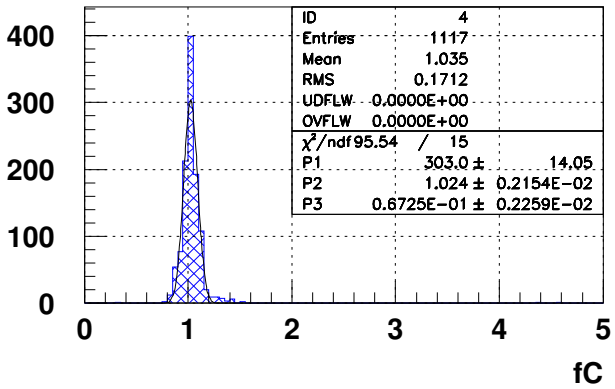
Threshold per channel, Ud=1000 mV



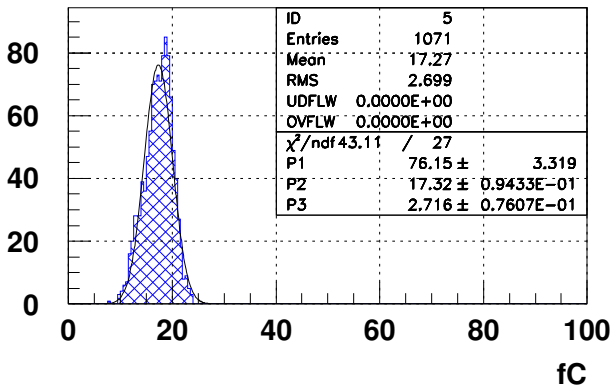
Noise per channel, Ud=1000 mV



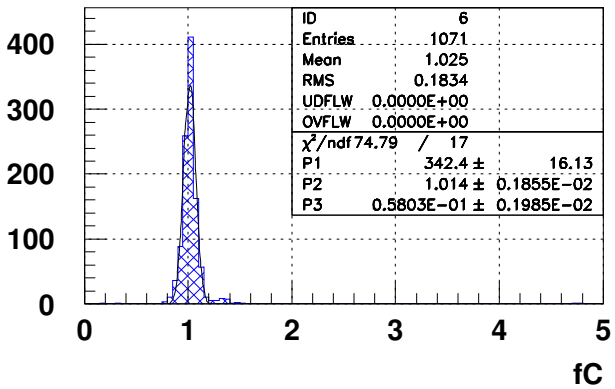
Threshold per channel, Ud=1200 mV



Noise per channel, Ud=1200 mV

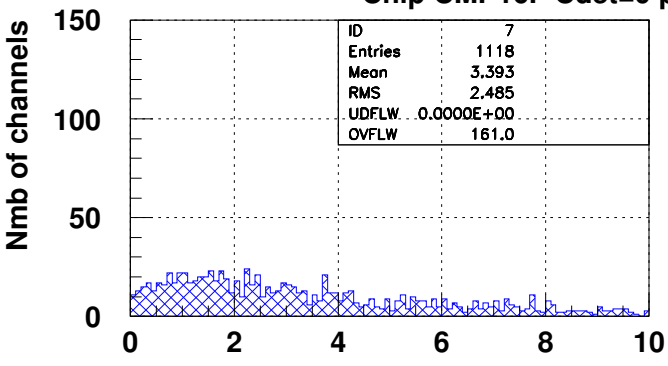


Threshold per channel, Ud=1400 mV

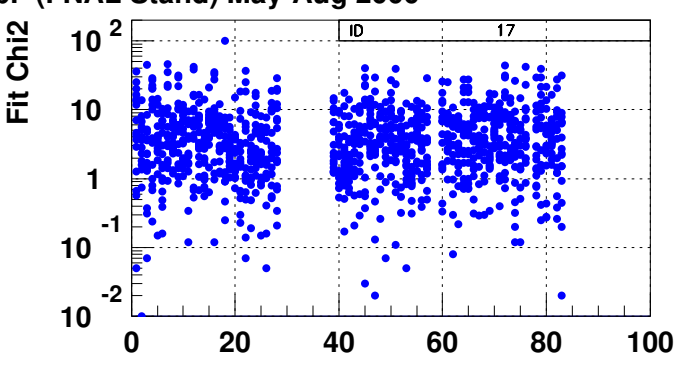


Noise per channel, Ud=1400 mV

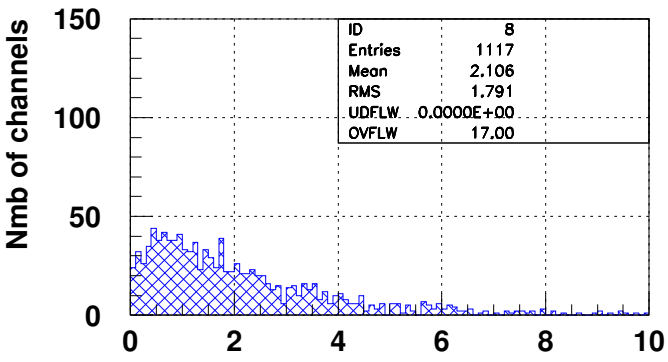
Chip CMP16F Cdet=0 pF (FNAL Stand) May-Aug 2000



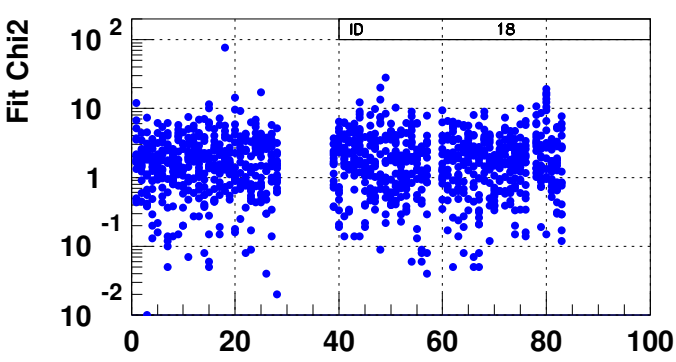
Fit Chi2 per channel, Ud=1000 mV



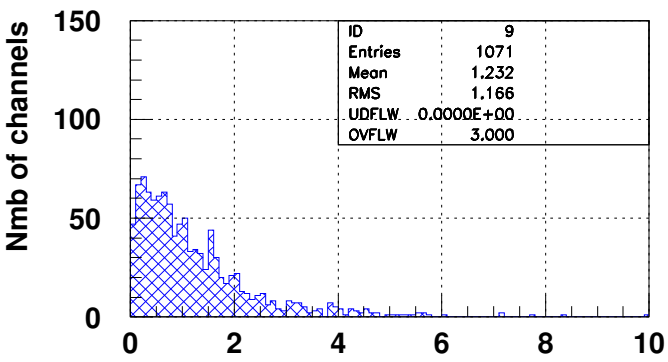
Fit Chi2 vs board number



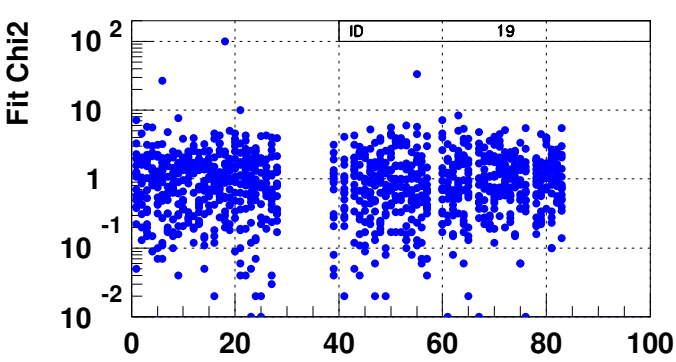
Fit Chi2 per channel, Ud=1200 mV



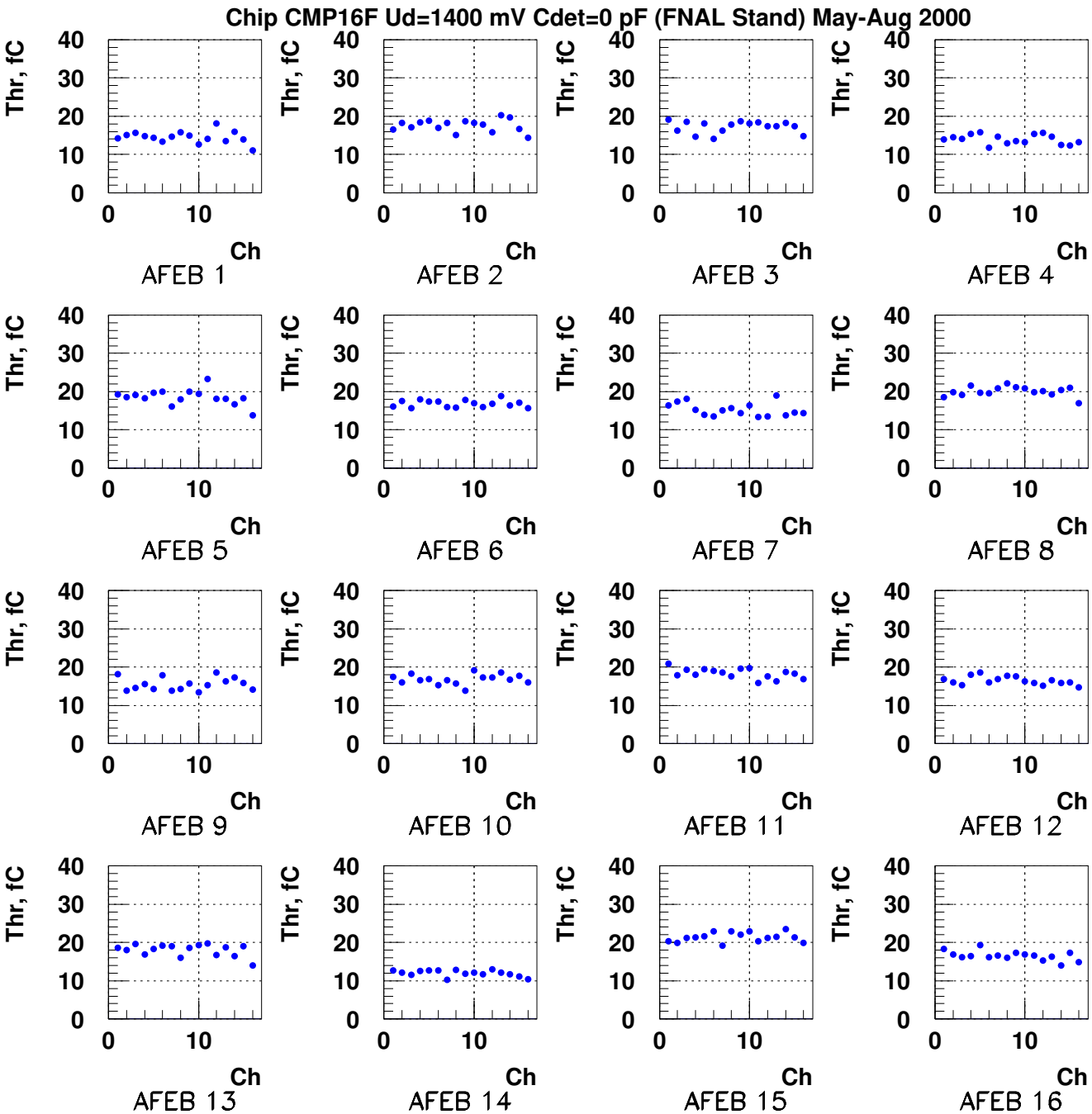
Fit Chi2 vs board number

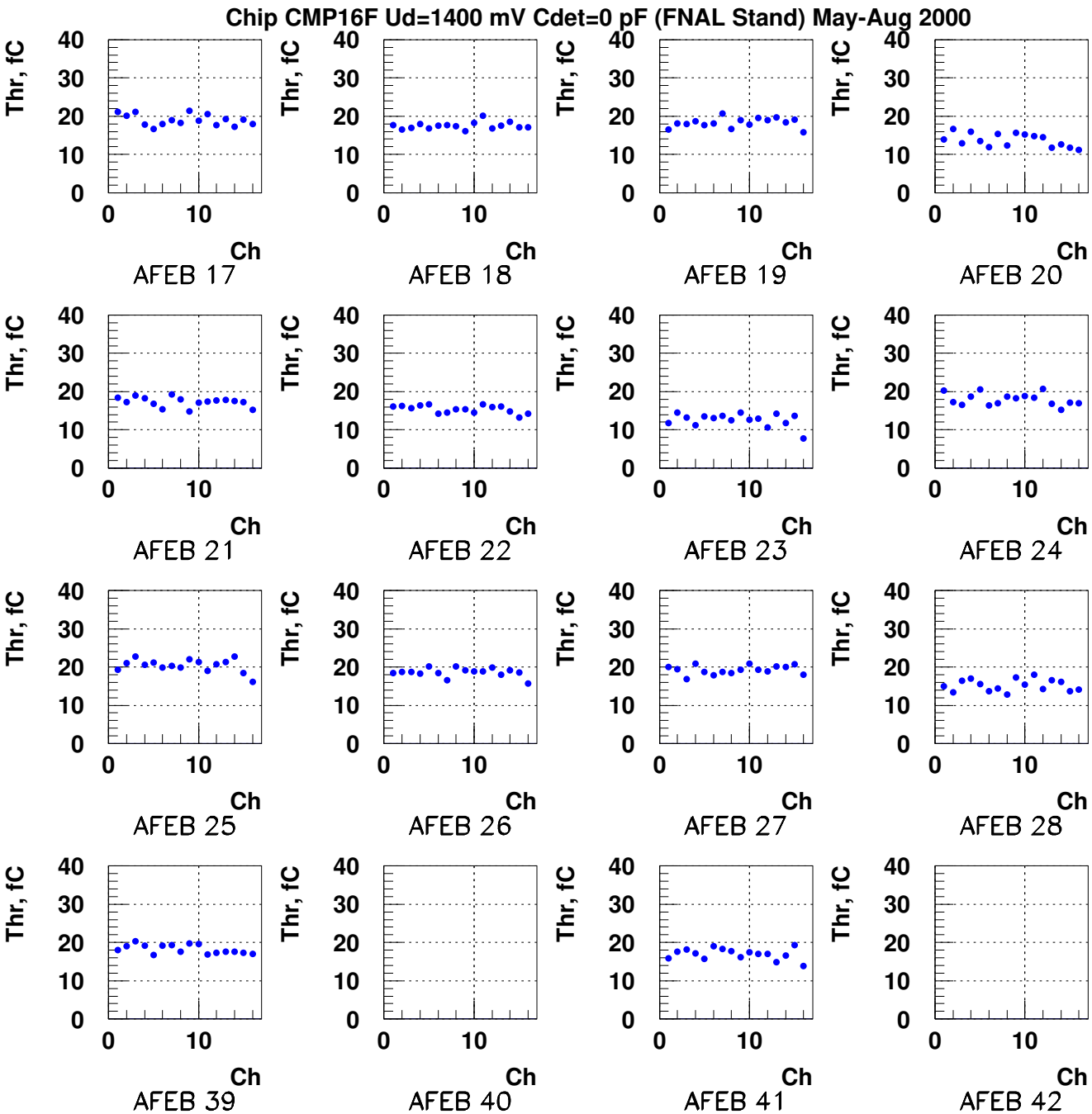


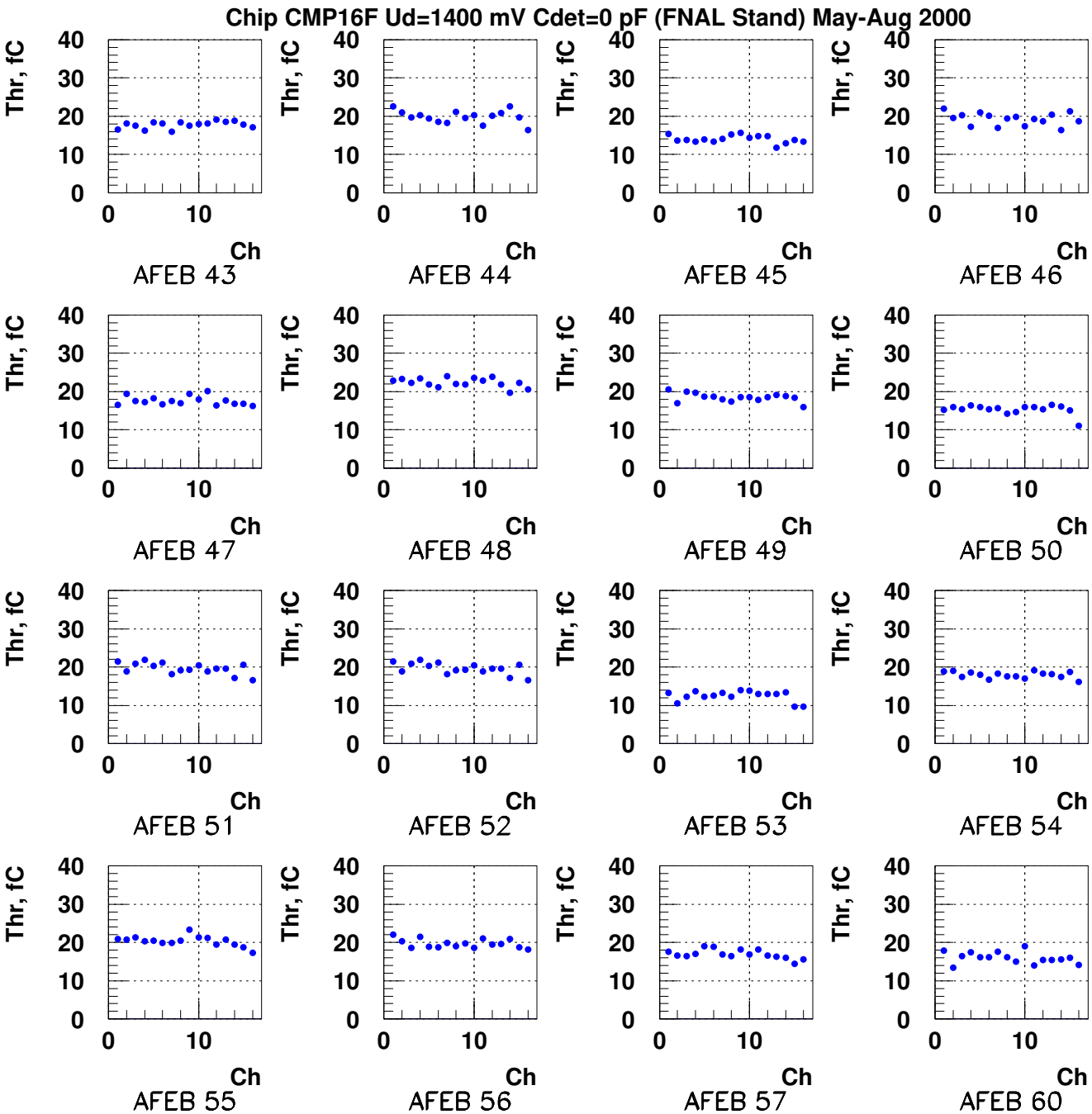
Fit Chi2 per channel, Ud=1400 mV

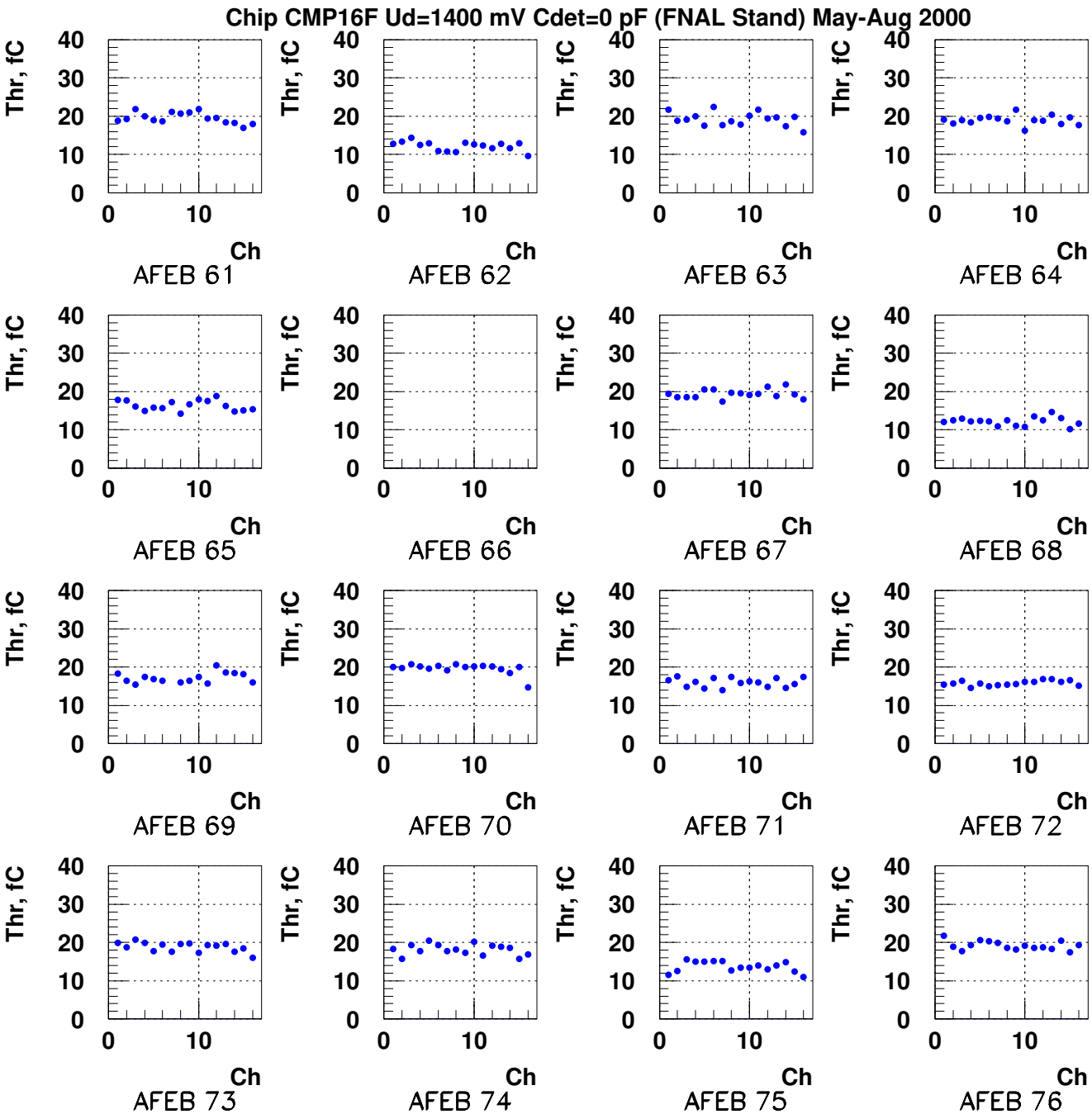


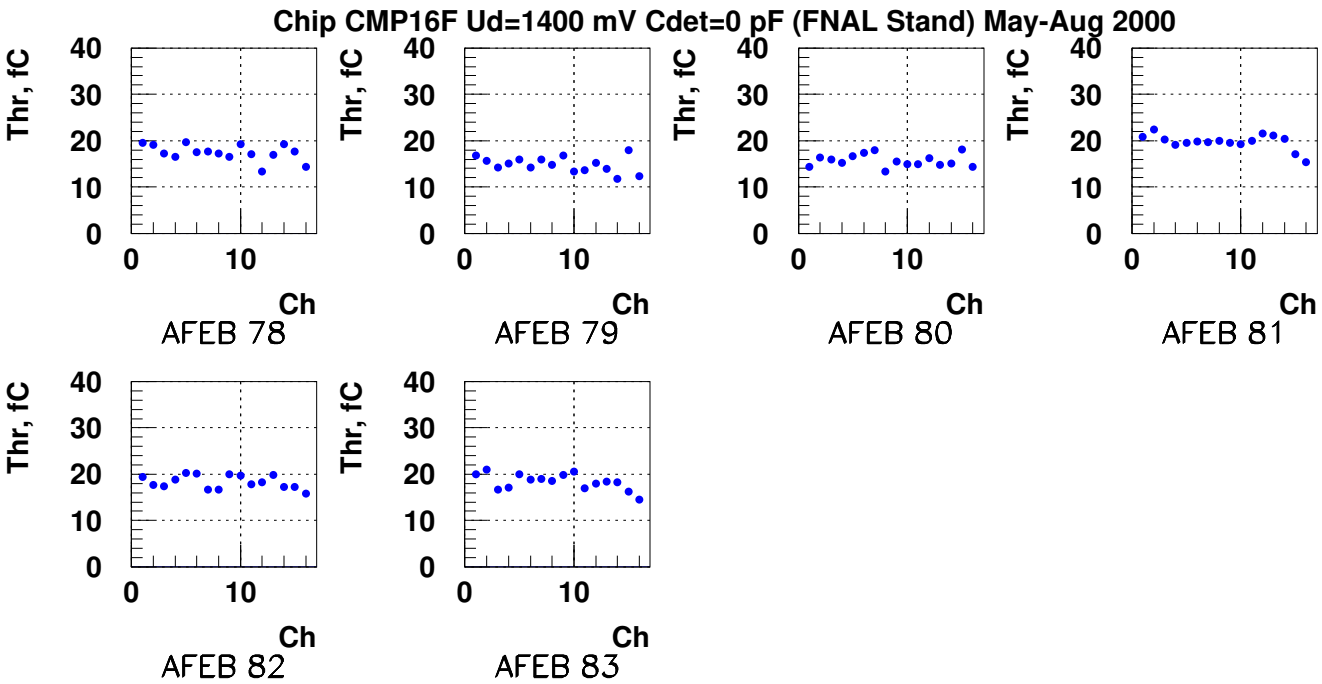
Fit Chi2 vs board number

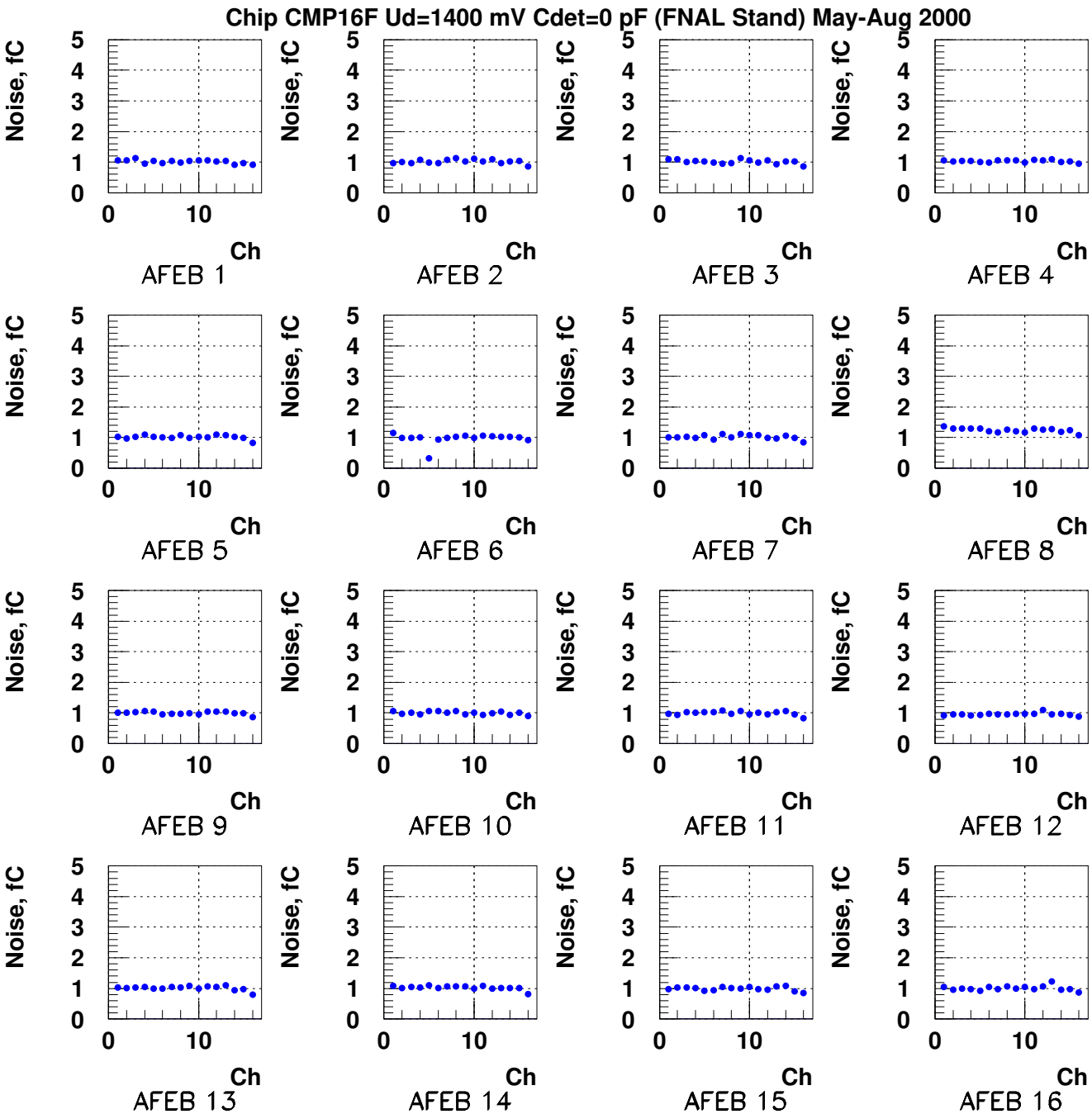

Chip CMP16F Ud=1400 mV Cdet=0 pF (FNAL Stand) May-Aug 2000
Thr, fC
Ch
AFEB 1
AFEB 2
AFEB 3
AFEB 4
AFEB 5
AFEB 6
AFEB 7
AFEB 8
AFEB 9
AFEB 10
AFEB 11
AFEB 12
AFEB 13
AFEB 14
AFEB 15
AFEB 16

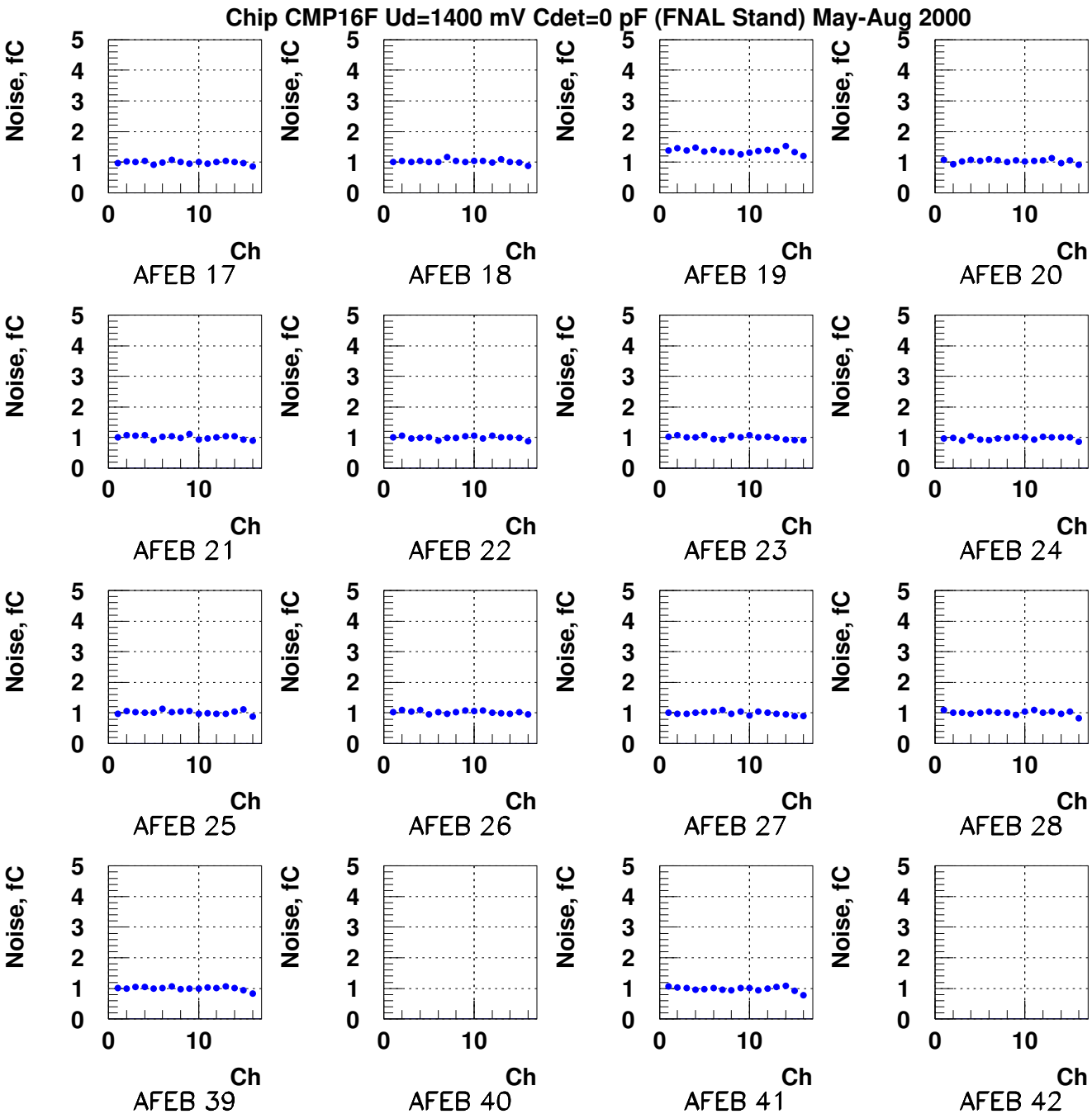

Chip CMP16F Ud=1400 mV Cdet=0 pF (FNAL Stand) May-Aug 2000
Thr, fC
Ch
AFEB 17
AFEB 18
AFEB 19
AFEB 20
AFEB 21
AFEB 22
AFEB 23
AFEB 24
AFEB 25
AFEB 26
AFEB 27
AFEB 28
AFEB 39
AFEB 40
AFEB 41
AFEB 42

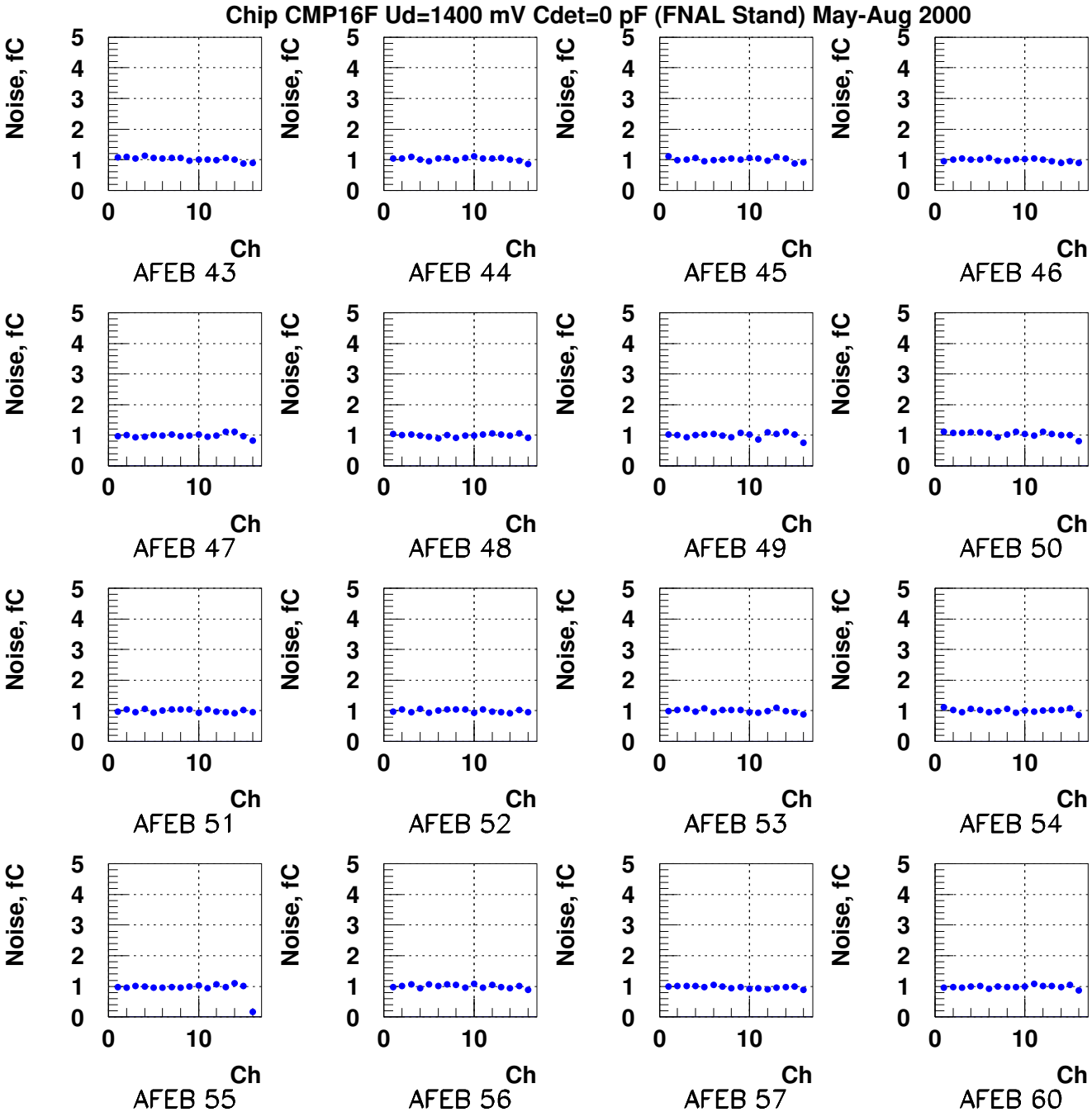

Chip CMP16F Ud=1400 mV Cdet=0 pF (FNAL Stand) May-Aug 2000
Thr, fC
Ch
AFEB 43
AFEB 44
AFEB 45
AFEB 46
AFEB 47
AFEB 48
AFEB 49
AFEB 50
AFEB 51
AFEB 52
AFEB 53
AFEB 54
AFEB 55
AFEB 56
AFEB 57
AFEB 60

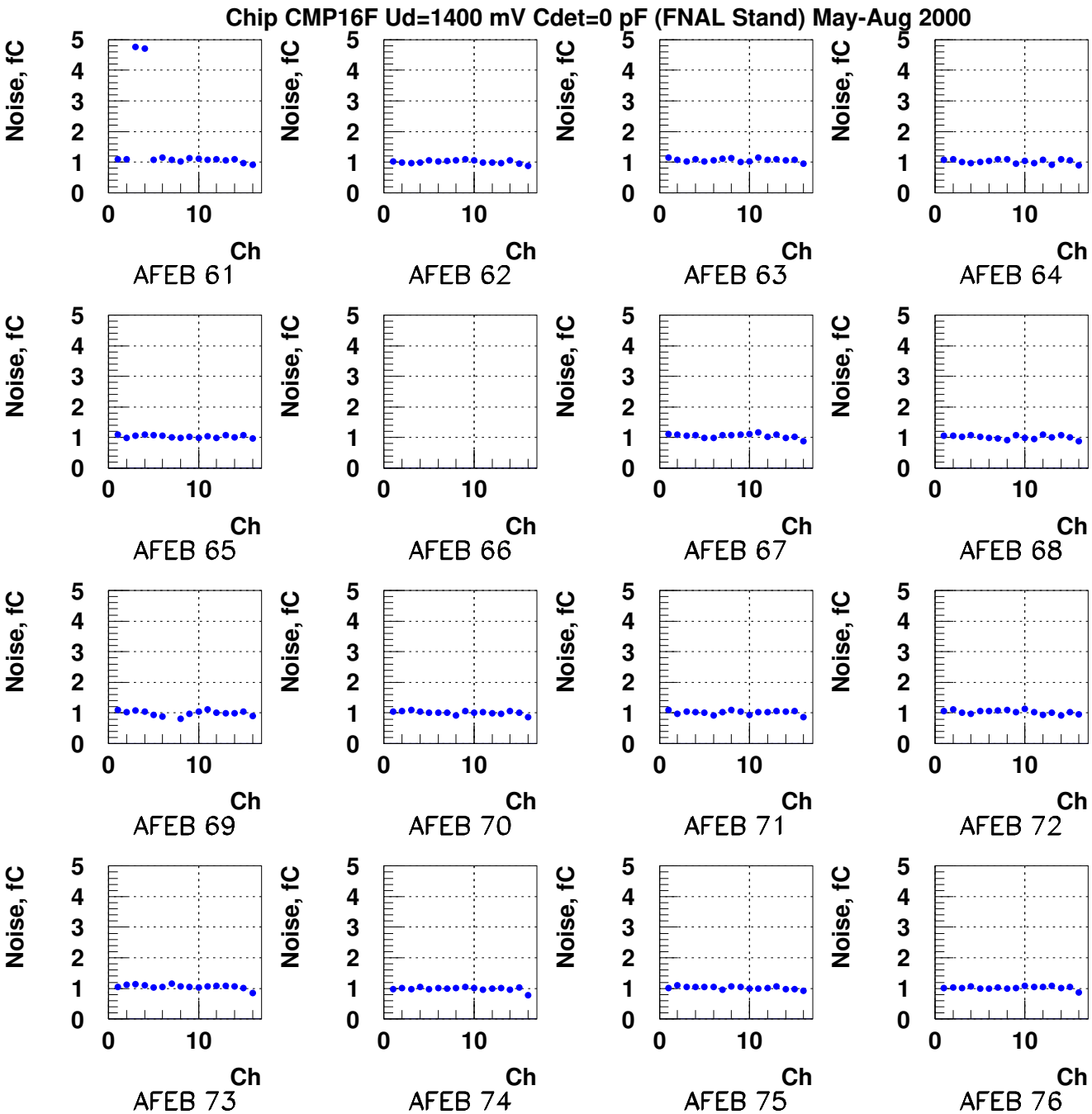

Chip CMP16F Ud=1400 mV Cdet=0 pF (FNAL Stand) May-Aug 2000
Thr, fC
Ch
AFEB 61
AFEB 62
AFEB 63
AFEB 64
AFEB 65
AFEB 66
AFEB 67
AFEB 68
AFEB 69
AFEB 70
AFEB 71
AFEB 72
AFEB 73
AFEB 74
AFEB 75
AFEB 76

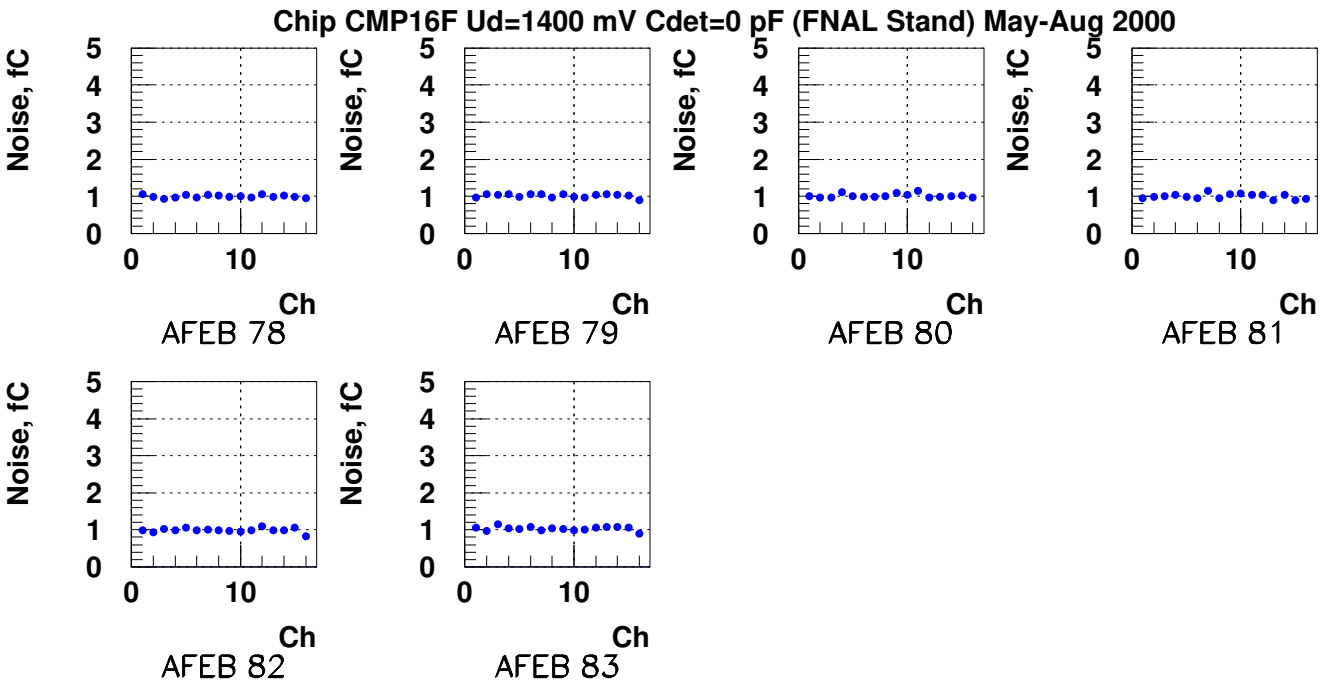

Chip CMP16F Ud=1400 mV Cdet=0 pF (FNAL Stand) May-Aug 2000
Thr, fC
40
30
20
10
0
0
10
Ch
AFEB 78
AFEB 79
AFEB 80
AFEB 81
AFEB 82
AFEB 83

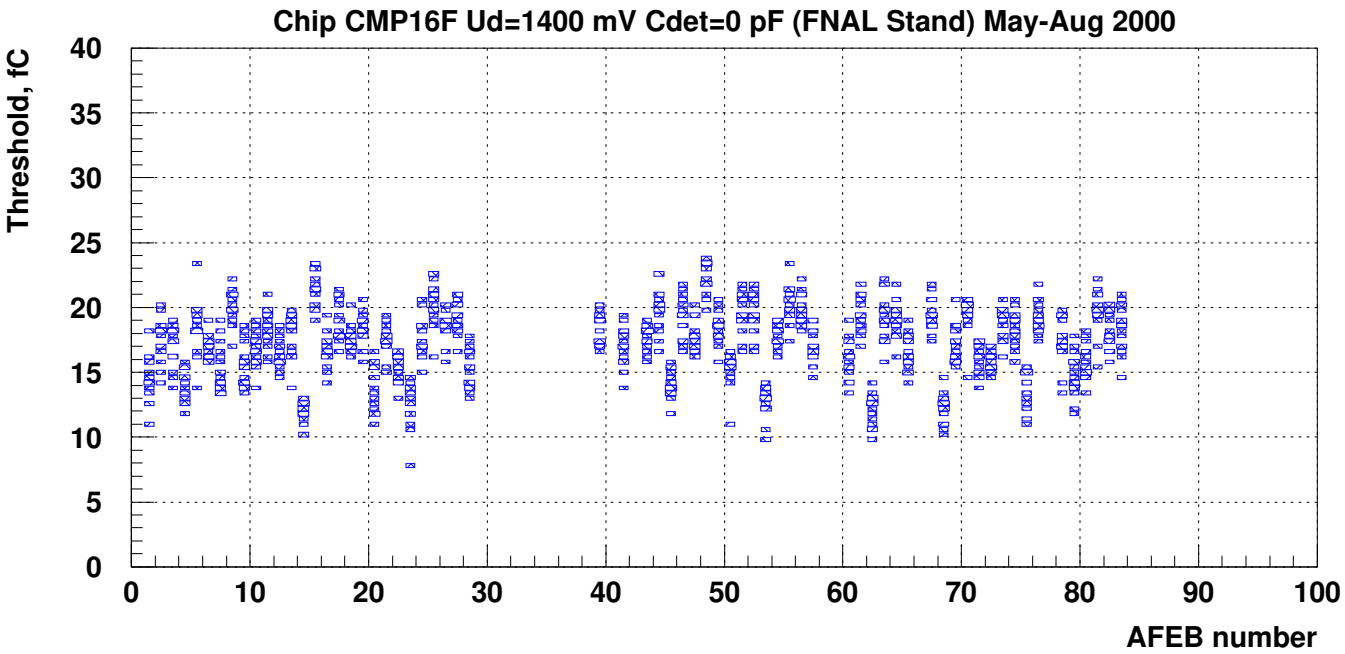

Chip CMP16F Ud=1400 mV Cdet=0 pF (FNAL Stand) May-Aug 2000
Noise, fC
Ch
AFEB 1
AFEB 2
AFEB 3
AFEB 4
AFEB 5
AFEB 6
AFEB 7
AFEB 8
AFEB 9
AFEB 10
AFEB 11
AFEB 12
AFEB 13
AFEB 14
AFEB 15
AFEB 16


Chip CMP16F Ud=1400 mV Cdet=0 pF (FNAL Stand) May-Aug 2000
Noise, fC
Ch
AFEB 17
AFEB 18
AFEB 19
AFEB 20
AFEB 21
AFEB 22
AFEB 23
AFEB 24
AFEB 25
AFEB 26
AFEB 27
AFEB 28
AFEB 39
AFEB 40
AFEB 41
AFEB 42

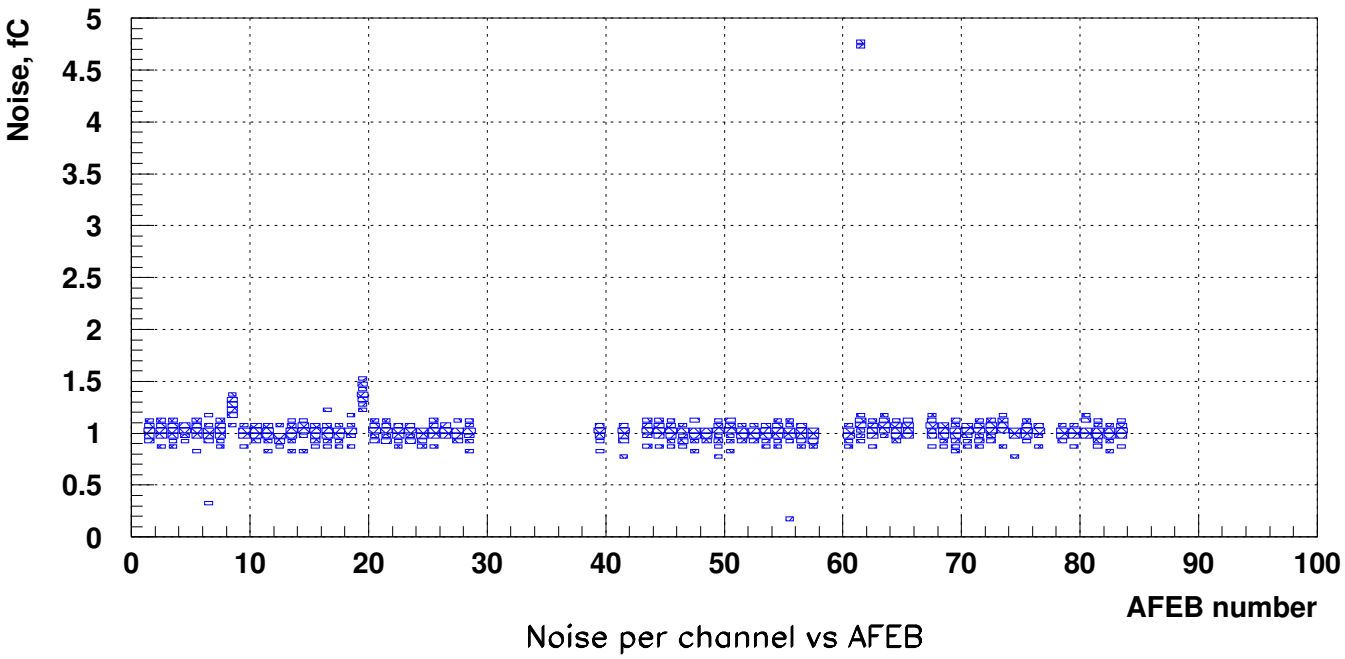

Chip CMP16F Ud=1400 mV Cdet=0 pF (FNAL Stand) May-Aug 2000
Noise, fC
Ch
AFEB 43
AFEB 44
AFEB 45
AFEB 46
AFEB 47
AFEB 48
AFEB 49
AFEB 50
AFEB 51
AFEB 52
AFEB 53
AFEB 54
AFEB 55
AFEB 56
AFEB 57
AFEB 60


Chip CMP16F Ud=1400 mV Cdet=0 pF (FNAL Stand) May-Aug 2000
Noise, fC
Ch
AFEB 61
AFEB 62
AFEB 63
AFEB 64
AFEB 65
AFEB 66
AFEB 67
AFEB 68
AFEB 69
AFEB 70
AFEB 71
AFEB 72
AFEB 73
AFEB 74
AFEB 75
AFEB 76

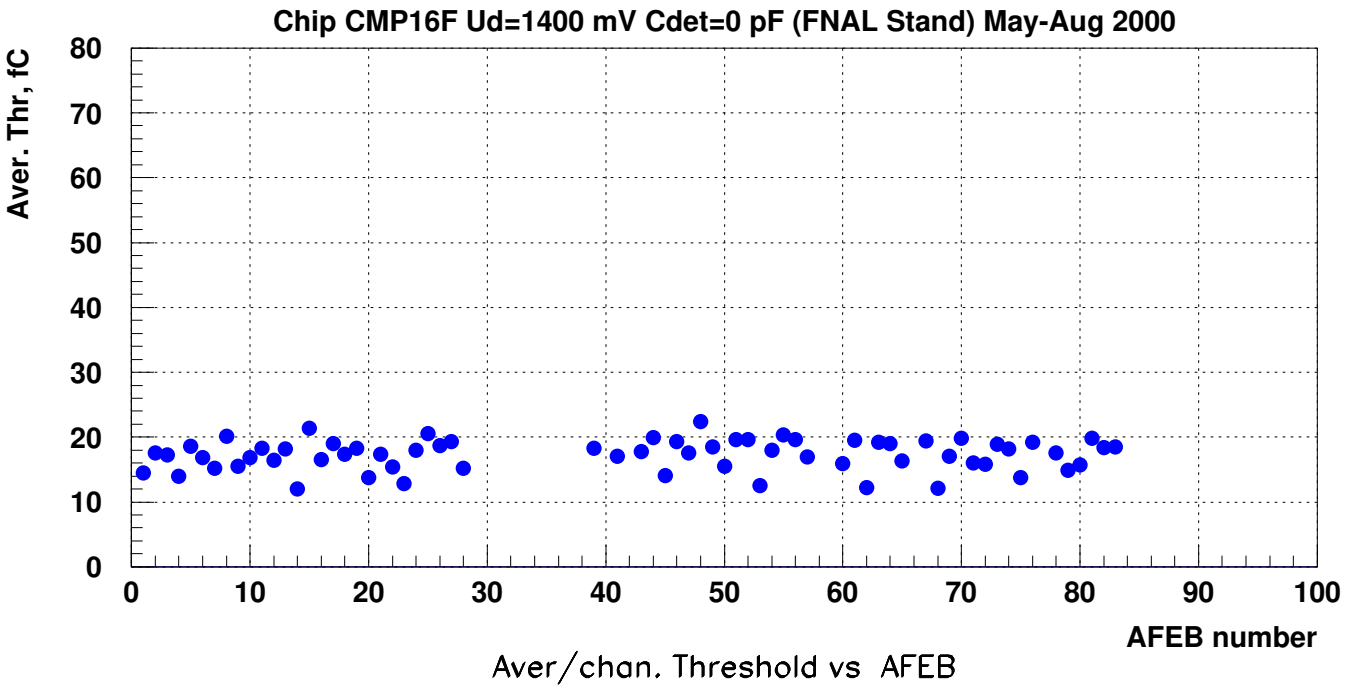

Chip CMP16F Ud=1400 mV Cdet=0 pF (FNAL Stand) May-Aug 2000
Noise, fC
Ch
AFEB 78
AFEB 79
AFEB 80
AFEB 81
AFEB 82
AFEB 83



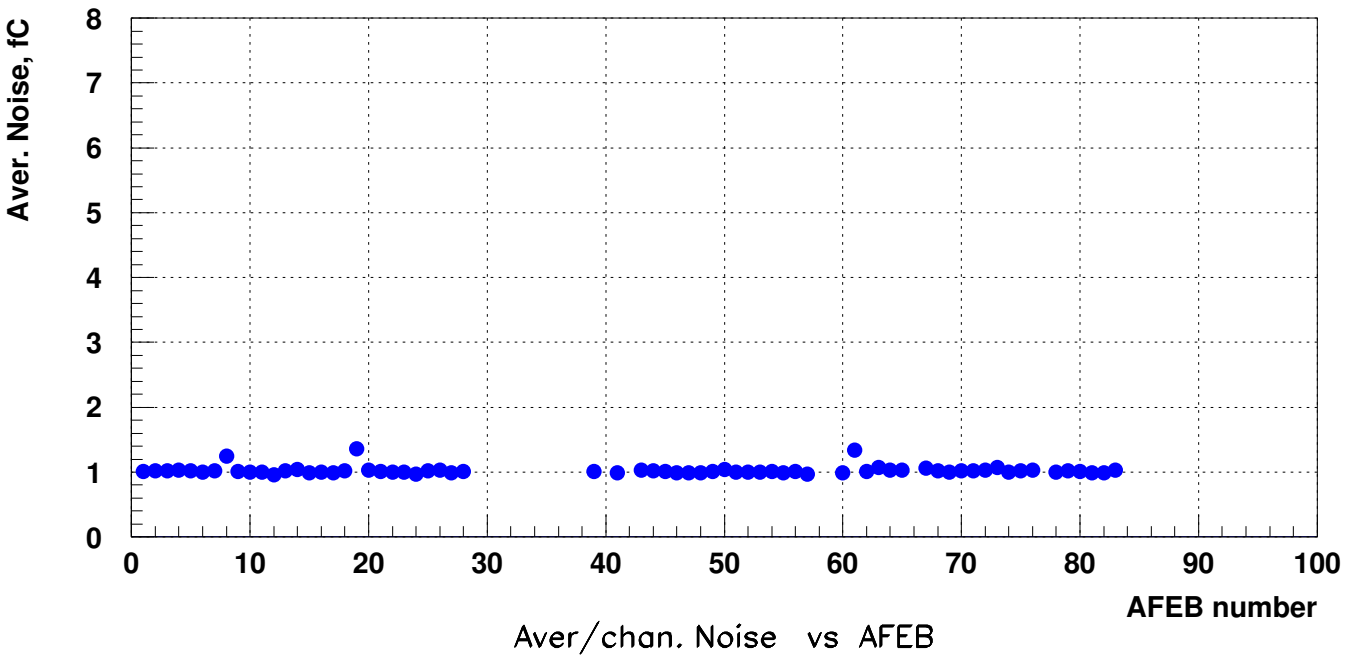
Threshold per channel vs AFEB



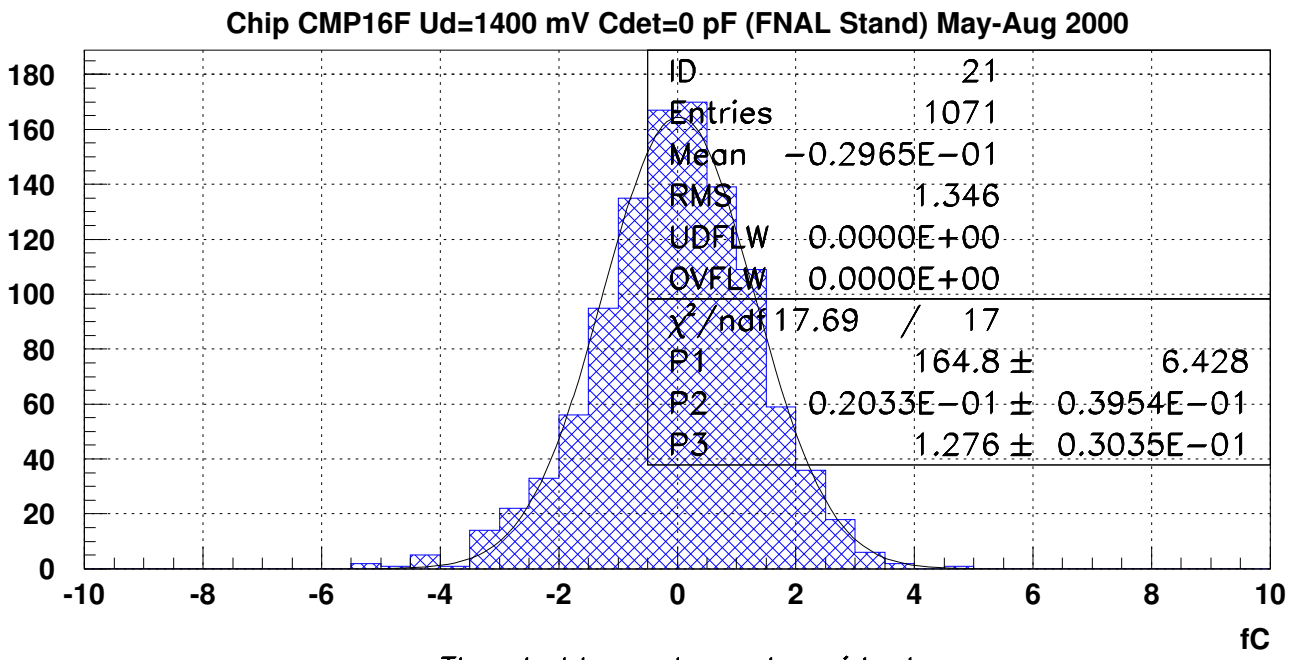
Noise per channel vs AFEB



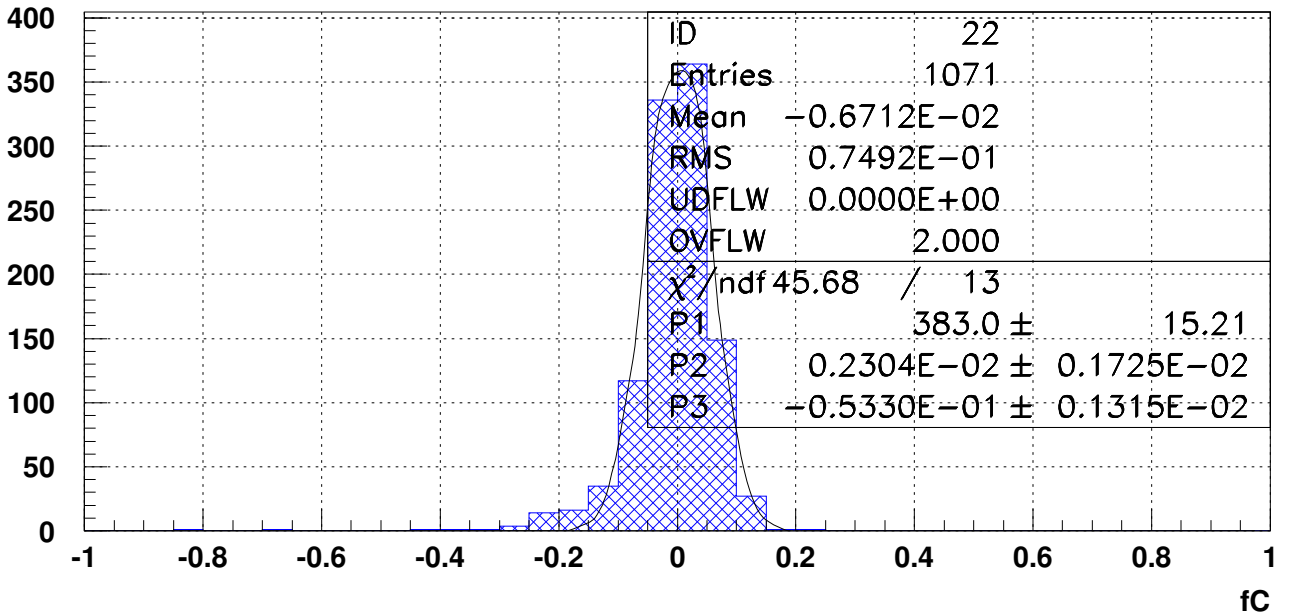
Aver/chan. Threshold vs AFEB



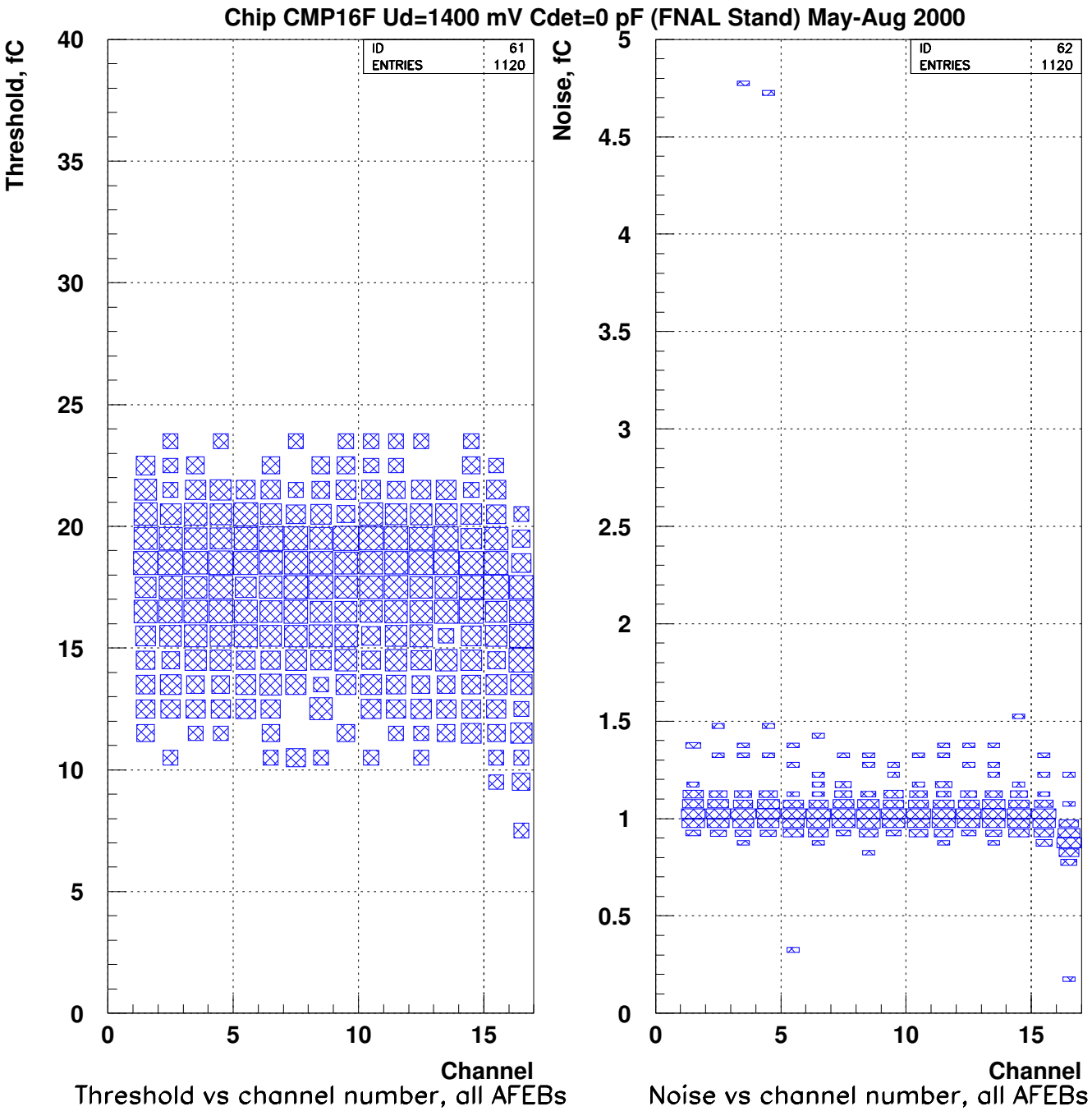
Aver/chan. Noise vs AFEB



Threshold per channel, residual

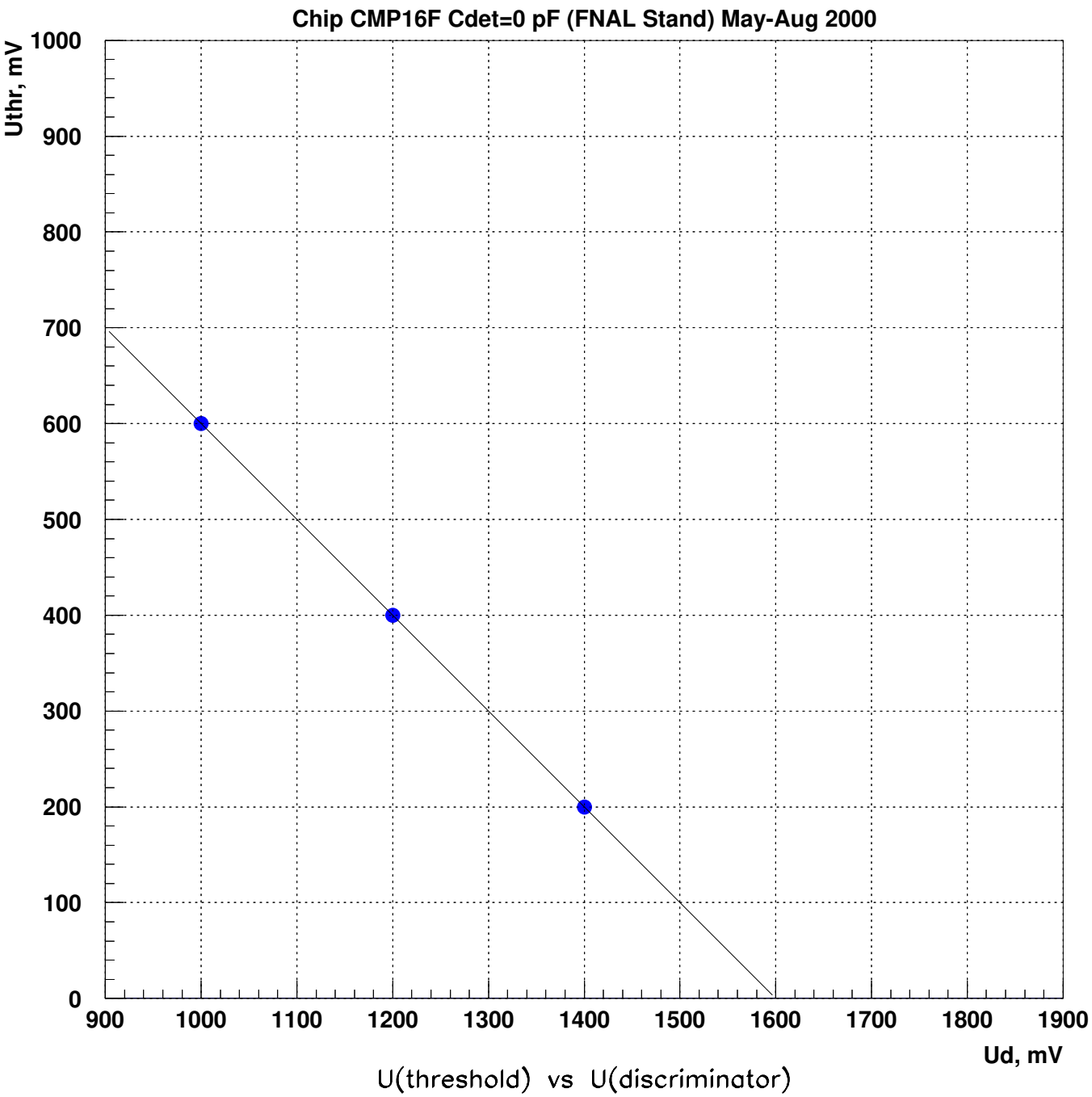


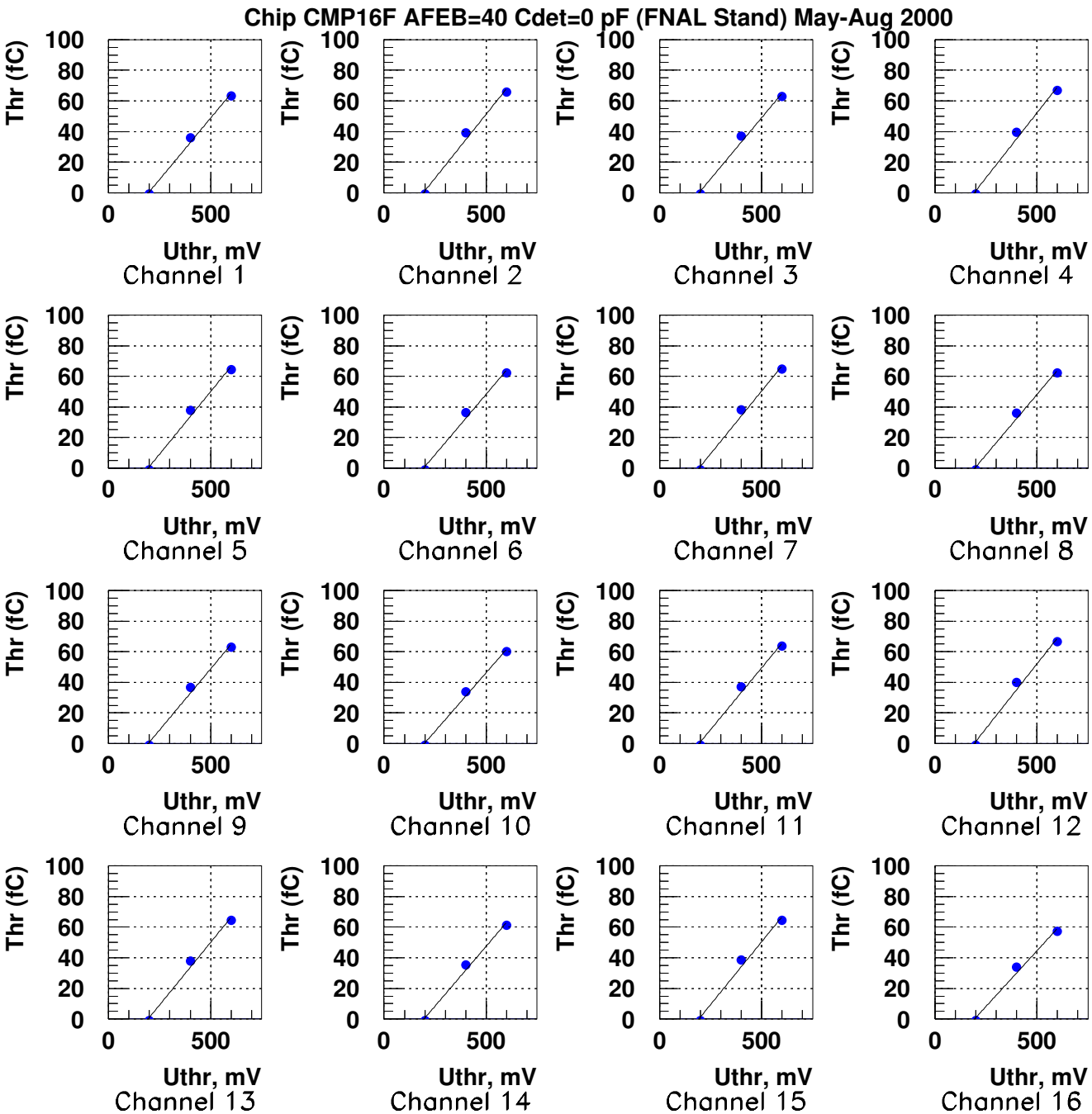
Noise per channel, residual

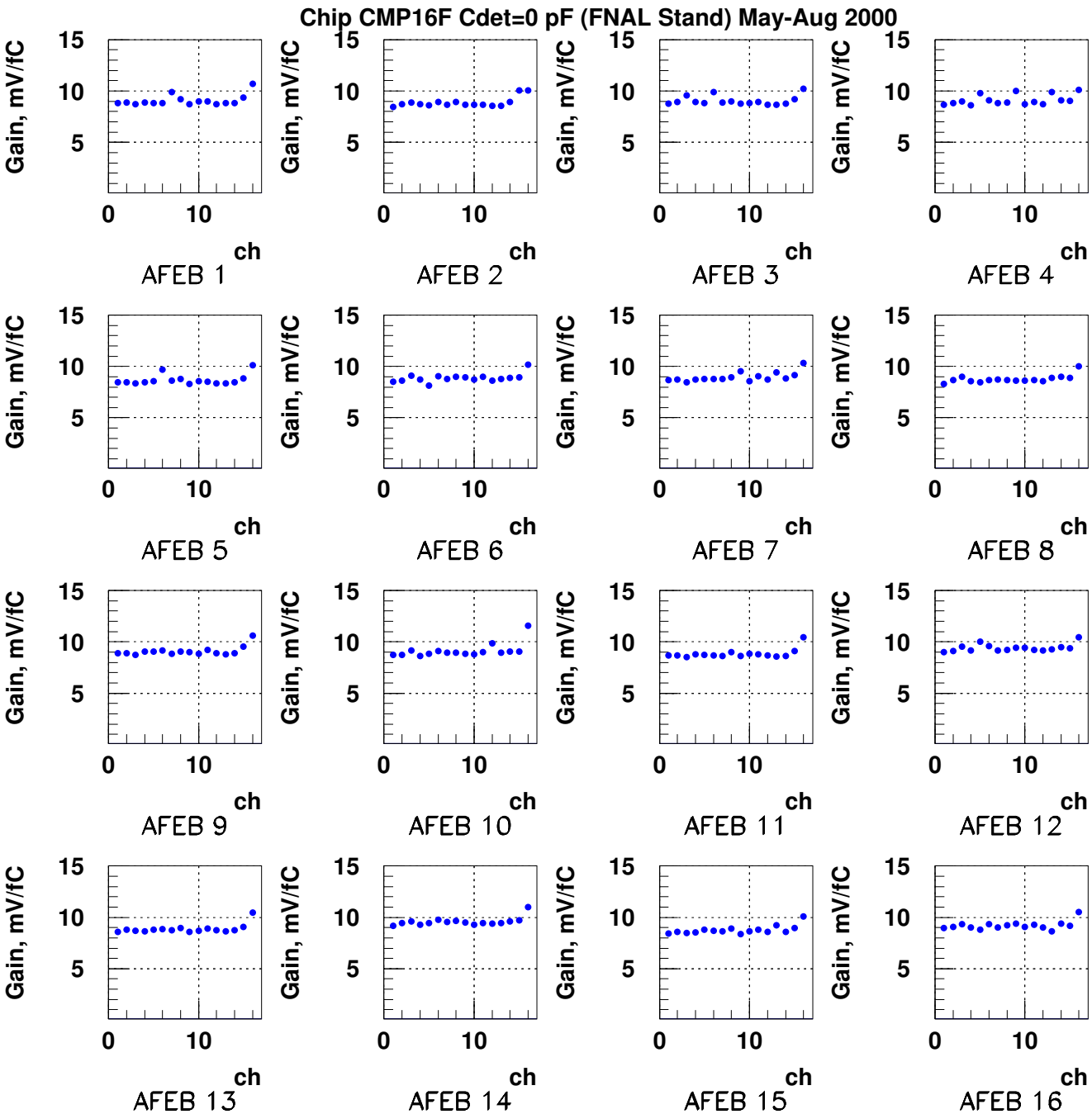


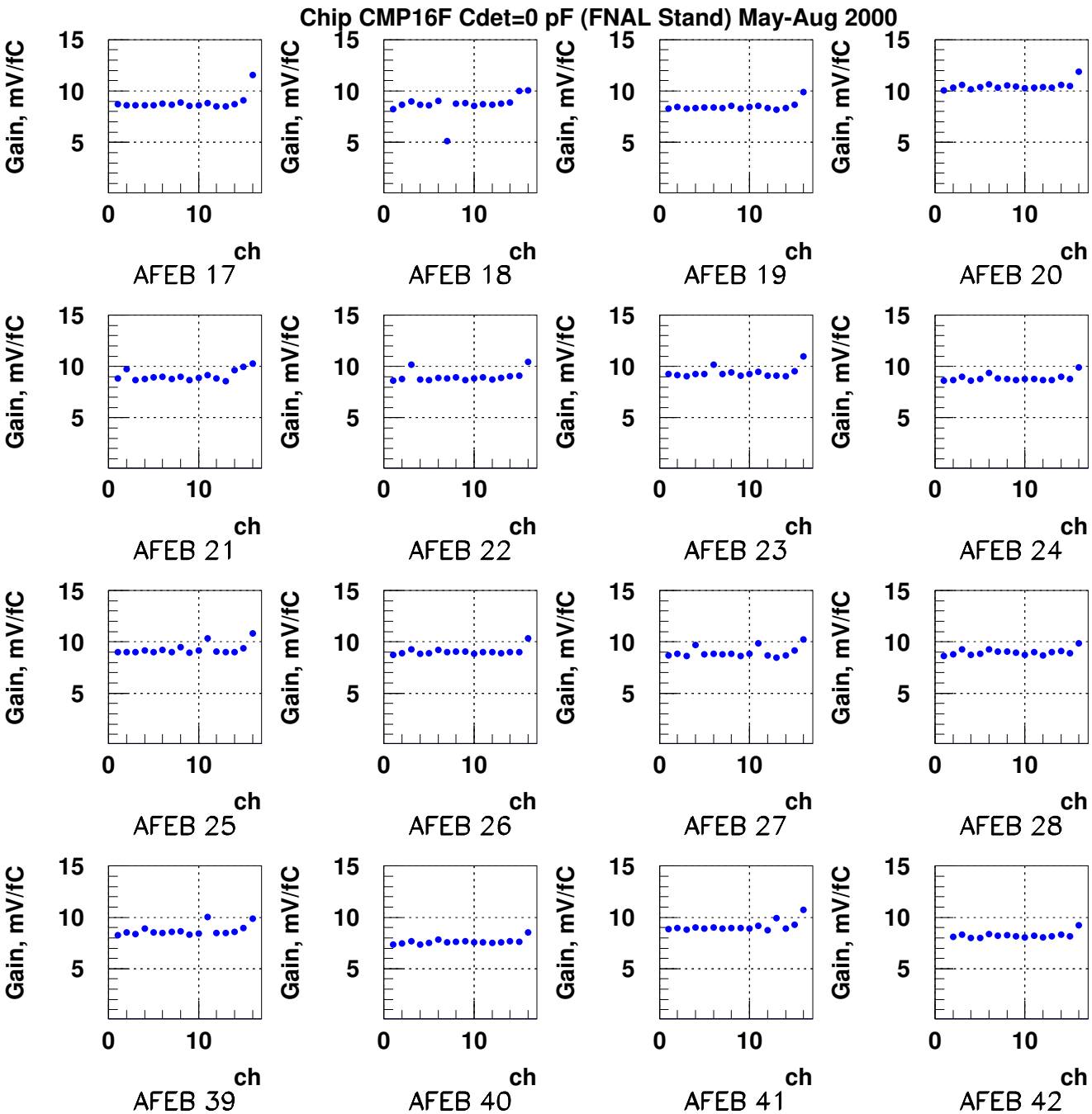
Threshold vs channel number, all AFEBs

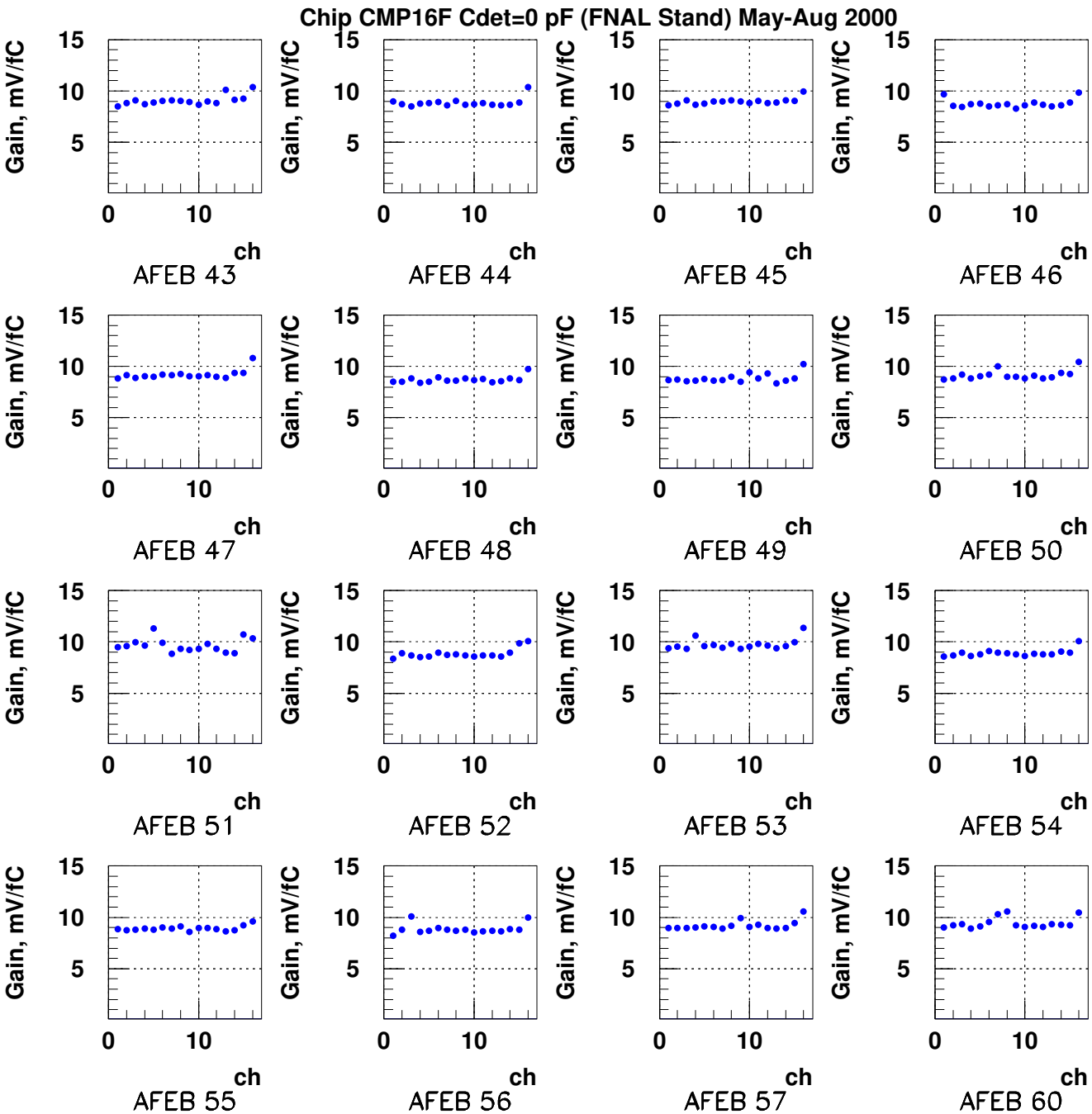
Noise vs channel number, all AFEBs

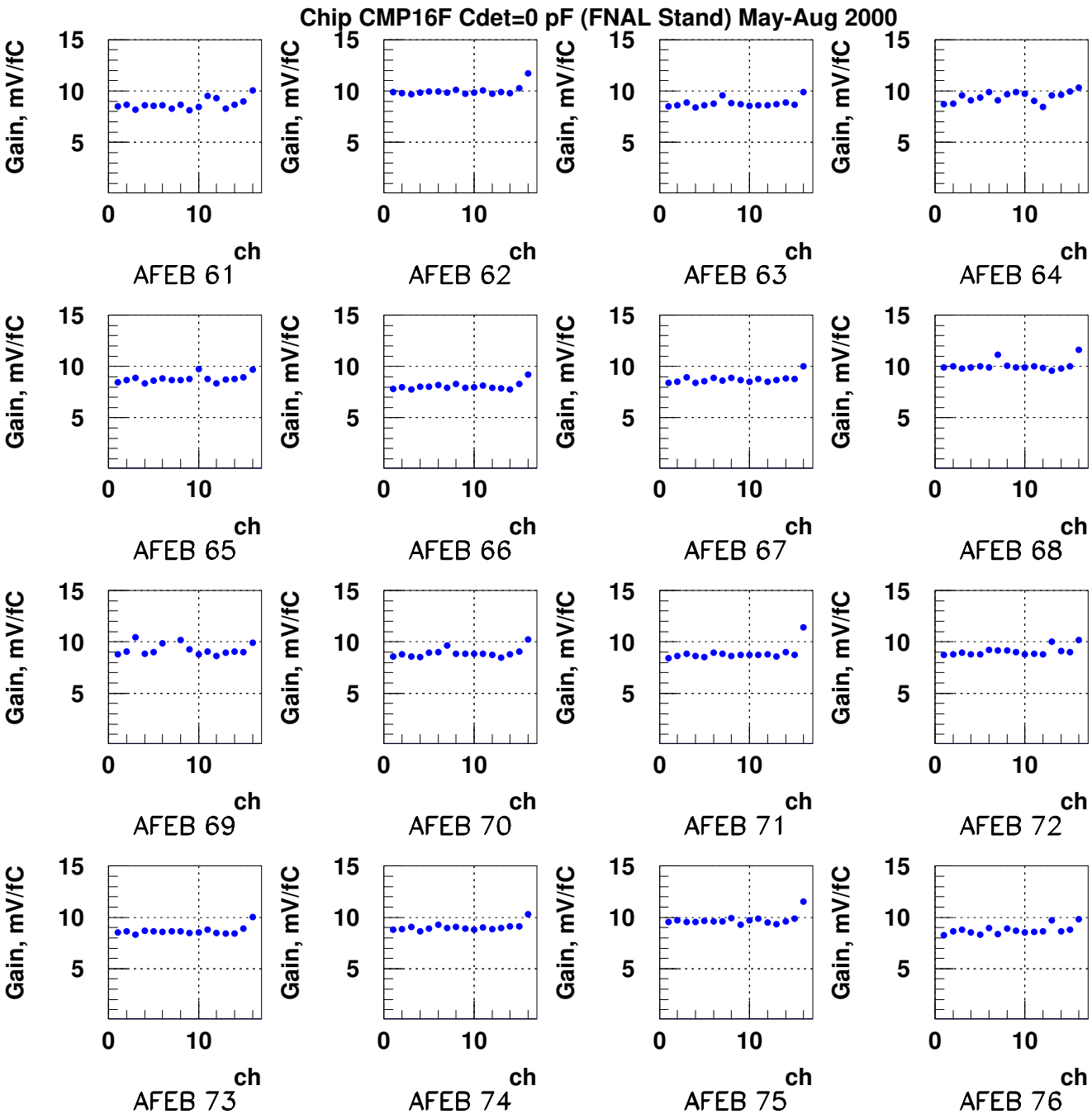

Chip CMP16F Cdet=0 pF (FNAL Stand) May-Aug 2000
Uthr, mV
1000
900
800
700
600
500
400
300
200
100
0
900
1000
1100
1200
1300
1400
1500
1600
1700
1800
1900
Ud, mV
U(threshold) vs U(discriminator)

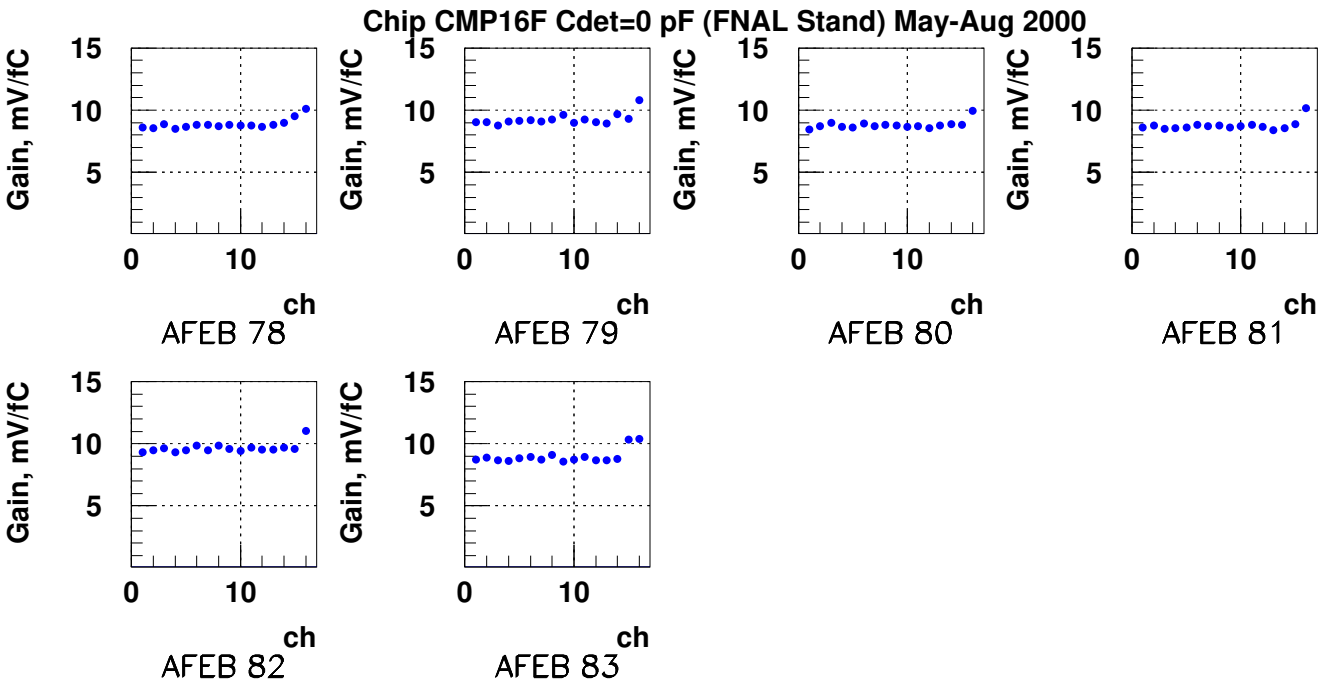

Chip CMP16F AFEB=40 Cdet=0 pF (FNAL Stand) May-Aug 2000
Thr (fC)
Uthr, mV
Channel 1
Channel 2
Channel 3
Channel 4
Channel 5
Channel 6
Channel 7
Channel 8
Channel 9
Channel 10
Channel 11
Channel 12
Channel 13
Channel 14
Channel 15
Channel 16

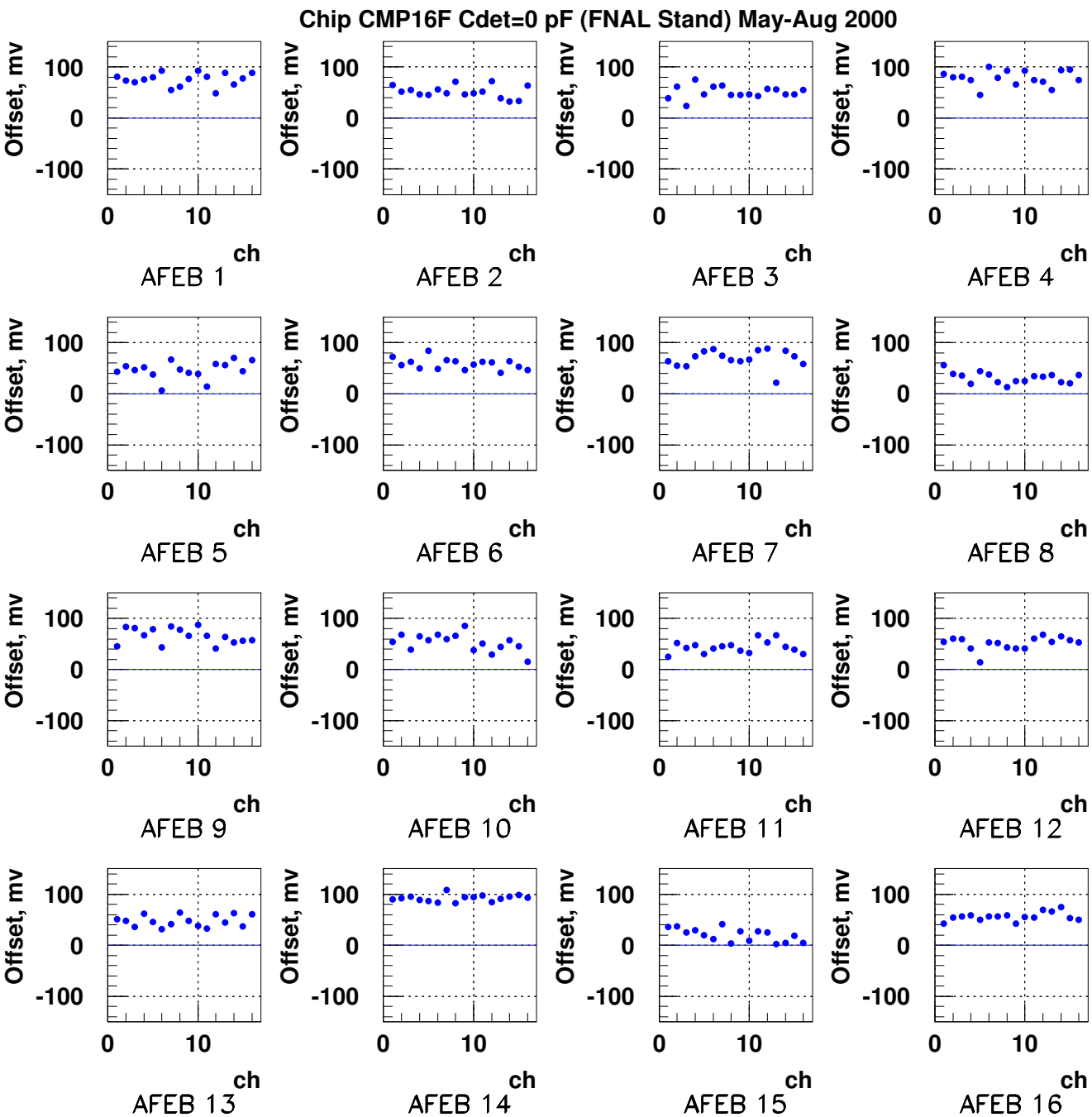

Chip CMP16F Cdet=0 pF (FNAL Stand) May-Aug 2000
Gain, mV/fC
15
10
5
0
10
ch
AFEB 1
AFEB 2
AFEB 3
AFEB 4
AFEB 5
AFEB 6
AFEB 7
AFEB 8
AFEB 9
AFEB 10
AFEB 11
AFEB 12
AFEB 13
AFEB 14
AFEB 15
AFEB 16

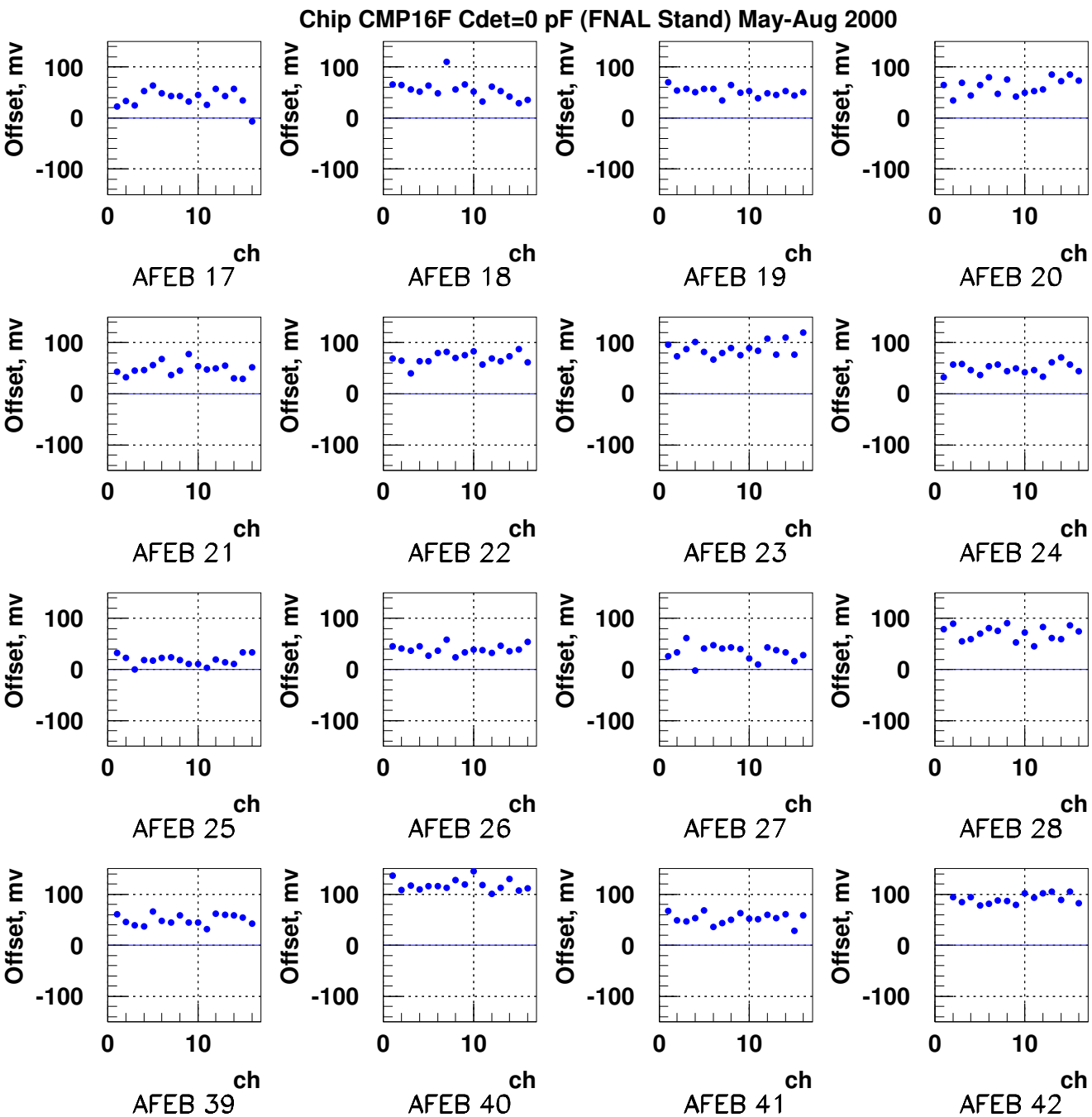

Chip CMP16F Cdet=0 pF (FNAL Stand) May-Aug 2000
Gain, mV/fC
15
10
5
0
10
ch
AFEB 17
AFEB 18
AFEB 19
AFEB 20
AFEB 21
AFEB 22
AFEB 23
AFEB 24
AFEB 25
AFEB 26
AFEB 27
AFEB 28
AFEB 39
AFEB 40
AFEB 41
AFEB 42

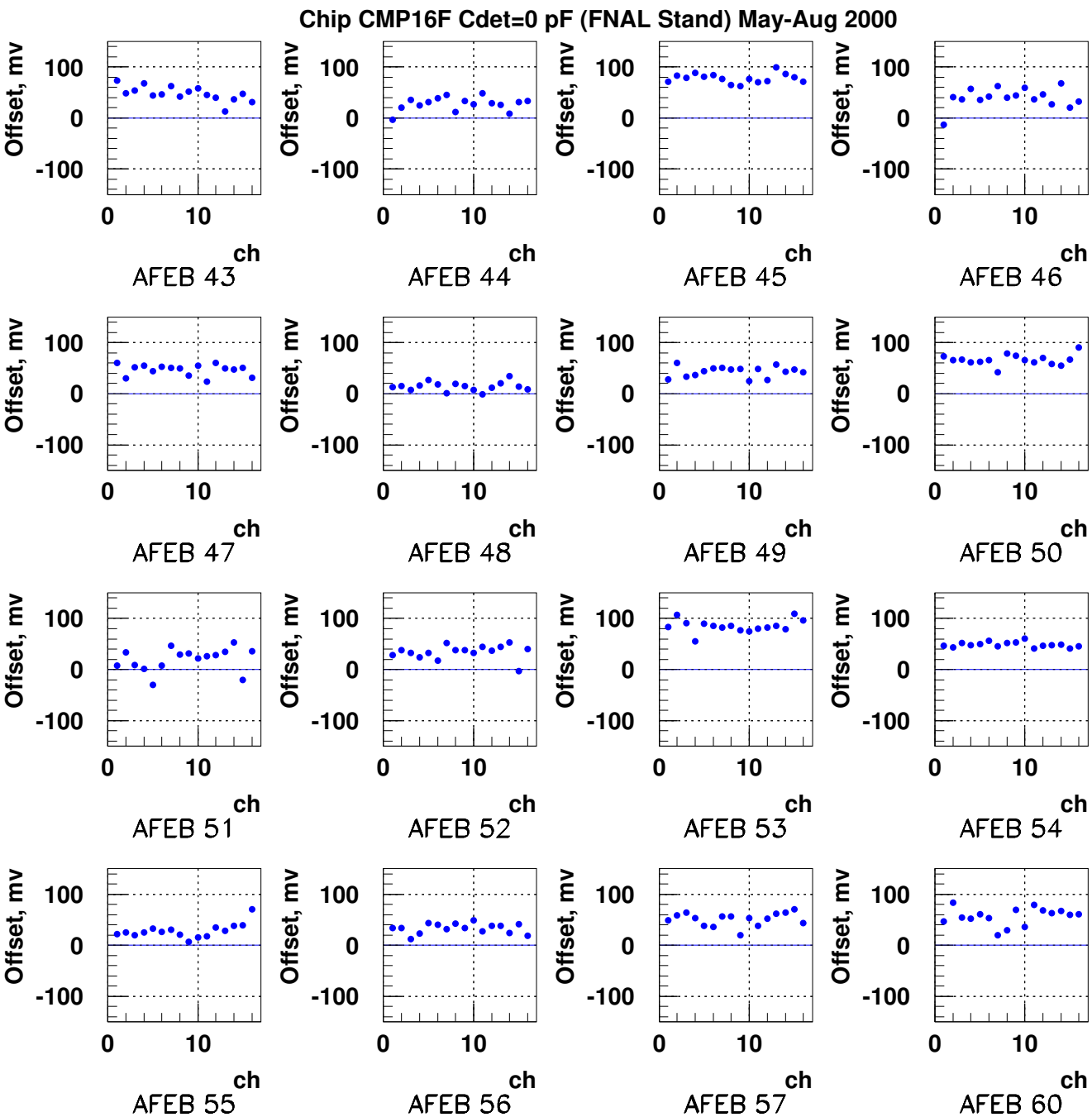

Chip CMP16F Cdet=0 pF (FNAL Stand) May-Aug 2000
Gain, mV/fC
15
10
5
0
10
ch
AFEB 43
AFEB 44
AFEB 45
AFEB 46
AFEB 47
AFEB 48
AFEB 49
AFEB 50
AFEB 51
AFEB 52
AFEB 53
AFEB 54
AFEB 55
AFEB 56
AFEB 57
AFEB 60

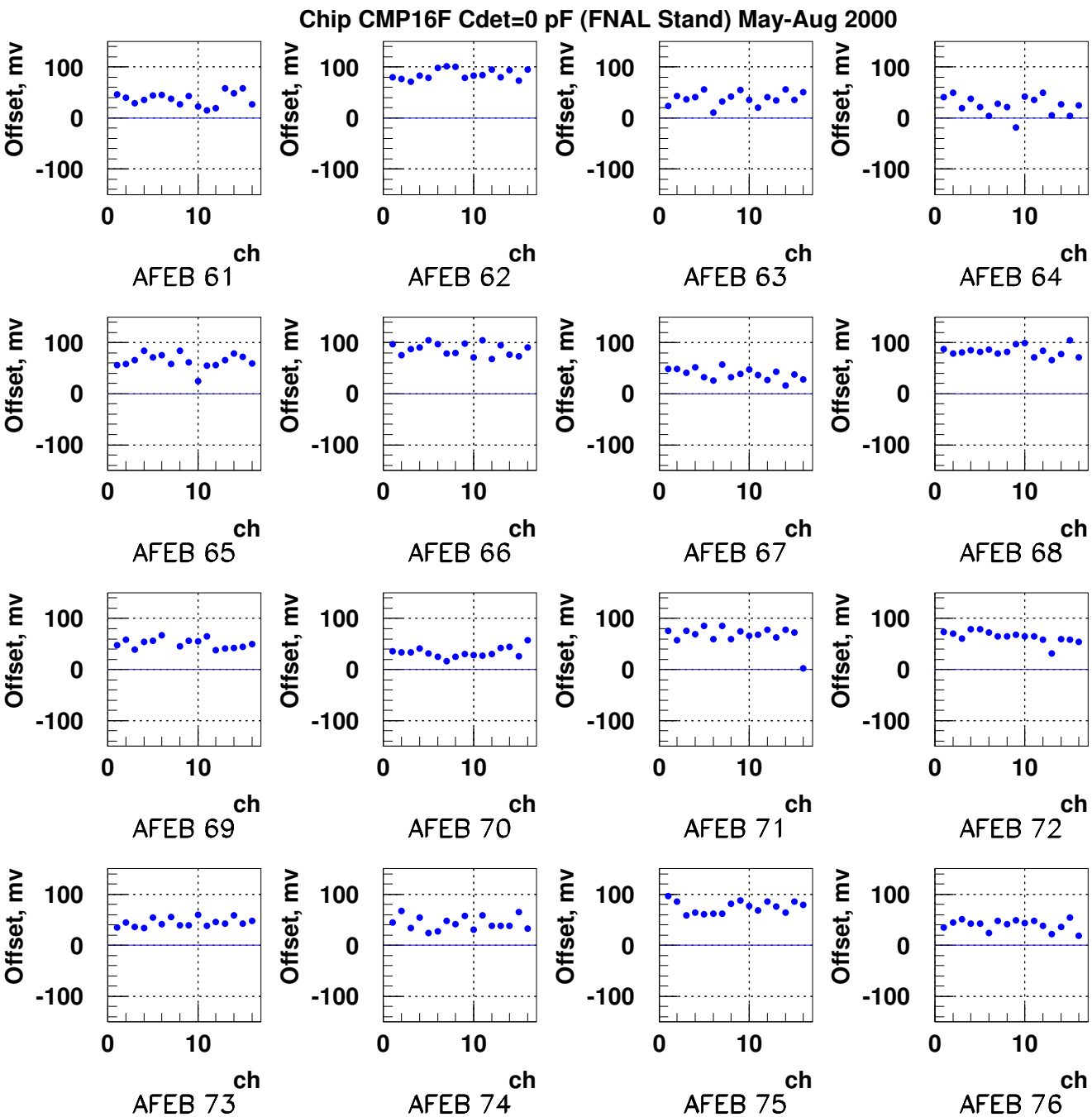

Chip CMP16F Cdet=0 pF (FNAL Stand) May-Aug 2000
Gain, mV/fC
15
10
5
0
10
ch
AFEB 61
AFEB 62
AFEB 63
AFEB 64
AFEB 65
AFEB 66
AFEB 67
AFEB 68
AFEB 69
AFEB 70
AFEB 71
AFEB 72
AFEB 73
AFEB 74
AFEB 75
AFEB 76

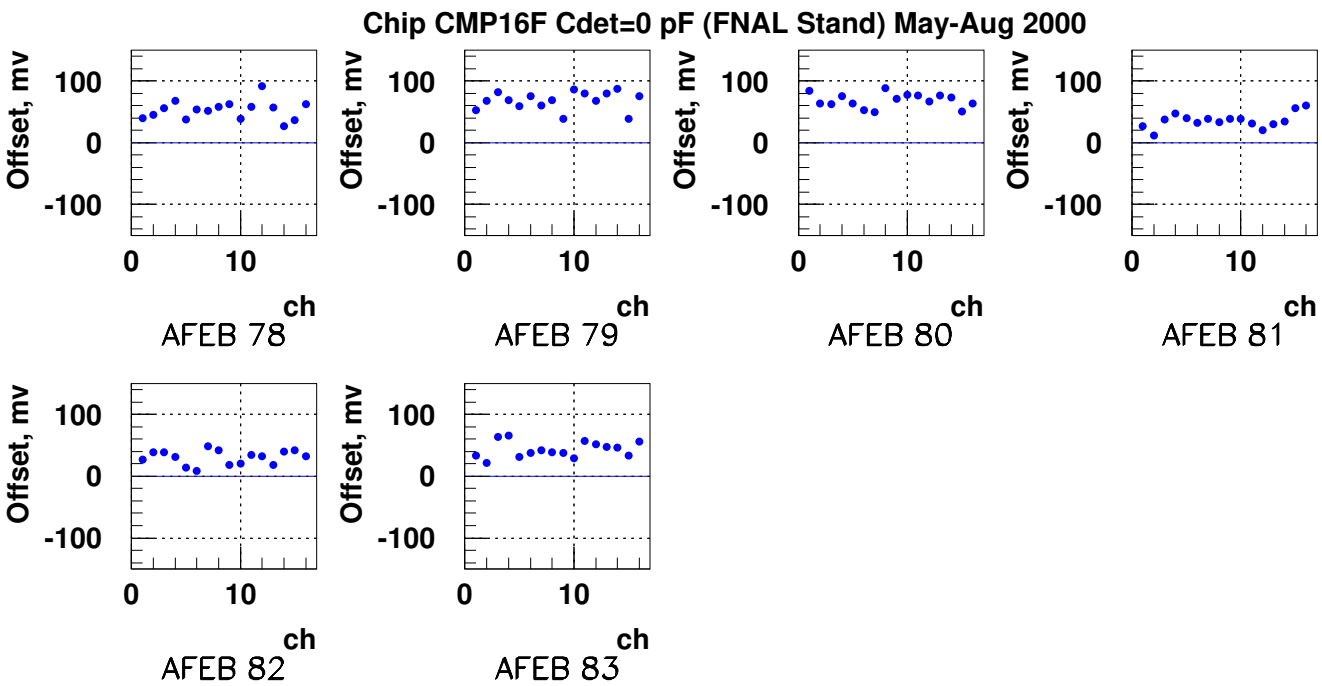

Chip CMP16F Cdet=0 pF (FNAL Stand) May-Aug 2000
Gain, mV/fC
15
10
5
0
10
ch
AFEB 78
AFEB 79
AFEB 80
AFEB 81
AFEB 82
AFEB 83

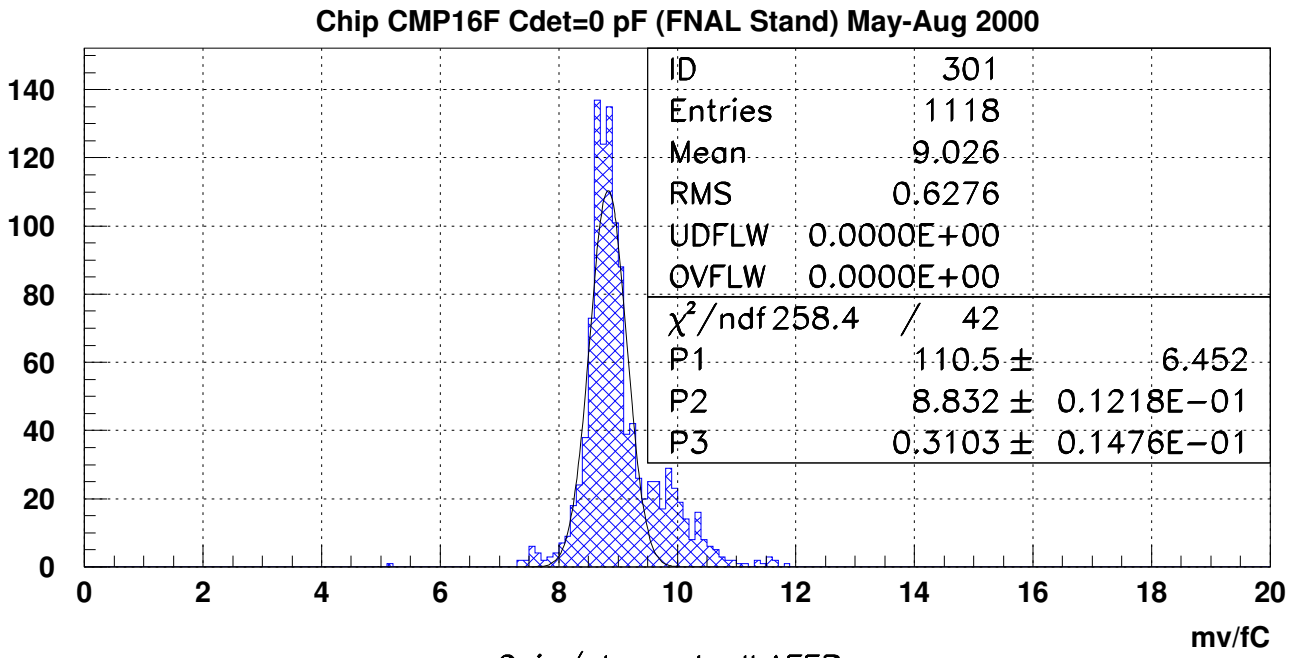

Chip CMP16F Cdet=0 pF (FNAL Stand) May-Aug 2000
Offset, mv
100
0
-100
0
10
ch
AFEB 1
AFEB 2
AFEB 3
AFEB 4
AFEB 5
AFEB 6
AFEB 7
AFEB 8
AFEB 9
AFEB 10
AFEB 11
AFEB 12
AFEB 13
AFEB 14
AFEB 15
AFEB 16


Chip CMP16F Cdet=0 pF (FNAL Stand) May-Aug 2000
Offset, mv
100
0
-100
0
10
ch
AFEB 17
AFEB 18
AFEB 19
AFEB 20
AFEB 21
AFEB 22
AFEB 23
AFEB 24
AFEB 25
AFEB 26
AFEB 27
AFEB 28
AFEB 39
AFEB 40
AFEB 41
AFEB 42

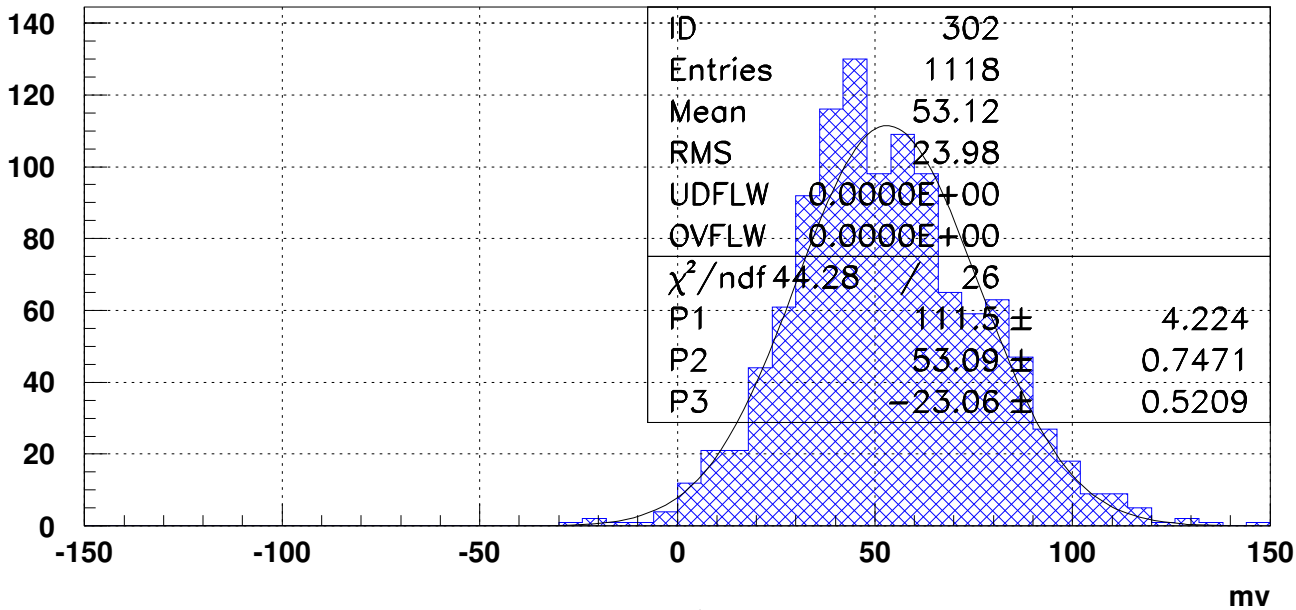

Chip CMP16F Cdet=0 pF (FNAL Stand) May-Aug 2000
Offset, mv
100
0
-100
0
10
ch
AFEB 43
AFEB 44
AFEB 45
AFEB 46
AFEB 47
AFEB 48
AFEB 49
AFEB 50
AFEB 51
AFEB 52
AFEB 53
AFEB 54
AFEB 55
AFEB 56
AFEB 57
AFEB 60


Chip CMP16F Cdet=0 pF (FNAL Stand) May-Aug 2000
Offset, mv
100
0
-100
0
10
ch
AFEB 61
AFEB 62
AFEB 63
AFEB 64
AFEB 65
AFEB 66
AFEB 67
AFEB 68
AFEB 69
AFEB 70
AFEB 71
AFEB 72
AFEB 73
AFEB 74
AFEB 75
AFEB 76

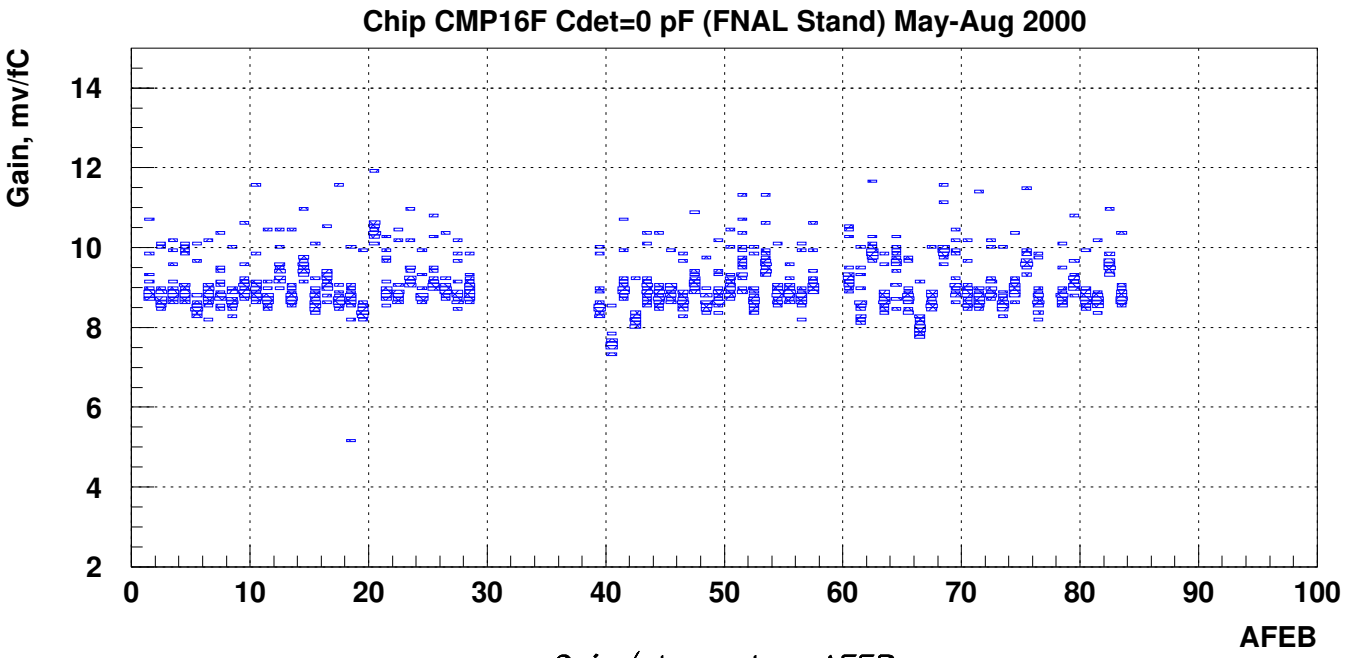

Chip CMP16F Cdet=0 pF (FNAL Stand) May-Aug 2000
Offset, mv
100
0
-100
0
10
ch
AFEB 78
AFEB 79
AFEB 80
AFEB 81
AFEB 82
AFEB 83



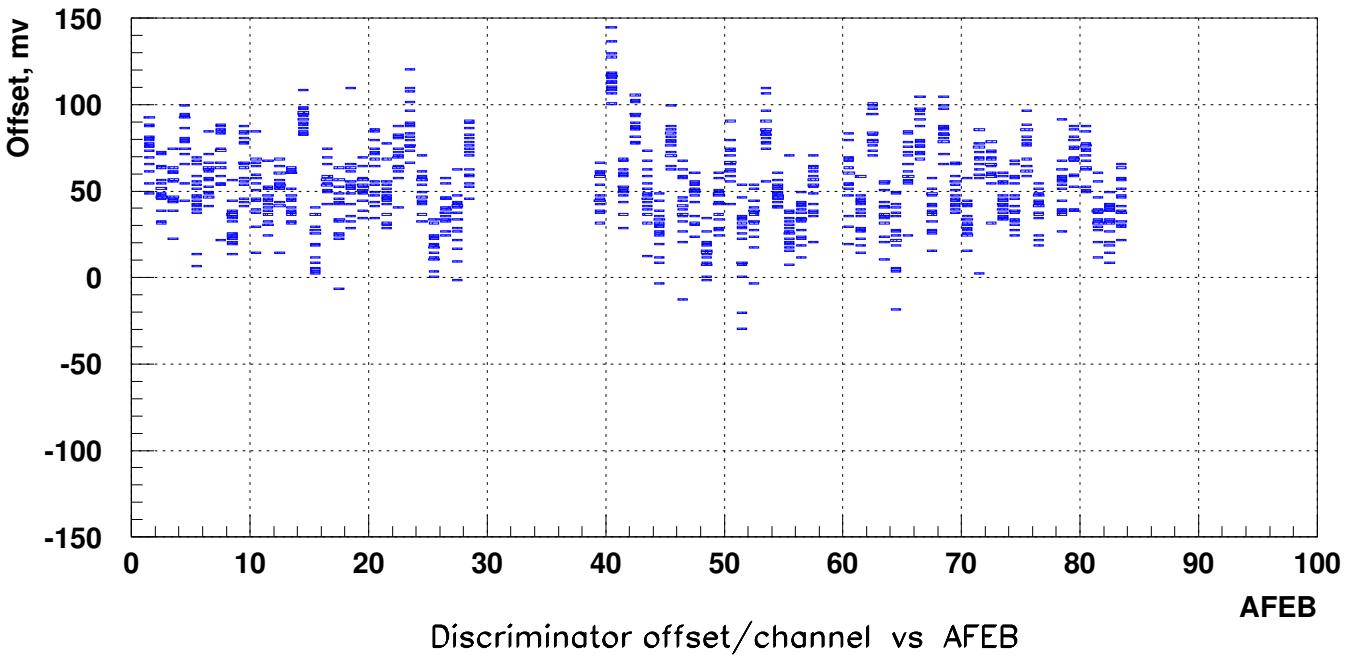
Gain/channel, all AFEBs



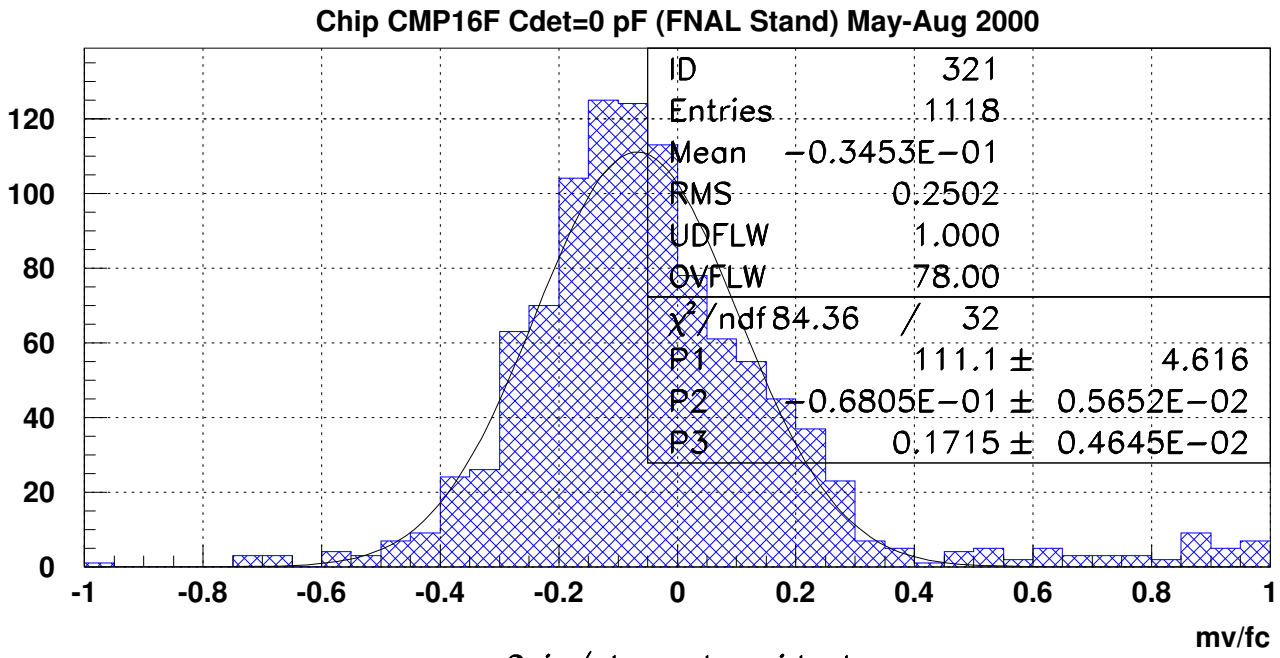
Discriminator offset/channel, all AFEBs



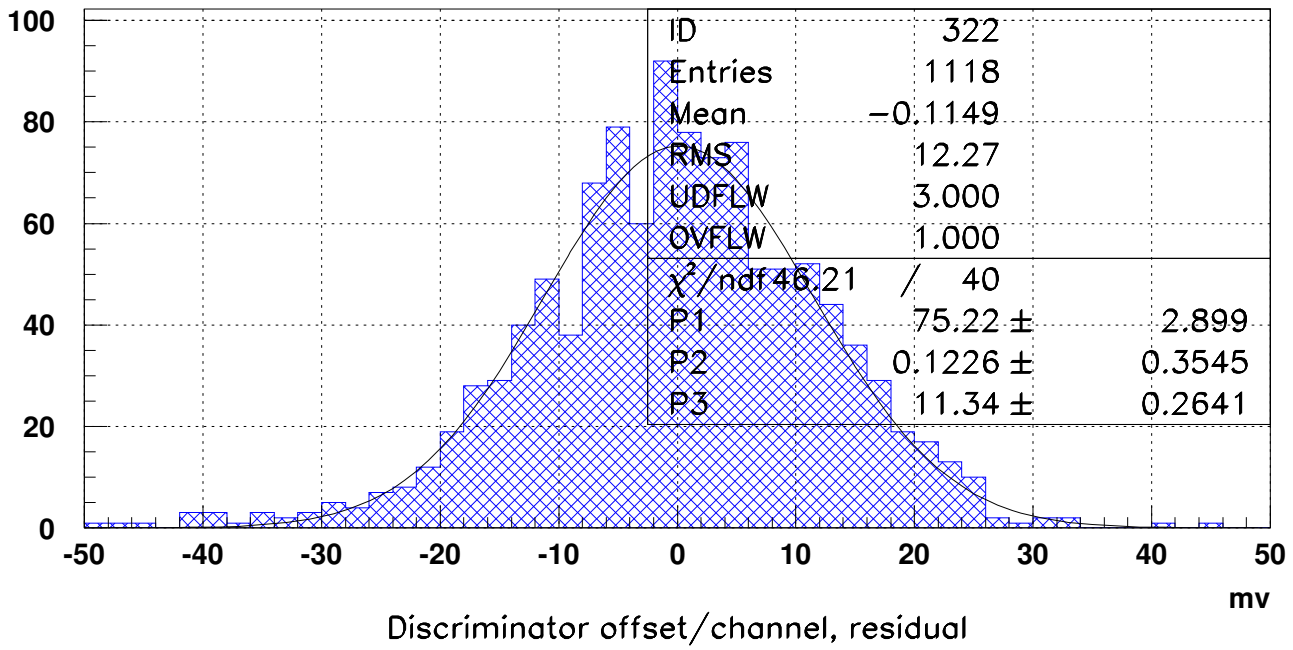
Gain/channel vs AFEB



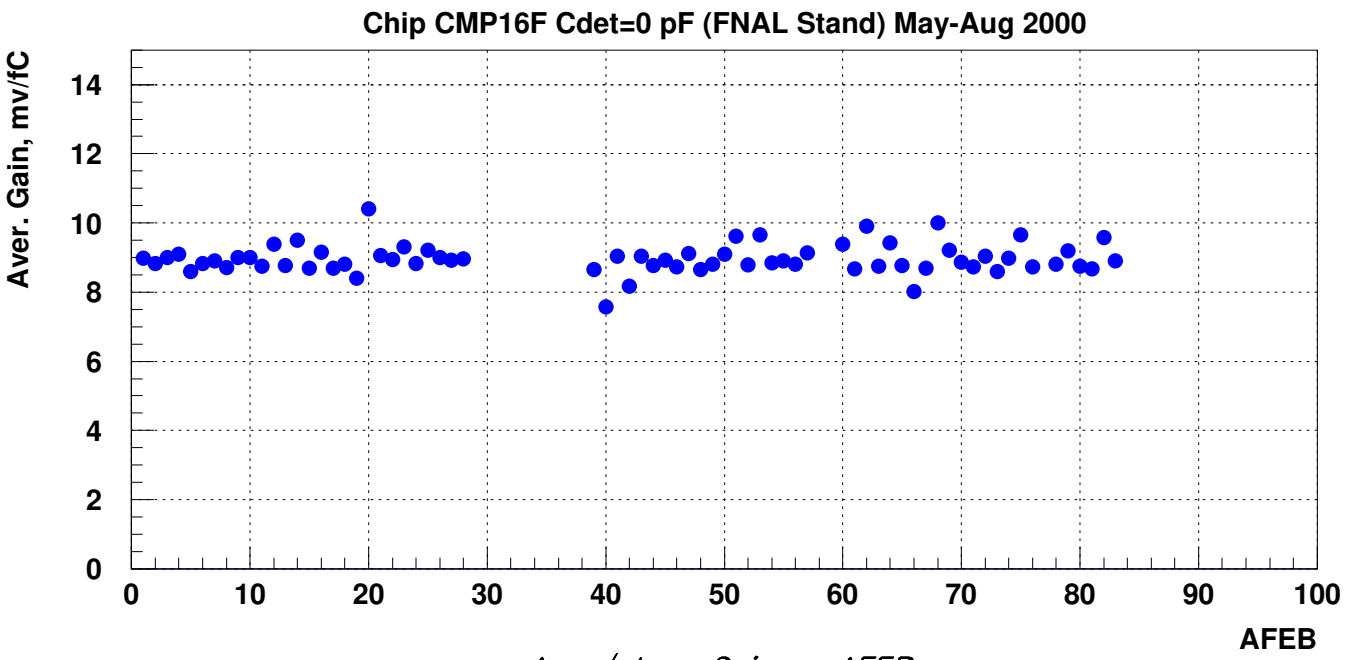
Discriminator offset/channel vs AFEB



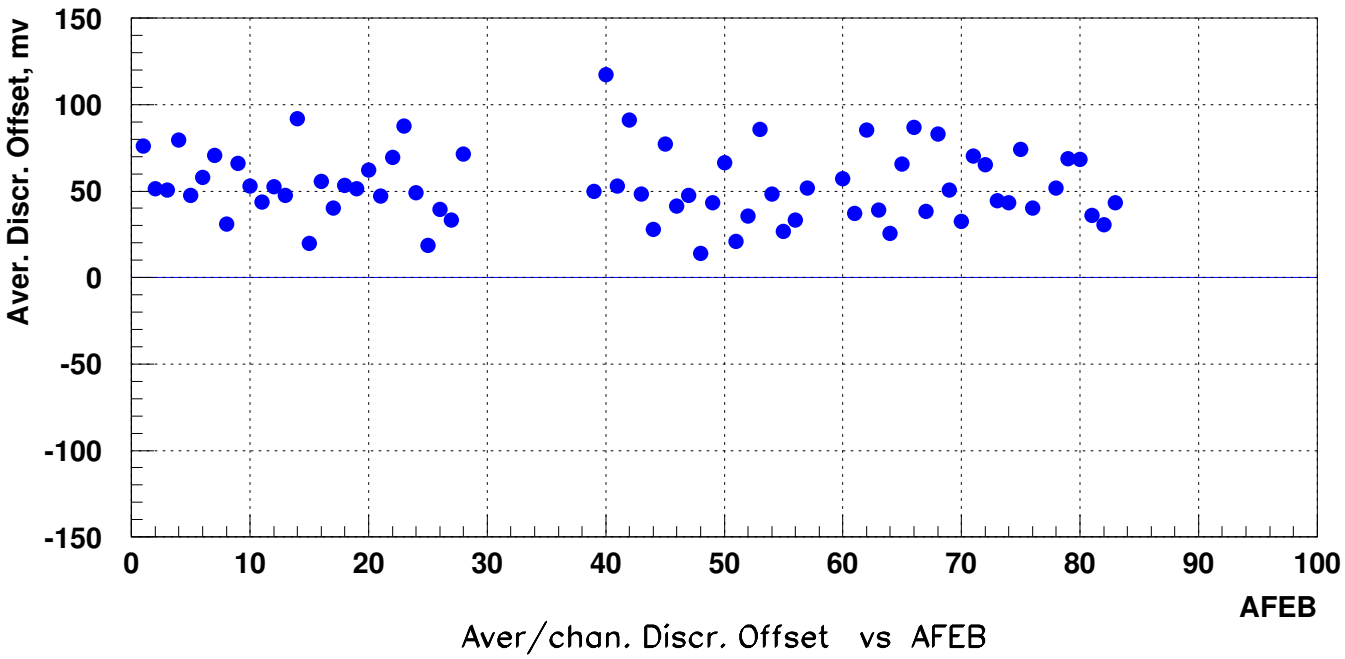
Gain/channel, residual



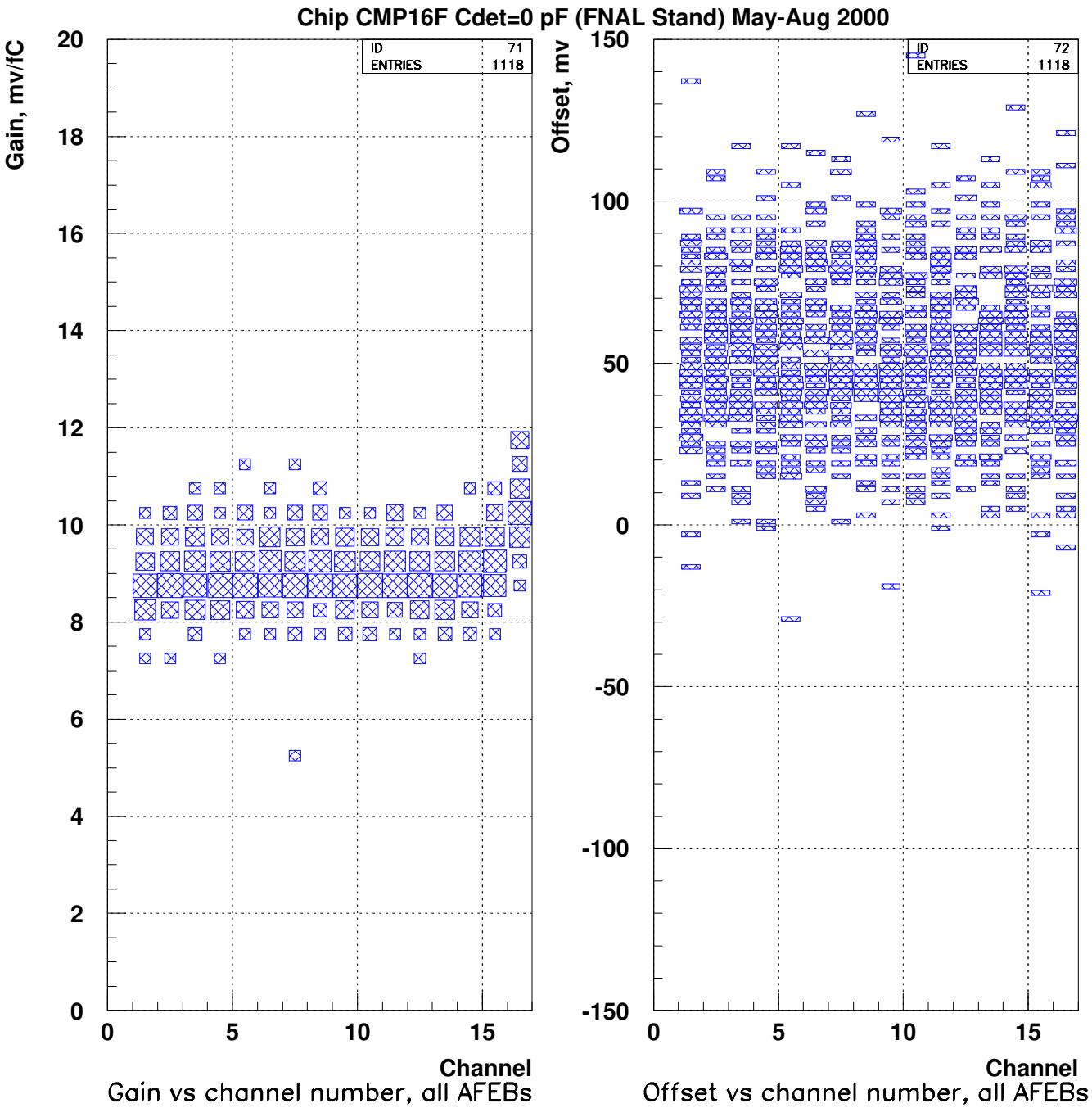
Discriminator offset/channel, residual

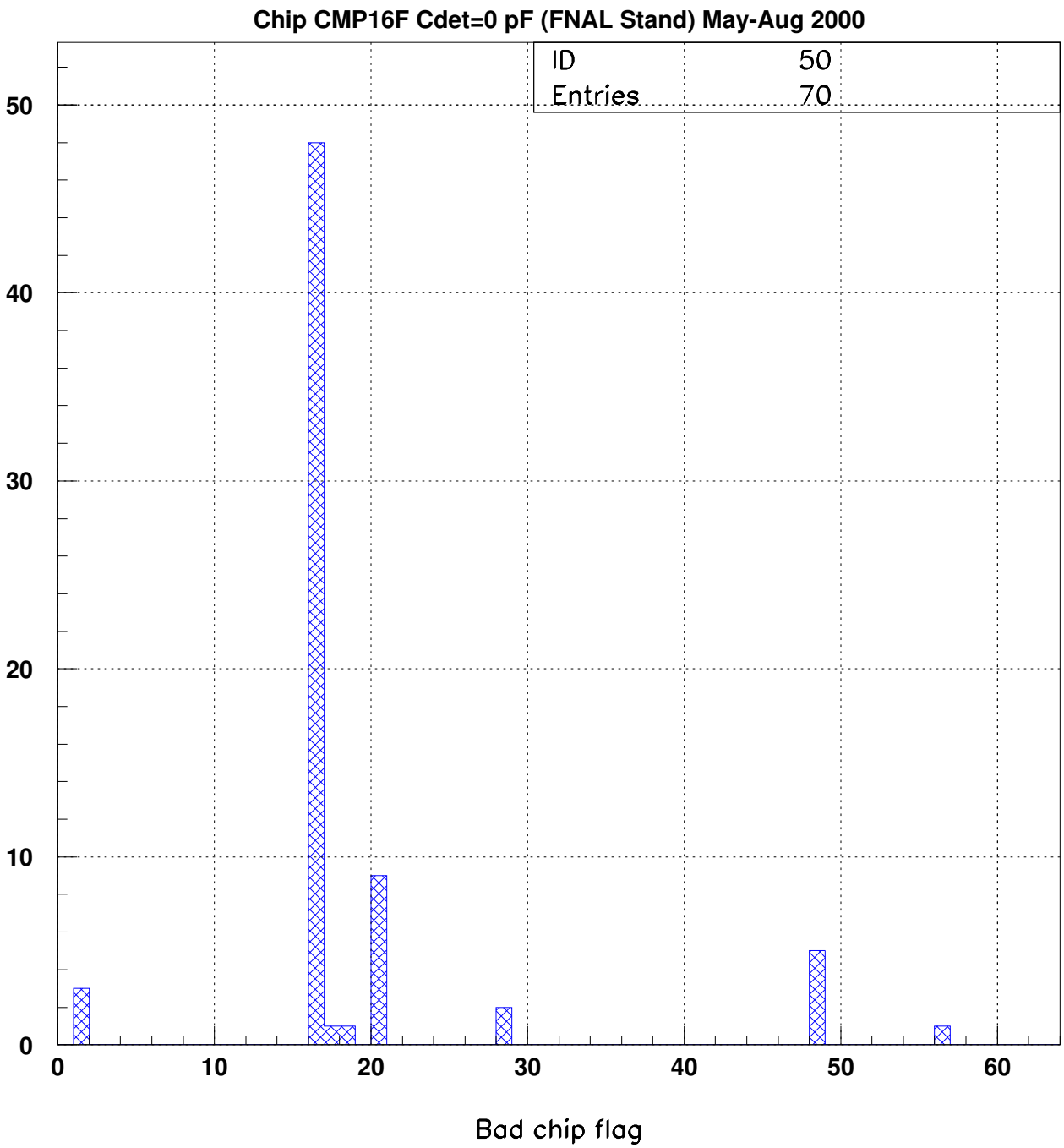


Aver/chan. Gain vs AFEB

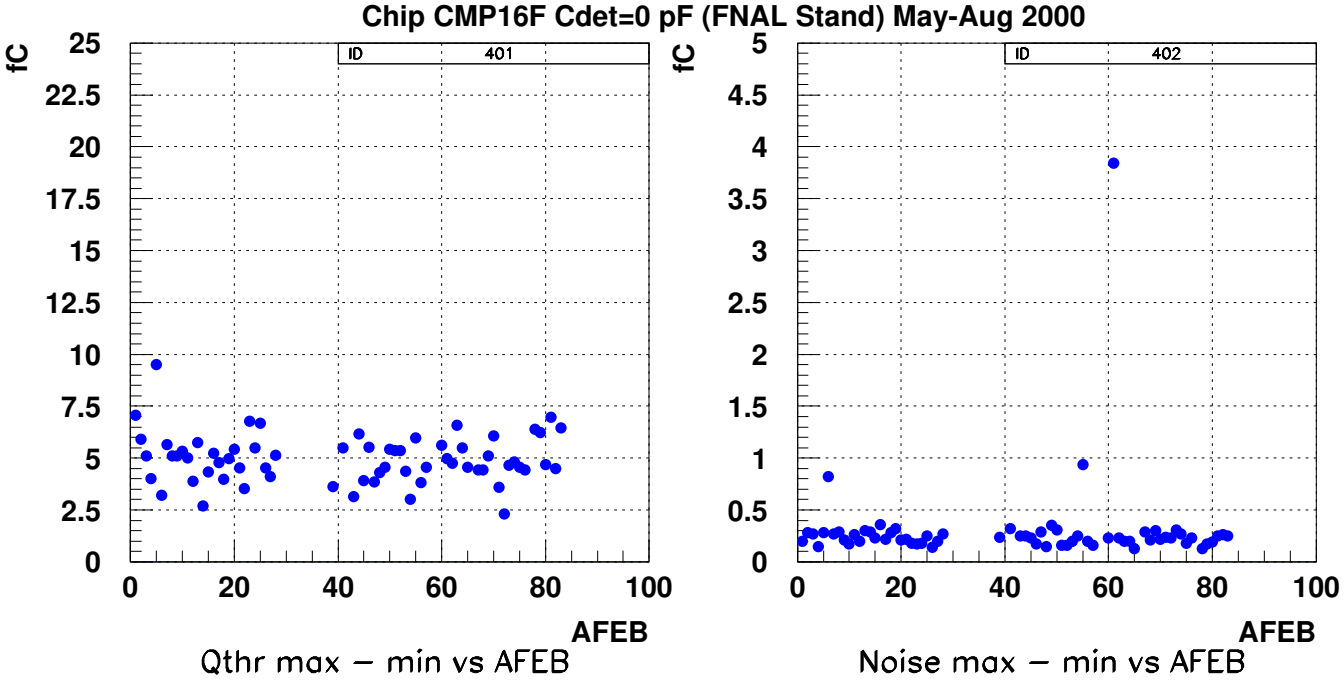


Aver/chan. Discr. Offset vs AFEB


Chip CMP16F Cdet=0 pF (FNAL Stand) May-Aug 2000
Gain, mv/fC
ID 71
ENTRIES 1118
Channel
Gain vs channel number, all AFEBs
Offset, mv
ID 72
ENTRIES 1118
Channel
Offset vs channel number, all AFEBs

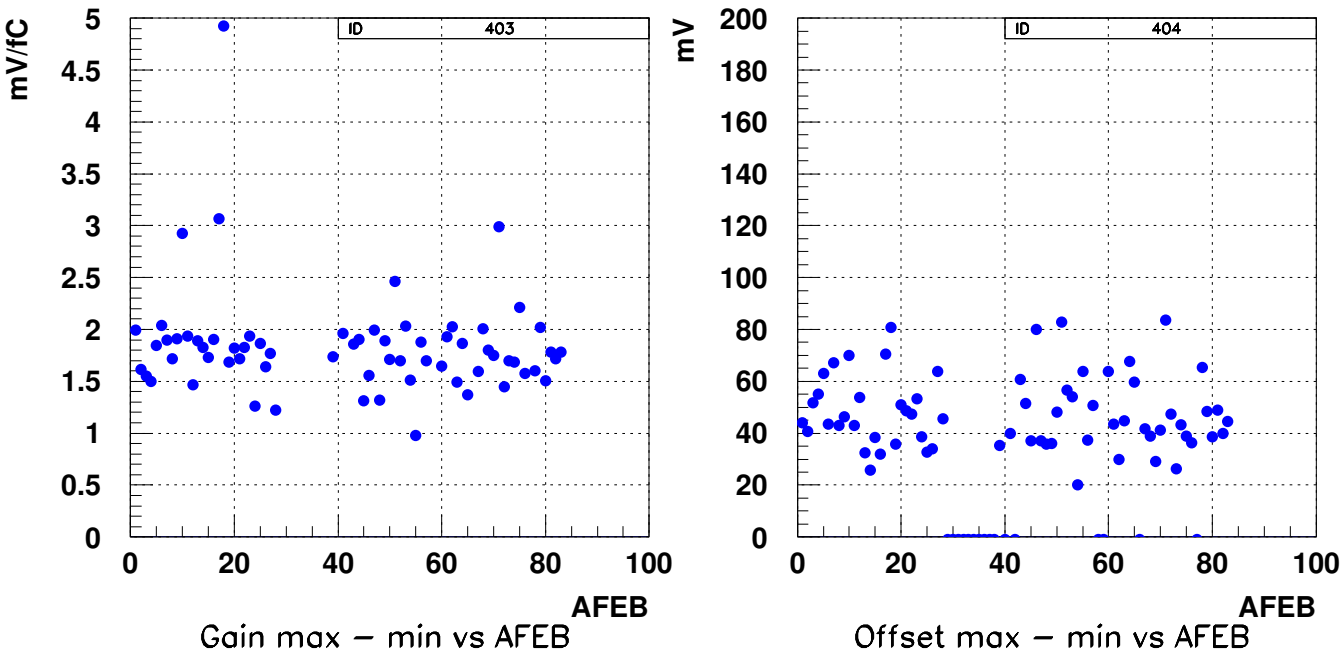

Chip CMP16F Cdet=0 pF (FNAL Stand) May-Aug 2000
ID 50
Entries 70
50
40
30
20
10
0
0
10
20
30
40
50
60
Bad chip flag

Chip CMP16F Cdet=0 pF (FNAL Stand) May-Aug 2000



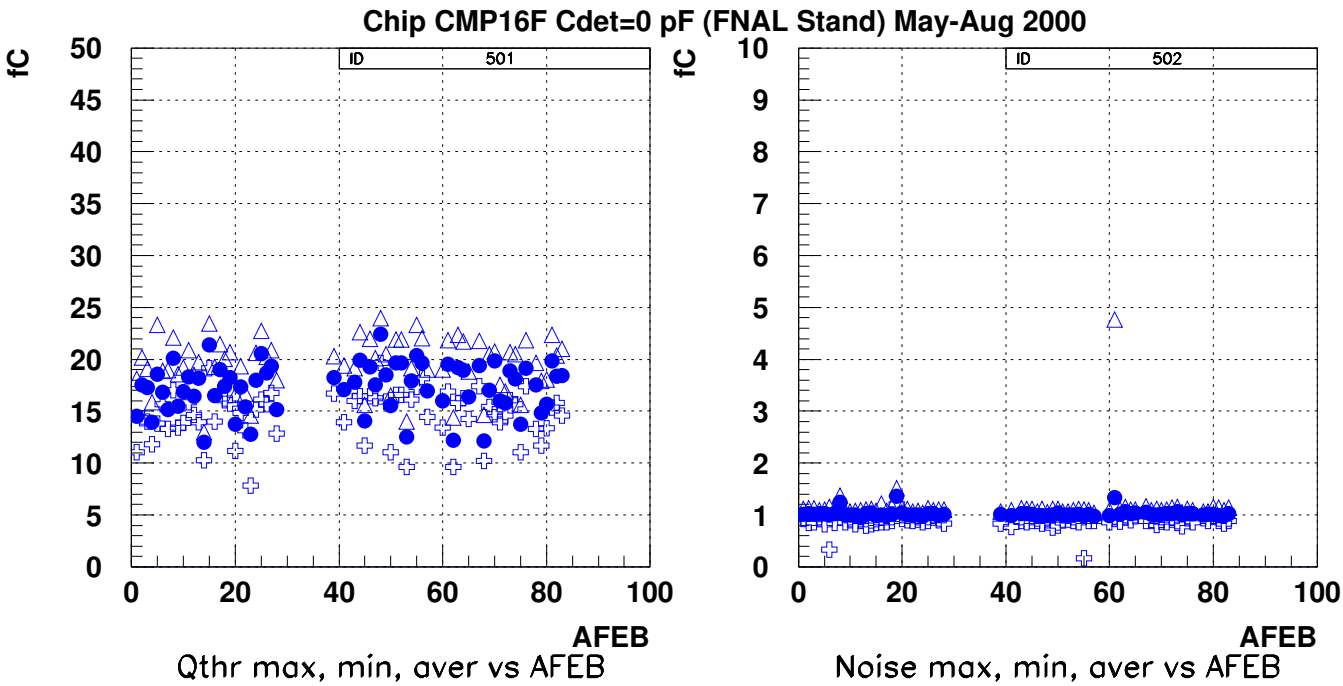
Qthr max − min vs AFEB

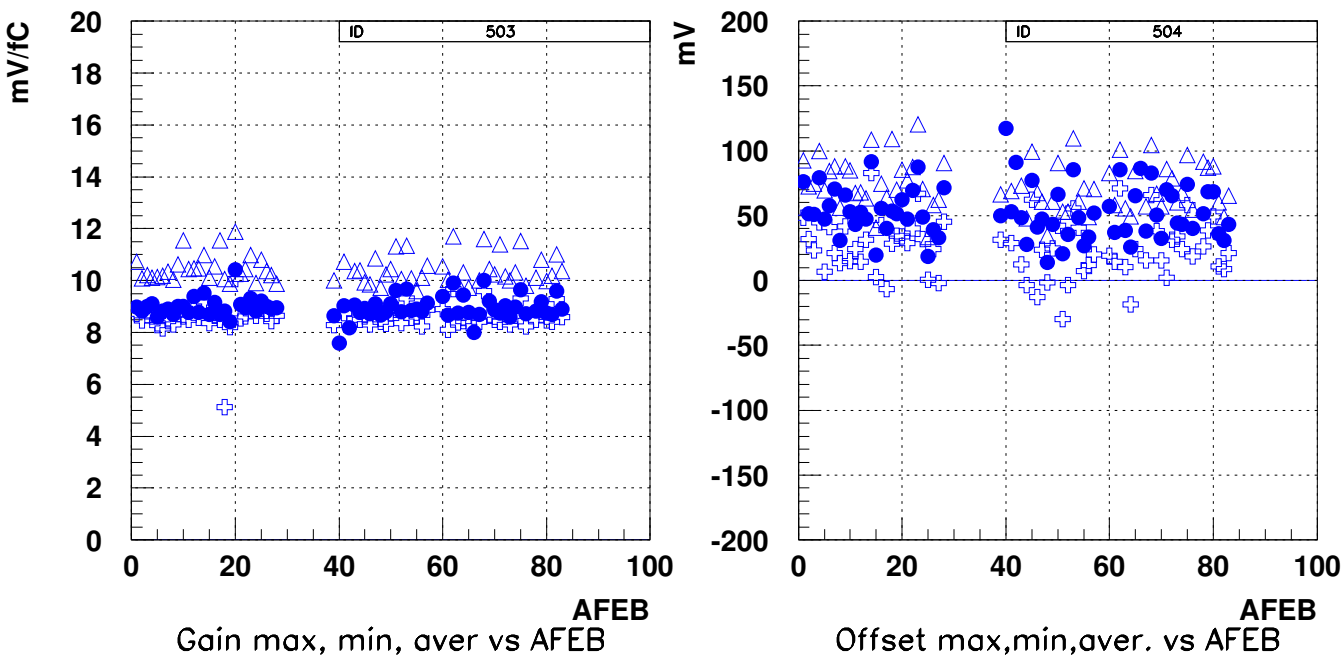
Noise max − min vs AFEB



Gain max − min vs AFEB

Offset max − min vs AFEB


Chip CMP16F Cdet=0 pF (FNAL Stand) May-Aug 2000
ID 501
fC
AFEB
Qthr max, min, aver vs AFEB
ID 502
fC
AFEB
Noise max, min, aver vs AFEB


ID 503
mV/fC
AFEB
Gain max, min, aver vs AFEB
ID 504
mV
AFEB
Offset max,min,aver. vs AFEB