New version of the 96-channels anode front-end board ADB96/CMP16 (1999).

The new Anode 96-channels front-end card ADB96/CMP16 was developed for the new improved 16-channels Amplifier-Discriminator chip CMP16. The characteristics of the board are listed below:

Board construction.

- Wire layers number — 8;
- Dimensions — 10.5 x 4.0 x 0.9 inch^3;

Inputs.

- Connectors — 68-pins headers Condo;
- Spare potential — 0.7V;
- Resistance — 20..40 Ohm;
- Static electricity protection;

Outputs.

- Connectors — 34-pins low profile headers;
- Signals — LVDS standard;
- Pulse width adjustment range — 60…120 ns;
- Pulse delay adjustment range — 0…16 ns;

Driving, monitoring and testing.

- Interface of the control system — JTAG of LVDS levels;
- Through JTAG there is a possibility
  to write down:
  - 6 thresholds;
  - Test pulse amplitude;
  - Delay setup for each of CMP16 chips;
  - Test pulse applying configuration (test pulse can be put on any CMP16 chip independently);

  to read out:
  - 6 thresholds;
  - Test pulse amplitude;
  - Temperature;
  - Board consuming current;
  - 11-bits board ID number.
- ADB96/CMP16 boards can be joined in JTAG-LVDS daisy chain;

Power supply.

- Supply voltage — 5.5V;
- Supply current — 0.5A.